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# Mercury Company Profile

## Taiwan Plant

Mercury Electronic Industrial Co., Ltd

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Tel : 886-2-2406-2779 Fax : 886-2-2496-0769

E-mail : sales-tw@mercury-crystal.com

Web site : www.mercury-crystal.com

- Established in 1973 .
- Proud to be a pioneer of the crystal industry in Taiwan .
- Corporate headquarters and volume production facility .
- ISO 9001 and 14001 certified .



## U.S.A. Plant

Mercury United Electronics , Inc.

9299 9th Street , Rancho Cucamonga,  
California 91730 U.S.A.

Tel : 1-(909)-466-0427 Fax : 1-(909)-466-0762

E-mail : sales-us@mercury-crystal.com

- Established in 1990 .
- Small volume production facility .
- R & D center .
- Quick turn ( from same day to 4 weeks ) service of  
clock oscillators , VCXOs , TCXOs and OCXOs .



## China Plant

Mercury Crystal Technology ( Kun Shan ) Inc.

瑪居禮石英晶體科技(昆山)有限公司

中國江蘇省昆山市經濟技術開發區三巷路99號

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Development Zone , Kun Shan City , Jiang Su , China

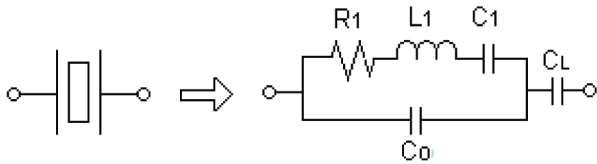
Tel : 86-(512)-5763-8100 Fax : 86-(512)-5763-8103

E-mail : sales-cn@mecxtal.com

- Established in 2002.
- Volume production facility.
- ISO 9001 certified .



# How To Specify A Quartz Crystal



$C_0$ : Shunt Capacitance
$C_1$ : Motional Capacitance
$L_1$ : Motional Inductance
$R_1$ : Equivalent Series Resistance
$C_L$ : Load Capacitance



## Holder Type

### SMD Type

### Thru - Hole Type

<input type="checkbox"/> X21 ( 2.0 * 1.6 * 0.6mm )	<input type="checkbox"/> MF ( 6.0 * 3.5 * 1.0mm )	<input type="checkbox"/> H49 ( 10.7 * 4.5 * 13.6mm )	<input type="checkbox"/> HUS ( 10.7 * 4.3 * 3.5mm )
<input type="checkbox"/> X22 ( 2.5 * 2.0 * 0.6mm )	<input type="checkbox"/> MQ ( 7.0 * 5.0 * 1.2mm )	<input type="checkbox"/> 49T ( 10.7 * 4.5 * 11.2mm )	<input type="checkbox"/> HUSL ( 10.7 * 4.3 * 2.5mm )
<input type="checkbox"/> X32 ( 3.2 * 2.5 * 0.7mm )	<input type="checkbox"/> M49 ( 12.4 * 4.5 * 4.0mm )	<input type="checkbox"/> T39 ( 3.0Ø * 9.0mm )	<input type="checkbox"/> U5 ( 7.8 * 3.2 * 6.0mm )
<input type="checkbox"/> X42 ( 4.0 * 2.5 * 0.6mm )	<input type="checkbox"/> ML49 ( 12.4 * 4.5 * 3.0mm )	<input type="checkbox"/> T38 ( 3.0Ø * 8.0mm )	<input type="checkbox"/> U1 ( 7.8 * 3.2 * 8.0mm )
<input type="checkbox"/> MJ ( 5.0 * 3.2 * 0.9mm )	<input type="checkbox"/> MP5 ( 12.9 * 4.5 * 4.8mm )	<input type="checkbox"/> T26 ( 2.0Ø * 6.0mm )	

### A Basic Spec. must be specified

A-1 : Holder type: \_\_\_\_\_  Thru-Hole type  SMD Gull Wing type  SMD Leadless

A-2 : Frequency : \_\_\_\_\_ MHz or \_\_\_\_\_ KHz

A-3 : Circuit Condition :  Series  Parallel

If **Series** resonance : Assign " S " as circuit load .

If **Parallel** : Specify CL ( Load Capacitance ) \_\_\_\_\_ pF ( Typical range is 8 to 32 pF ) .

A-4 : Calibration Tolerance: ± \_\_\_\_\_ ppm at 25°C

A-5 : Frequency Stability: ± \_\_\_\_\_ ppm over \_\_\_\_\_ °C to \_\_\_\_\_ °C

A-6 : Maximum Equivalent Series Resistance (ESR): \_\_\_\_\_ ohms ( max. )

### B Other Spec. If not specified MEC standards will be applied.

B-1 : Shunt Capacitance (Co) : \_\_\_\_\_ pF (max.)

B-2 : Motional Capacitance (C1) : \_\_\_\_\_ fF ± \_\_\_\_\_ %

B-3 : Motional Inductance (L1) : \_\_\_\_\_ mH ± \_\_\_\_\_ %

B-4 : Capacitance Ratio (Co/C1) : \_\_\_\_\_ ± \_\_\_\_\_ %

B-5 : Trim Sensitivity : \_\_\_\_\_ ppm / pF

B-6 : Frequency Pull Ability : When CL= \_\_\_\_\_ pF, \_\_\_\_\_ ppm (max.) ; When CL= \_\_\_\_\_ pF, \_\_\_\_\_ ppm (min.)

B-7 : Drive Level : \_\_\_\_\_ micro Watts (uW) (max.)

B-8 : Crystal cutting angle type :  AT - cut  BT - cut  SL - cut

B-9 : Mode of Oscillation :  Fundamental mode  3rd overtone  5th overtone

B-10 : Spurious: \_\_\_\_\_ dB (or ohms) (min.) in ± \_\_\_\_\_ kHz range of the main mode

B-11 : Crystal Q: \_\_\_\_\_ ( min.)

B-12 : Aging: \_\_\_\_\_ ppm per year ( max.)

B-13 : Shock: \_\_\_\_\_ ; Vibration: \_\_\_\_\_

## Part Number Format and Example

Example: X32 - 16.000 - 10P - 15 / 20 / -30+75 / 100R

= User to specify

X32	-	16.000	-	10P	-	15	/	20	/	-30+75	/	100R	
(1)		(2)		(3)		(4)		(5)		(6)		(7)	

(1) Package code ; (2) Frequency in MHz ; (3) Load Capacitance in pF or "S" for series resonance ; (4) Frequency tolerance at 25°C ; (5) Frequency stability ;

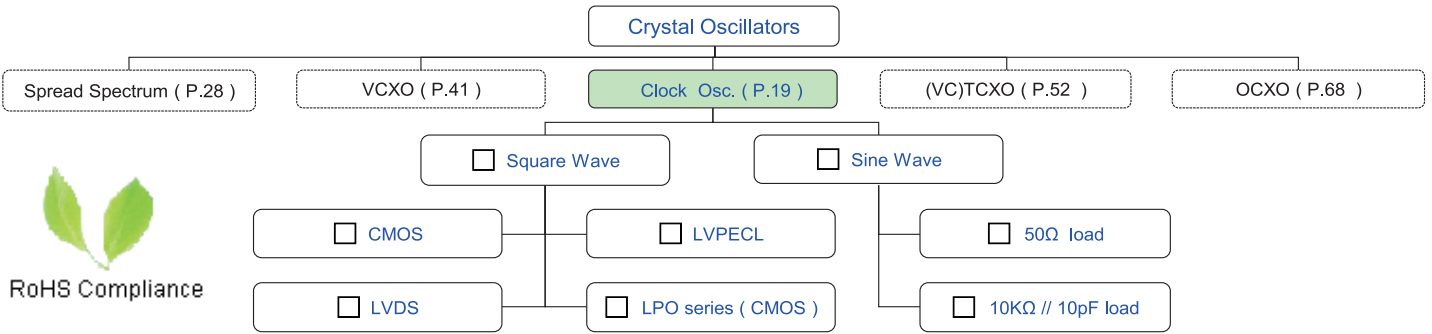
(6) Operating temperature range -30°C to +75°C in this example. Use " X " for -10°C ~ 60°C ; Use " Y " for -20°C ~ 70°C ; Use " I " for -40°C ~ 85°C ; (7) ESR max

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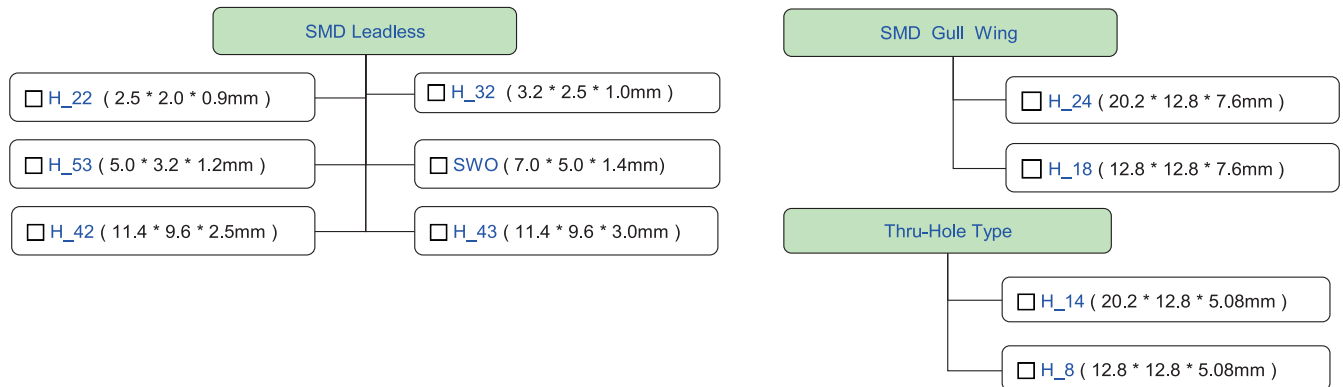
# How To Specify A Crystal Oscillator

## Output Wave Forms and Logics



Part Number Format : " H " --- for Oscillators

" \_ " --- represents PRODUCT SERIES selections in table 1 and 2 below .



## Basic Spec. must be specified

1 : Holder type with output wave : \_\_\_\_\_  Thru-Hole type  SMD type  Gull Wing type

Output Wave type :

Table 1 --- Square Wave	
output wave	product series
<input type="checkbox"/> CMOS	H , HB , HK , HF , HV , HW
<input type="checkbox"/> LVPECL	HPF , HPW
<input type="checkbox"/> LVDS	HDF , HDW

Table 2 --- Sine Wave	
output load	product series
<input type="checkbox"/> 50Ω load	HS
<input type="checkbox"/> 10KΩ // 10pF load	HSR

2 : Frequency : \_\_\_\_\_ MHz or \_\_\_\_\_ KHz

3 : Input Voltage :  +5.0V  +3.3V  +3.0V  +2.5V  +1.8V  others : \_\_\_\_\_

4 : Frequency Stability :

Stability / Temp .	Commercial -10°C ~ +70 °C	Industrial -40°C ~ +85 °C
± 25ppm	<input type="checkbox"/> A	<input type="checkbox"/> D
± 50ppm	<input type="checkbox"/> B	<input type="checkbox"/> E
± 100ppm	<input type="checkbox"/> C	<input type="checkbox"/> F
<input type="checkbox"/> Custom ( ± _____ ppm over _____ to _____ °C )		

5 : Output Logic " 1 " \_\_\_\_\_ V (min.) ; Output Logic " 0 " \_\_\_\_\_ V (max.)

6 : Rise time ( Tr ) and Fall time ( Tf ) : \_\_\_\_\_ nano seconds (max.) .

7 : Start-up time : \_\_\_\_\_ mini seconds (max.) .

8 : Current Consumption : \_\_\_\_\_ mA (max.) or \_\_\_\_\_ uA (max.) .

9 : Symmetry (Duty Cycle) :  Standard ( 50%±10% )  Option ( 50%±5% ) [ Add " S " at the end of part number ] .

10 : Pin 1 options :  No Connection  Tri - state ( Output Enable )  Power-down ( Note : Tri-state is standard for SWO , H53 and H32 series ) .

## Part Number Format and Example

Example: 3H42 - DT - 33.000-S

= User to specify = Option

			-			-		-	
3	H_42	G	-	D	T	-	33.000	-	S
(1)	(2)	(3)		(4)	(5)		(6)		(7)

(1) Supply voltage code : " 3 " for "+3.3V" ; " 5 " for "+5.0V" ; (2) Package code ; (3) RoHS compliance ( omit " G " if not required ) ; (4) Frequency Stability

(5) Tri-state option. Omit "T" if not required ; (6) Center Frequency in MHz ; (7) Add " S " for 50% ± 5% duty cycle . Omit " S " if not required .

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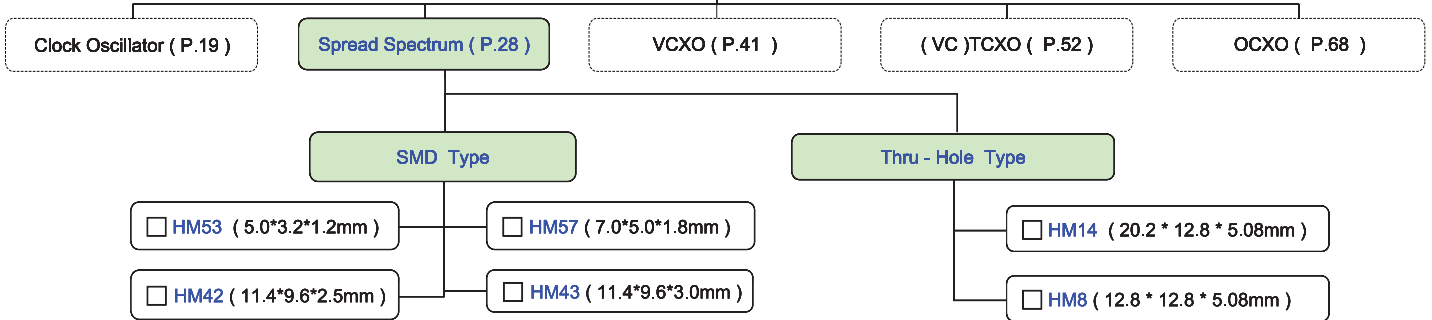
# How To Specify A EMI Reduction Spread Spectrum Clock Oscillator

Holder Type



## Crystal Oscillator

RoHS Compliance



### Basic Spec. must be specified

1 : Holder type: \_\_\_\_\_  Thru-Hole type  SMD type

2 : Frequency : \_\_\_\_\_ MHz

3 : Input Voltage :  +3.3V

4 : Output Wave : Square Wave ( CMOS )

5 : Frequency Stability :

( Exclude frequency modulation )

Stability / Temp .	Commercial -10°C ~ +70 °C	Industrial -40°C ~ +85 °C
± 25ppm	<input type="checkbox"/> A	<input type="checkbox"/> D
± 50ppm	<input type="checkbox"/> B	<input type="checkbox"/> E
± 100ppm	<input type="checkbox"/> C	<input type="checkbox"/> F
<input type="checkbox"/> Custom ( ± _____ ppm over _____ to _____ °C )		

6 : Group & Spread type :

(1)  Group R ( 250 ps cycle-to-cycle jitter ) :

Center spread :  C0.5: ±0.5 %  C1.5: ±1.5 %

Down spread :  D1.0 : -1.0 %  D3.0 : -3.0 %

(2)  Group Y ( 100 ps cycle-to-cycle jitter ) :

Center spread :  C0.5: ±0.5 %  C1.0: ±1.0 %  C1.5: ±1.5 %

Down spread :  D1.0 : -1.0 %  D2.0 : -2.0 %  D3.0 : -3.0 %

(3)  Group P ( 100 ps cycle-to-cycle jitter ) :

Center spread :  ±0.25 %  ±0.375 %  ±0.625 %  ±1.0 %  ±1.25 %  ±1.5 %  ±1.75 %  ±1.875 %

Down spread :  -0.5 %  -0.75 %  -1.25 %  -2.0 %  -2.5 %  -3.0 %  -3.5 %  -3.75 %

### Part Number Format and Example

Examples of Low EMI Oscillator : 3HM57-DT-33.000R-C0.5									
✍ = User to specify      ★ = Option									
	✍		✍	★		✍	✍		✍
3	HM57	-	D	T	-	33.000	R	-	C0.5
(1)	(2)		(4)	(5)		(6)	(7)		(8)
(1) Supply voltage code : " 3 " for "+3.3V ; (2) Package code ; (3) RoHS compliance. Omit " G " is not required. ; (4) Frequency stability ;									
(5) Tri-state option ( Tri - state is standard for group " R " and " Y " ; Tri - state is not available for group " P " ) ; (6) Frequency in MHz ; (7) Group ;									
(8) Spread type & percentage ; " C " for center spread , " D " for down spread. C0.5 represents center spread ± 0.5% ( total 1% ).									

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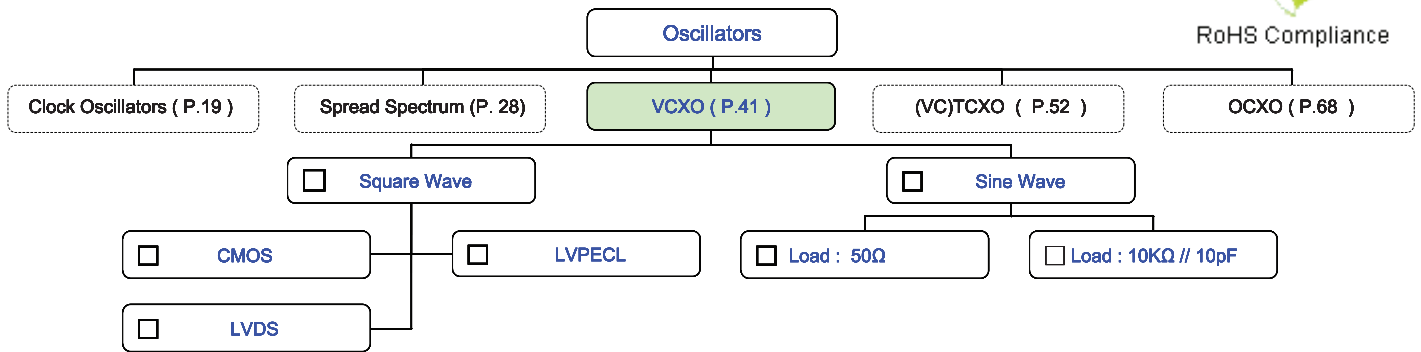
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# How To Specify A VCXO

Holder Type

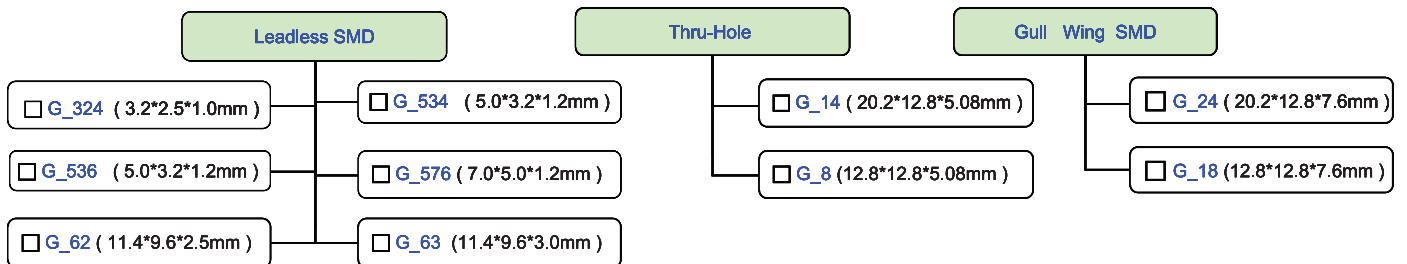


RoHS Compliance



Part Number Format : " G " --- for VCXO

" \_ " --- represents PRODUCT SERIES selections in table 1 and 2 below .



Basic Spec. must be specified

1 : Holder type : \_\_\_\_\_  Thru-Hole type  Leadless SMD type  Gull Wing type

Output Wave type :

Table 1 --- Square Wave	
output wave	product series
<input type="checkbox"/> CMOS	G , GF , GV , GW
<input type="checkbox"/> LVPECL	GPF , GPW
<input type="checkbox"/> LVDS	GDF , GDW

Table 2 --- Sine Wave	
output load	product series
<input type="checkbox"/> 50Ω load	GS
<input type="checkbox"/> 10KΩ // 10pF load	GSR

2 : Frequency : \_\_\_\_\_ MHz

3 : Input Voltage :  +5.0V  +3.3V  +1.8V

4 : Control voltage center and range :  +2.5V ± 2V ( 0.5V ~ 4.5V )  +1.65V ± 1.35V ( 0.3V ~ 3.0V )  +0.9V ± 0.72V ( 0.183V ~ 1.62V )

5 : Frequency Stability :

Stability / Temp .	Commercial: -10°C ~ +70 °C	Industrial: -40°C ~ +85 °C
± 25ppm	<input type="checkbox"/> A	<input type="checkbox"/> D
± 50ppm	<input type="checkbox"/> B	<input type="checkbox"/> E
± 100ppm	<input type="checkbox"/> C	<input type="checkbox"/> F
<input type="checkbox"/> Custom ( ± _____ ppm over _____ to _____ °C )		

6 : Frequency deviation range : ± \_\_\_\_\_ ppm  min.  max.  typical ( ± 20% ) .

7 : Linearity : \_\_\_\_\_ % (max.)

8 : Transfer function :  Positive

9 : Input Impedance : \_\_\_\_\_ KΩ (min.)

10 : Modulation band width : ± \_\_\_\_\_ KHz (min.)

Part Number Format and Example

Examples of VCXO : 3G62G - DT - 150M - 27.000									
☞ = User to specify      ★ = Option									
☞	☞	★		☞	★		☞		☞
3	G_62	G	-	D	T	-	150M	-	27.000
(1)	(2)	(3)		(4)	(5)		(6)		(7)
(1) Supply voltage code : " 18 " for "+1.8V", " 3 " for "+3.3V", " 5 " for "+5.0V" ; (2) Package code ; (3) RoHS compliance . Omit " G " if not required ;									
(4) Frequency stability ; (5) Tri-state ; (6) Pulling range in ppm ( " N " : minimum , " M " : maximum , " T " : typical ) ; (7) Center Frequency in MHz									

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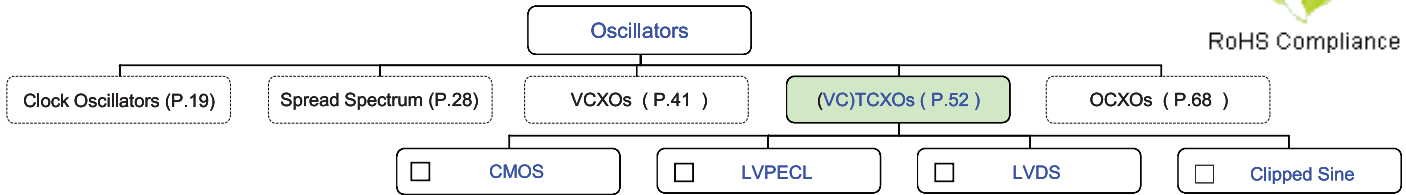
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# How To Specify A ( VC )TCXO



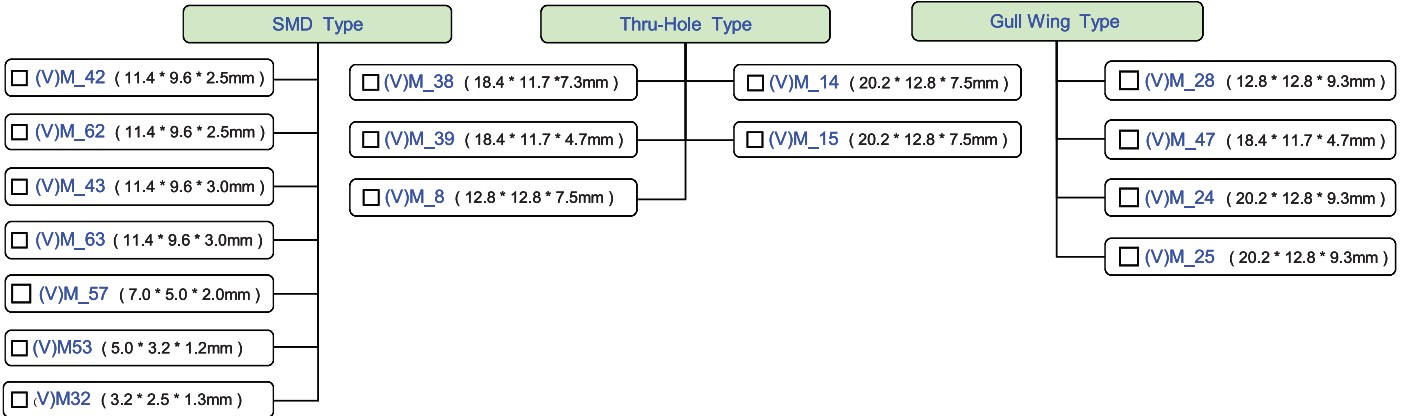
RoHS Compliance

## Holder Type



Part Number Format : " M " for TCXO ; " VM " for VCTCXO (TCXO with voltage control function)

" \_ " --- represents PRODUCT SERIES selections in table 1 and 2 below .



## Basic Spec. must be specified

- 1 :  TCXO ( code " M " series )     VCTCXO ( code " VM " series )
- 2 : Holder type : \_\_\_\_\_  Thru-Hole type     SMD type     Gull Wing type
- Output Wave type : " \_ " --- represents Package Code . See page 67 and 68 .

output wave	Square Wave							
	product series							
	TCXO --- " M "				VCTCXO --- " VM "			
<input type="checkbox"/> CMOS	M_T	MF_T	MV_T	MW_T	VM_T	VMF_T	VMV_T	VMW_T
<input type="checkbox"/> PECL	MF_P		MW_P		VMF_P		VMW_P	
<input type="checkbox"/> LVDS	MF_D		MW_D		VMF_D		VMW_D	

- 3 : Frequency : \_\_\_\_\_ MHz or \_\_\_\_\_ KHz
- 4 : Input Voltage :  +5.0V     +3.3V     +3.0V     +1.8V     others : \_\_\_\_\_
- 5 : Frequency Stability :
- |                                   |                                   |                                   |
|-----------------------------------|-----------------------------------|-----------------------------------|
| <input type="checkbox"/> ± 1.0ppm | <input type="checkbox"/> ± 1.5ppm | <input type="checkbox"/> ± 2.0ppm |
| <input type="checkbox"/> ± 2.5ppm | <input type="checkbox"/> ± 3.0ppm | <input type="checkbox"/> ± 5.0ppm |
- 6 : Operating Temp. :
- |  |  |  |  |
|--|--|--|--|
| <input type="checkbox"/> 0 °C ~ +60 °C   | <input type="checkbox"/> 0 °C ~ +70 °C   | <input type="checkbox"/> -10 °C ~ +60 °C | <input type="checkbox"/> -20 °C ~ +70 °C |
| <input type="checkbox"/> -30 °C ~ +60 °C | <input type="checkbox"/> -30 °C ~ +70 °C | <input type="checkbox"/> -40 °C ~ +85 °C |  |
- 7 : ● [ " T " series , Square Wave ] : Output Voltage " 1 " : \_\_\_\_\_ V min. ; Output Voltage " 0 " : \_\_\_\_\_ V ( max. )  
 ● [ " S " series , Clipped Sine Wave ] : Output Voltage Level : \_\_\_\_\_ Vp-p ( min. )
- 8 : Start-up time : \_\_\_\_\_ mini seconds ( max. ) .
- 9 : Current Consumption : \_\_\_\_\_ mA ( max. )
- 10 : ● Mechanical frequency tuning ( MFT ) range : ± \_\_\_\_\_ ppm ( min. ) . For TCXOs and VCTCXOs . ( No MFT for (V)M32 , (V)M53 , (V)M57 packages . )  
 ● Electrical frequency tuning range ( EFT ) : ± \_\_\_\_\_ ppm ( min. ) . For VCTCXOs only .  
 ● Control voltage center and range : **1.5V ± 1.0V** is standard for 5.0V , 3.3V , 1.5V supply voltages .

## Part Number Format and Example

Examples of TCXO : **VM38GS5-12.800-2.5/-30+75**      ✎ = User to specify ; ★ = Option

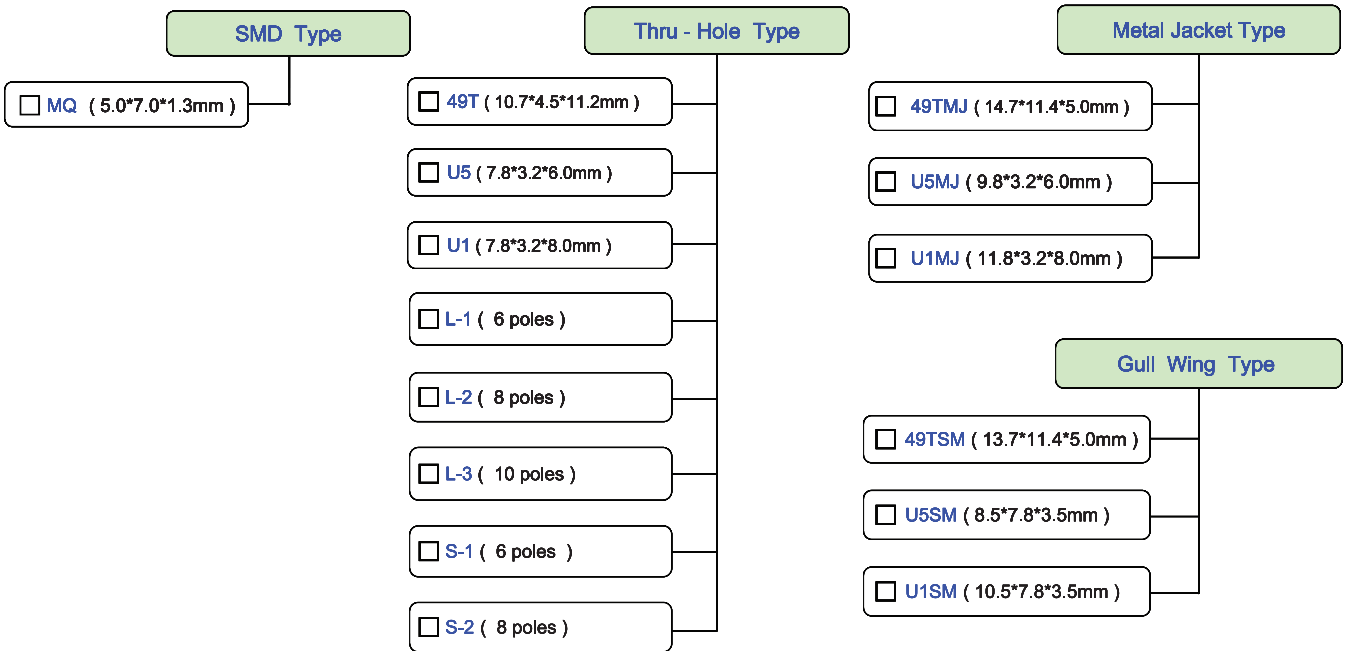
✎	✎	★	✎	✎		✎		✎		✎	customer to specify
V	M38	G	S	5	-	12.800	-	2.5	/	-30+75	
(1)	(2)	(3)	(4)	(5)		(6)		(7)		(8)	

(1) " V " for VCTCXO ; Omit " V " if TCXO ; (2) Package code ; (3) " G " RoHS compliance . Omit " G " if not required .  
 (4) Wave form code " S " for clipped sine wave ; (5) Supply voltage code : " 3 " for +3.0V , " 33 " for +3.3V , " 5 " for +5.0V ;  
 (6) Frequency in MHz ; (7) Frequency stability in ±ppm ; (8) Operating temperature range in °C . "-30+75" represents -30C to +75°C operating temperature range .

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# How To Specify A Monolithic Crystal Filter

## Holder Type



## Basic Spec. must be specified

- A-1 : Holder type: \_\_\_\_\_  SMD Leadless  Thru-Hole type  SMD Gull Wing type  Jacket type
- A-2 : Frequency : \_\_\_\_\_ MHz  10.7 MHz ( 10.5 MHz ~ 11.000 MHz available only in 49T series )  
 21.400 MHz  21.700 MHz  45.000 MHz ( fundamental mode )  45.000 MHz ( 3rd overtone )
- A-3 : No. of poles :  2 poles  4 poles  6 poles  8 poles  10 poles
- A-4 : Pass Bandwidth :  ±3.75 KHz  ±4.5 KHz  ±7.5 KHz  ±12.0 KHz  ±15.0 KHz min at 3 dB
- A-5 : Stop Bandwidth : ± \_\_\_\_\_ KHz max at \_\_\_\_\_ dB and ± \_\_\_\_\_ KHz max at \_\_\_\_\_ dB
- A-6 : Ripple : \_\_\_\_\_ dB ( max. )
- A-7 : Insertion Loss : \_\_\_\_\_ dB ( max. )
- A-8 : Guaranteed Attenuation : fo + \_\_\_\_\_ KHz to fo - \_\_\_\_\_ KHz min. at \_\_\_\_\_ dB
- A-9 : Terminating Impedance : \_\_\_\_\_ Ω // \_\_\_\_\_ pF
- A-10 : Operating Temperature : \_\_\_\_\_ °C to \_\_\_\_\_ °C

## Part Number Format and Example

Examples of quartz crystal : 21.7M15DU5SMG ~~/~~ = User to specify; ★ = Option

<del>/</del>		<del>/</del>		<del>/</del>		<del>/</del>		★	<del>/</del> User to specify
21.7 M		15		D		U5		G	
(1)		(2)		(3)		(4)		(6)	

(1) Frequency code ; (2) Pass Bandwidth code ( " 7.5 " for ±3.75 KHz band width, " 15 " for ±7.5 KHz band width, " 30 " for ±15.0 KHz band width ) ;  
 (3) No. of poles ( " A " for 2 poles, " B " for 4 poles, " C " for 6 poles, " D " for 8 poles, " E " for 10 poles ) ;  
 (4) Package code ; (5) Gull Wing Type ; (6) RoHS compliance ( If not, leave blank ) ;

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# X21

[ 2.0 \* 1.6 \* 0.6 mm ]

Surface Mount Crystals

Fund.

Min.  
20MHz

Max.  
54MHz

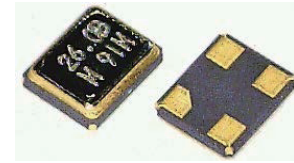


RoHS Compliance

## Features

### Specifications

- The entire package can be grounded via the top metal lid and the two bottom pads .
- Small footprint. Ideal for space constrained applications
- Exhibits extremely low aging with a high shock & vibration resistance.



## General Specifications

Item / Type	X21 series ( 2.0 * 1.6 * 0.6 mm )
Frequency Range	20.000 ~ 54.000 MHz
Crystal Cut	Fundamental Mode , AT-Cut
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance
Drive Level	10μ W typica ( 100μ W max. )
Frequency Tolerance	± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C
Frequency Stability	See Table 2
Aging	ΔF / F : ±3 ppm / year ( max. )
Storage Temperature Range	- 50°C to 105°C

Table 1

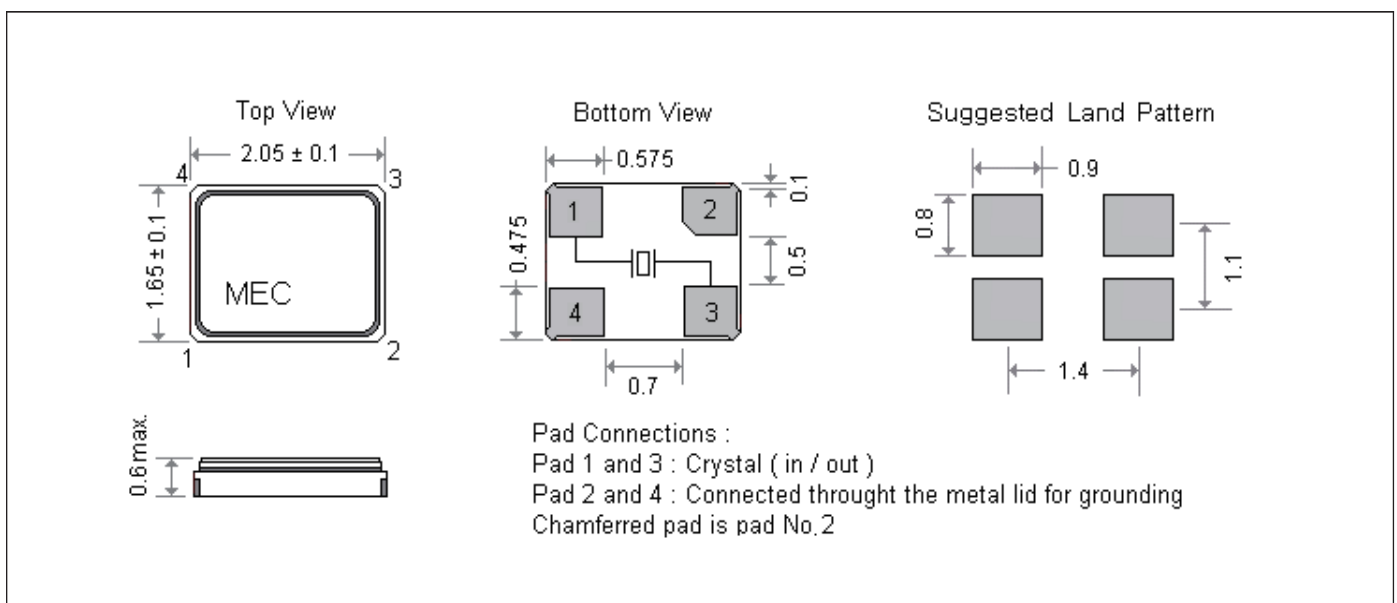
ESR ( Equivalent Series Resistance )	
Frequency Range	E. S. R.
20.000 ~ 30.000 MHz	120 Ω max.
30.100 ~ 54.000 MHz	80 Ω max

Table 2

Frequency stability Vs Operating temperature range							
Stability code	Temp. (°C) \ ppm	± 5	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	▲	○	○	○	○	○
Y	-20 to 70°C		○	○	○	○	○
I	-40 to 85°C			○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )



Mercury [www.mercury-crystal.com](http://www.mercury-crystal.com)

# X22

[ 2.5 \* 2.0 \* 0.6 mm ]

Surface Mount Crystals

Fund.

Min.  
16MHz

Max.  
60MHz

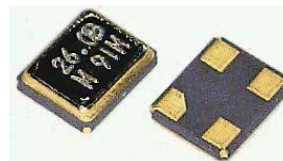


RoHS Compliance

## Features

### Specifications

- The entire package can be grounded via the top metal lid and the two bottom pads .
- Small footprint. Ideal for space constrained applications
- Exhibits extremely low aging with a high shock & vibration resistance.



## General Specifications

Item / Type	X22 series ( 2.5 * 2.0 * 0.6 mm )
Frequency Range	16.000 ~ 60.000 MHz
Crystal Cut	Fundamental Mode , AT-Cut
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance
Drive Level	10μ W typica ( 100μ W max. )
Frequency Tolerance	± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C
Frequency Stability	See Table 2
Aging	ΔF / F : ±3 ppm / year ( max. )
Storage Temperature Range	- 50°C to 105°C

Table 1

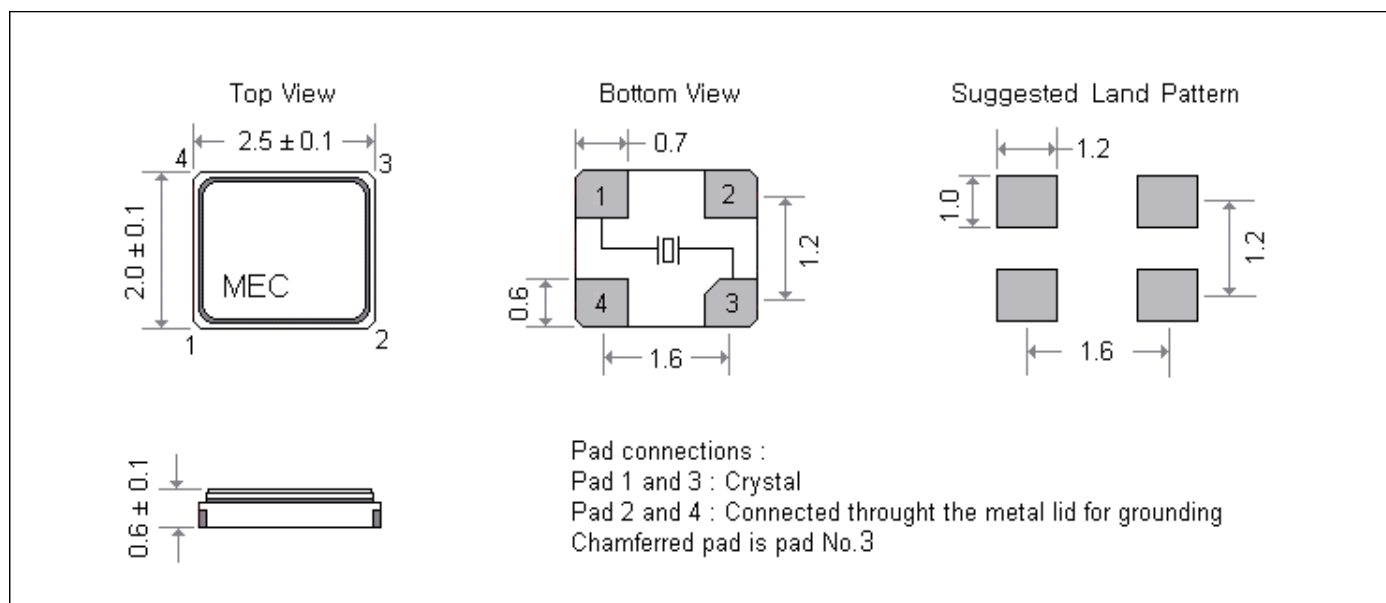
ESR ( Equivalent Series Resistance )	
Frequency Range	E. S. R.
16.000 ~ 21.000 MHz	120 Ω max.
21.100 ~ 26.000 MHz	80 Ω max
26.100 ~ 60.000 MHz	60 Ω max

Table 2

Frequency stability Vs Operating temperature range							
Stability code	Temp. (°C) \ ppm	± 5	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	○	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○	○
I	-40 to 85°C			○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )



Mercury [www.mercury-crystal.com](http://www.mercury-crystal.com)

# X32

[ 3.2 \* 2.5 \* 0.7 mm ]

Surface Mount Crystals

Fund.

Min.  
12MHz

Max.  
60MHz

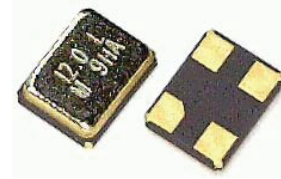


RoHS Compliance

## Features

### Specifications

- The entire package can be grounded via the top metal lid and the two bottom pads .
- Small footprint. Ideal for space constrained applications
- Exhibits extremely low aging with a high shock & vibration resistance.



## General Specifications

Item / Type	<b>X32 series ( 3.2 * 2.5 * 0.7 mm )</b>
Frequency Range	12.000 ~ 60.000 MHz
Crystal Cut	Fundamental Mode , AT-Cut
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance
Drive Level	10μ W typica ( 100μ W max. )
Frequency Tolerance	± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C
Frequency Stability	See Table 2
Aging	ΔF / F : ±3 ppm / year ( max. )
Storage Temperature Range	- 50°C to 105°C

Table 1

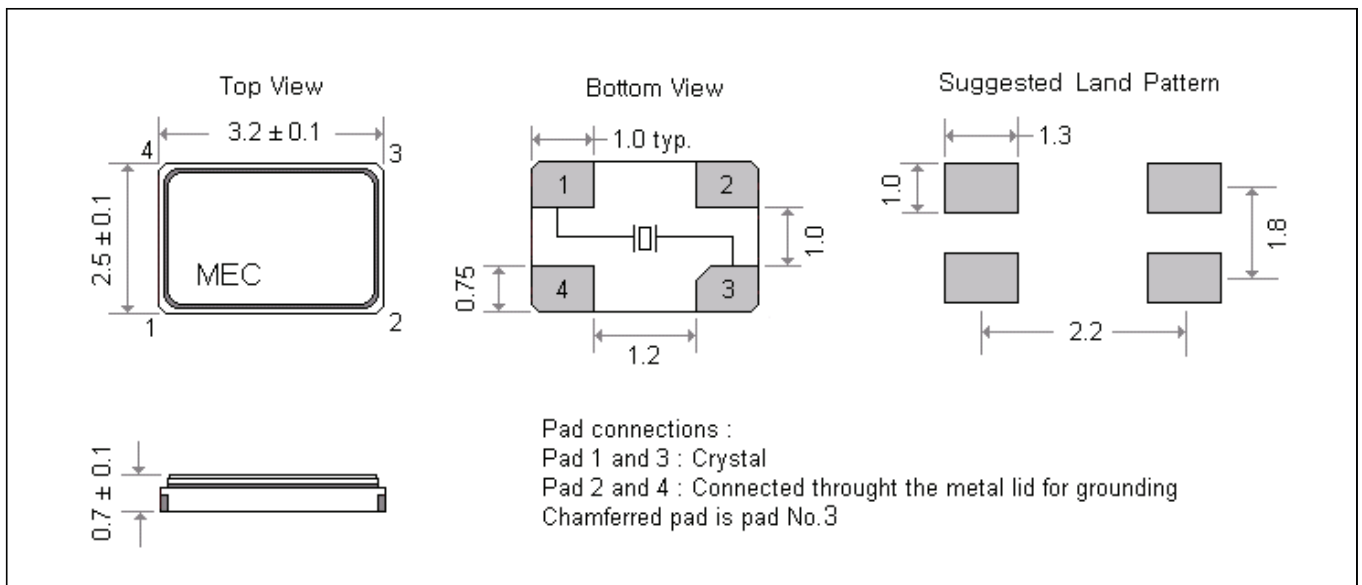
ESR ( Equivalent Series Resistance )	
Frequency Range	E. S. R.
12.000 ~ 20.000 MHz	100 Ω max.
20.100 ~ 60.000 MHz	60Ω max

Table 2

Frequency stability Vs Operating temperature range							
Stability code	Temp. (°C) \ ppm	± 5	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	○	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○	○
I	-40 to 85°C			○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )



Mercury [www.mercury-crystal.com](http://www.mercury-crystal.com)

# X42

[ 4.0 \* 2.5 \* 0.7 mm ]

Surface Mount Crystals

Fund.

Min.  
12MHz

Max.  
60MHz

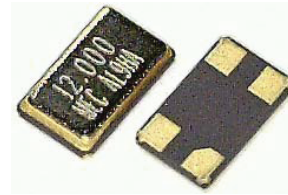


RoHS Compliance

## Features

### Specifications

- Exhibits extremely low aging and a high shock and vibration resistance .
- The entire package can be grounded via the top metal lid and two bottom pads .
- This low 0.6mm package height is ideal for height constrained applications.



## General Specifications

Item / Type	<b>X42 series ( 4.0 * 2.5 * 0.6 mm )</b>
Frequency Range	12.000 ~ 60.000 MHz , AT-cut , Fundamental Mode ( see Table 1 )
Crystal Cut	Fundamental Mode , AT-Cut
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance
Drive Level	10μ W typica ( 100μ W max. )
Frequency Tolerance	± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C
Frequency Stability	See Table 2
Aging	$\Delta F / F : \pm 3 \text{ ppm / year ( max. )}$
Storage Temperature Range	- 50°C to 105°C

Table 1

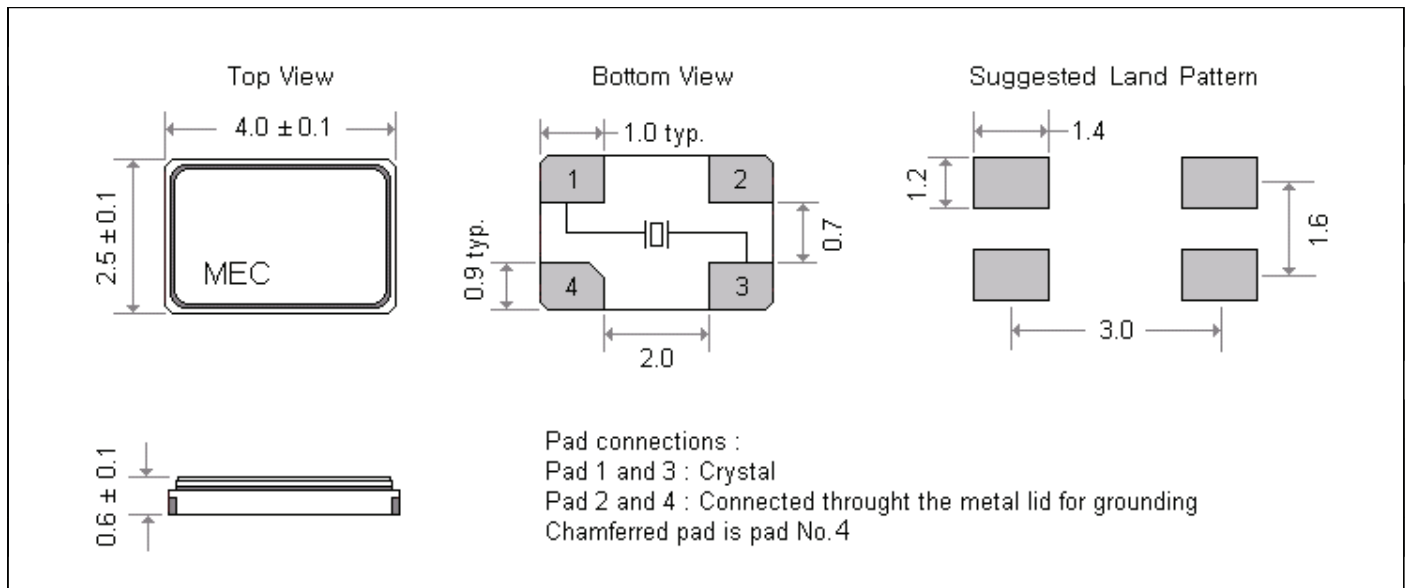
ESR ( Equivalent Series Resistance )	
Frequency Range	E. S. R.
12.000 ~ 16.000 MHz	80 Ω max
16.100 ~ 60.000 MHz	50 Ω max

Table 2

Frequency stability vs Operating temperature range							
Stability code	Temp. (°C) \ ppm	± 5	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	○	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○	○
I	-40 to 85°C			○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )



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**MJ**

[ 5.0 \* 3.2 \* 0.9 mm ]

Surface Mount Crystals

Fund.

3rd O.T.

Min.  
8MHzMax.  
125MHz

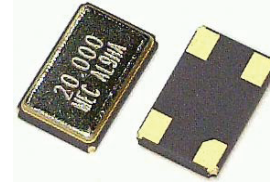
RoHS Compliance

Crystal Units

## Features

## Specifications

- The entire package can be grounded via the top metal lid and the two bottom pads
- Frequency tolerance as tight as +/-5ppm is available.



## General Specifications

Item / Type	MJ series ( 5.0 * 3.2 * 0.9 mm )
Frequency Range & Crystal Cut	8.000 ~ 50.000 MHz , AT-cut , Fundamental Mode ( see Table 1 )
	40.000 ~ 125.000 MHz , AT-cut , 3rd overtone ( see Table 1 )
	NOTE : Frequencies between 40 and 50 MHz can be either AT fundamental or AT 3rd overtone. Please add " AF " after the frequency on the part number for an AT-cut fundamental mode crystal and " A3 " for an AT-Cut 3rd overtone crystal. For example: MJ-40.000AF-20P.
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance
Drive Level	10 $\mu$ W typica ( 100 $\mu$ W max. )
Frequency Tolerance	$\pm$ 5 ppm , $\pm$ 10 ppm , $\pm$ 20 ppm or $\pm$ 30 ppm at 25°C
Frequency Stability	See Table 2
Aging	$\Delta$ F / F : $\pm$ 3 ppm / year ( max. )
Storage Temperature Range	- 50°C to 105°C

Table 1

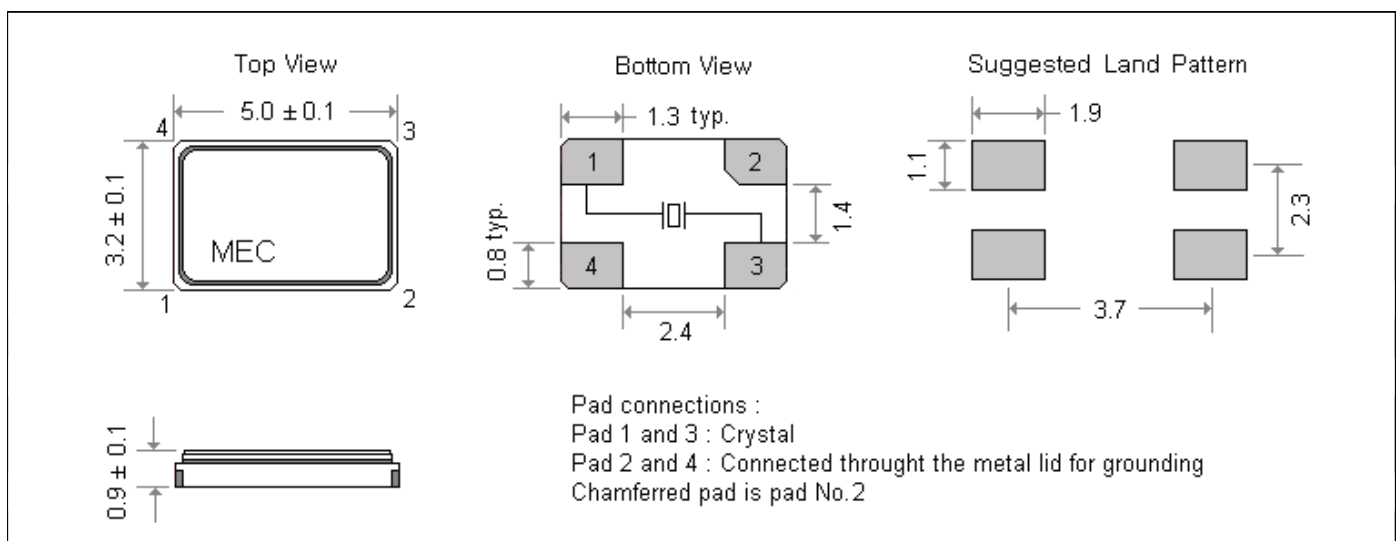
ESR ( Equivalent Series Resistance )		
Frequency Range	Oscillator Mode	E. S. R.
8.000 ~ 10.000 MHz	AT-Cut , Fund. Mode	80 $\Omega$ max
10.100 ~ 50.000 MHz	AT-Cut , Fund. Mode	50 $\Omega$ max
40.000 ~ 125.000 MHz	AT-Cut , 3rd Overtone	80 $\Omega$ max

Table 2

Frequency stability vs Operating temperature range							
Stability code	Temp. (°C) \ ppm	$\pm$ 5	$\pm$ 10	$\pm$ 15	$\pm$ 20	$\pm$ 25	$\pm$ 30
X	-10 to 60°C	○	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○	○
I	-40 to 85°C			○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )

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# MF

[ 6.0 \* 3.5 \* 1.0 mm ]

Surface Mount Crystals

Fund.

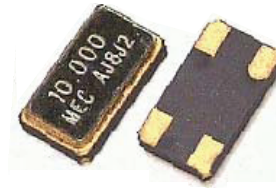
3rd O.T.

Min.  
8MHz

Max.  
125MHz



RoHS Compliance



## Features

### Specifications

- The entire package can be grounded via the top metal lid and the two bottom pads.
- This tight tolerance and tight stability crystal is ideal for telecommunications.
- This crystal package will withstand solder reflow .

## General Specifications

Item / Type	MF series ( 6.0 * 3.5 * 1.0 mm )
Frequency Range & Crystal Cut	8.000 ~ 50.000 MHz , AT-cut , Fundamental Mode ( see Table 1 ) 30.000 ~ 125.000 MHz , AT-cut , 3rd overtone ( see Table 1 )
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance
Drive Level	10μ W typica ( 100μ W max. )
Frequency Tolerance	± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C
Frequency Stability	See Table 2
Aging	ΔF / F : ±3 ppm / year ( max. )
Storage Temperature Range	-50°C to 105°C

Table 1

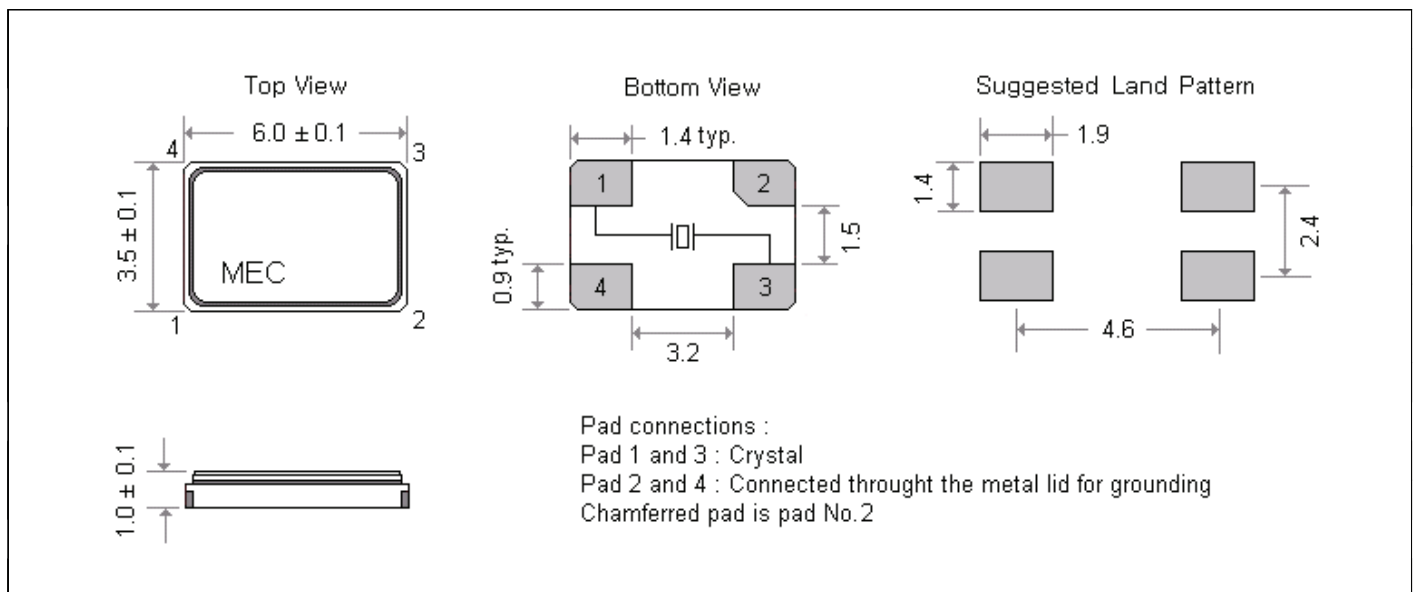
ESR ( Equivalent Series Resistance )					
Freq.(MHz)	Osc. Mode	E.S.R.	Freq.(MHz)	Osc. Mode	E.S.R.
8.0 ~ 12.0	AT , Fund.	80 Ω	30.0~ 40.0	AT , 3rd	100 Ω
12.1 ~ 16.0	AT , Fund.	60 Ω	40.1~ 50.0	AT , 3rd	80 Ω
16.1~ 50.0	AT , Fund.	40 Ω	50.1~ 125.0	AT , 3rd	50 Ω

Table 2

Frequency stability vs Operating temperature range							
Stability code	Temp. (°C) \ ppm	± 5	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	○	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○	○
I	-40 to 85°C			○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )



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**MQ**

[ 7.0 \* 5.0 \* 1.2 mm ]

Surface Mount Crystals

Fund.

3rd O.T.

Min.

6MHz

Max.

125MHz



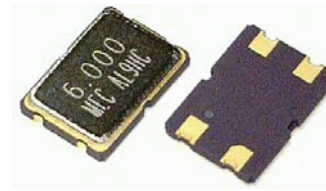
RoHS Compliance

Crystal Units

## Features

## Specifications

- 1.2 mm height. Ideal for space-constrained applications such as PCMCIA
- A ceramic package with a seam sealed kovar lid.
- The whole package can be grounded via the top metal lid and the two bottom pads .



## General Specifications

Item / Type	MQ series ( 7.0 * 5.0 * 1.2 mm )
Frequency Range & Crystal Cut	6.000 ~ 50.000 MHz , AT-cut , Fundamental Mode ( see Table 1 ) 30.000 ~ 125.000 MHz , AT-cut , 3rd overtone ( see Table 1 )
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance
Drive Level	10μ W typica ( 100μ W max. )
Frequency Tolerance	± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C
Frequency Stability	See Table 2
Aging	ΔF / F : ±3 ppm / year ( max. )
Storage Temperature Range	- 50°C to 105°C

Table 1

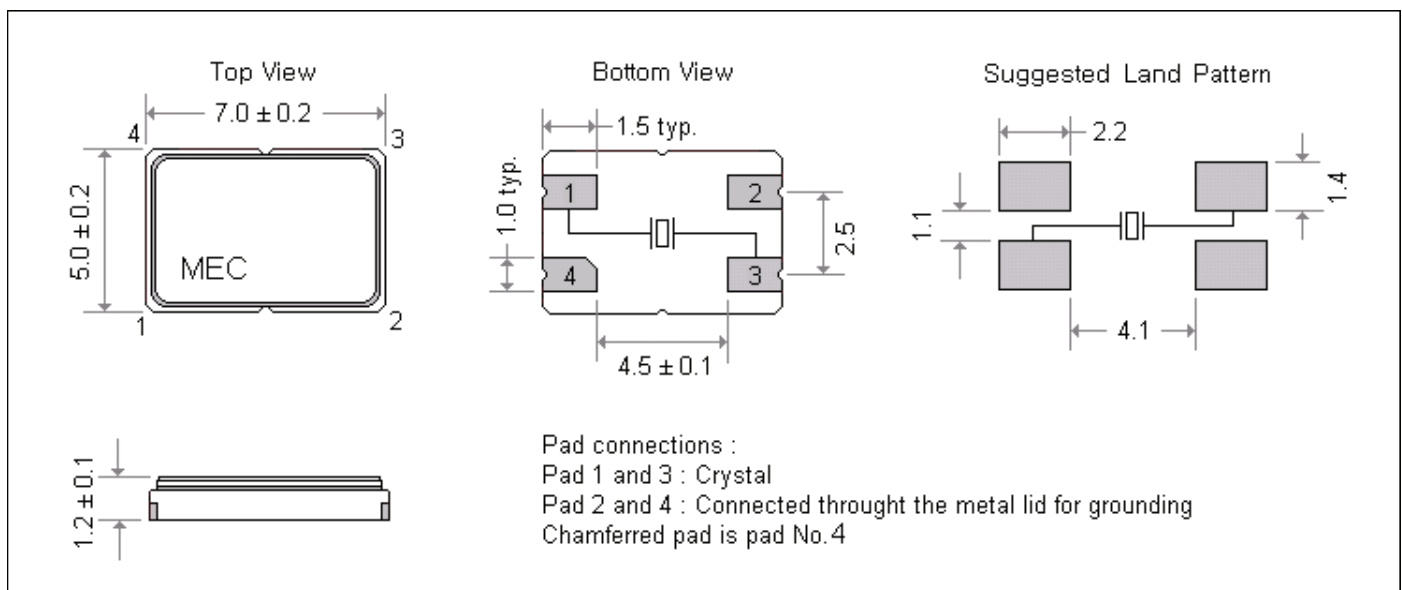
ESR ( Equivalent Series Resistance )					
Freq.(MHz)	Osc. Mode	E.S.R.	Freq.(MHz)	Osc. Mode	E.S.R.
6.0 ~ 8.0	AT , Fund.	80 Ω	30.0~ 40.0	AT , 3rd	100 Ω
8.1 ~ 11.0	AT , Fund.	60 Ω	40.1~ 50.0	AT , 3rd	80 Ω
11.1~ 14.0	AT , Fund.	50 Ω	50.1~125.0	AT , 3rd	90 Ω
14.1~ 50.0	AT , Fund.	40 Ω			

Table 2

Frequency stability Vs Operating temperature range							
Stability code	Temp. (°C) \ ppm	± 5	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	○	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○	○
I	-40 to 85°C			○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )

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# M49

[ 12.4 \* 4.5 \* 4.0 mm ]

# ML49

[ 12.4 \* 4.5 \* 3.0 mm ]

Surface Mount Crystals

Fund.

3rd O.T.

Min.

3.2MHz

Max.

70MHz

## Features

### Specifications

- High frequency fundamental mode is available in BT-cut
- Withstands solder reflow and is available in EIA-481A tape and reel.
- AT-strip crystal inside. Optimized for low spurious.
- Lowest cost among all Mercury SMD crystals



## General Specifications

Item / Type	M49 ( 12.4 * 4.5 * 4.0mm )	ML49 ( 12.4 * 4.5 * 3.0mm ) series
Frequency Range & Crystal Cut	3.2.000 ~ 48.000 MHz , AT-cut , Fundamental Mode ( see Table 1 ) 27.000 ~ 70.000 MHz , AT-cut , 3rd overtone ( see Table 1 ) 24.000 ~ 48.000 MHz , BT-cut , Fundamental Mode ( see Table 1 )	
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance	
Drive Level	100μ W typical ( 500μ W max. )	
Frequency Tolerance	± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C	
Frequency Stability	See Table 2	
Aging	ΔF / F : ±3 ppm / year ( max. )	
Storage Temperature Range	- 50°C to 105°C	

Table 1

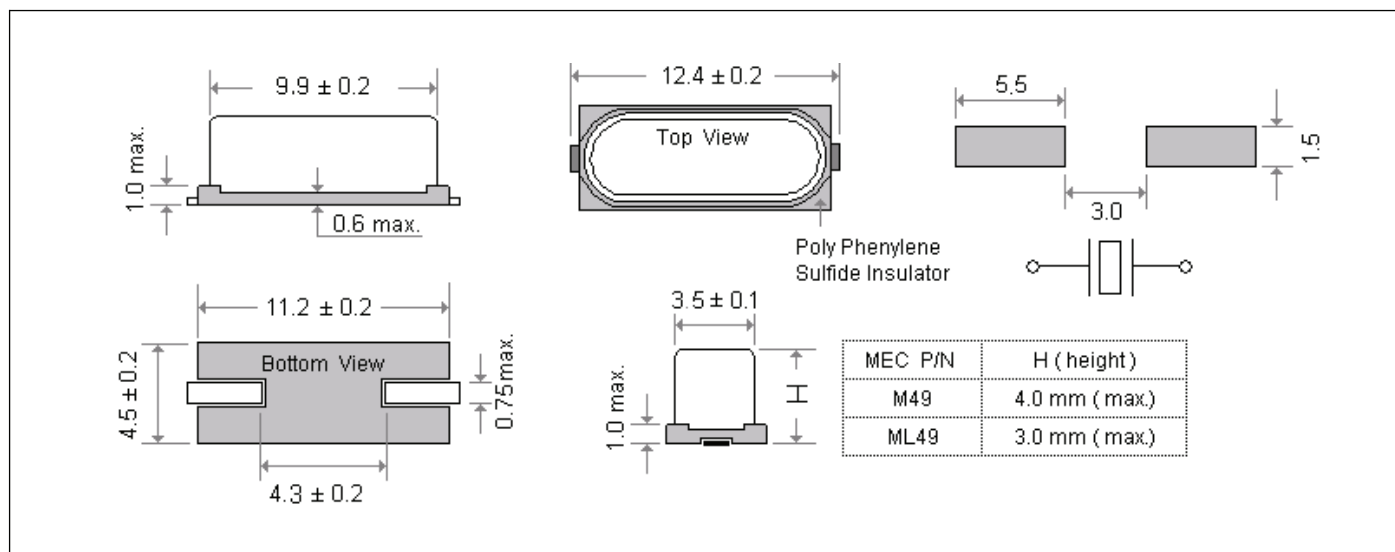
ESR ( Equivalent Series Resistance )					
Freq.(MHz)	Osc. Mode	E.S.R.	Freq.(MHz)	Osc. Mode	E.S.R.
3.2 ~ 3.4	AT , Fund.	300 Ω	24.0 ~ 48.0	BT , Fund.	40 Ω
3.5 ~ 6.0	AT , Fund.	120 Ω	27.0 ~ 30.0	AT , 3rd	150 Ω
6.1 ~ 10.0	AT , Fund.	60 Ω	30.1 ~ 50.0	AT , 3rd	100 Ω
10.1 ~ 30.0	AT , Fund.	40 Ω	50.1 ~ 70.0	AT , 3rd	80 Ω

Table 2

Frequency stability Vs Operating temperature range						
Stability code	Temp. (°C) \ ppm	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○
I	-40 to 85°C		○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )



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# MP4 , MP5

[ 12.9 \* 4.5 \* 4.8 ( 3.8 ) mm ]

# MP24 , MP25

[ 11.4 \* 5.0 \* 4.8 ( 3.8 ) mm ]

Surface Mount Crystals

Fund.

3rd O.T.

Min.

3.2MHz

Max.

70MHz

## Features

### Specifications

- MP4 , MP5 , MP24 and MP25 are designed for top board assembly and a one time solder reflow only . Do not mount these products with the metal housing downward.



## General Specifications

Item / Type	MP4 ( 12.9 * 4.5 * 3.8 mm )	MP5 ( 12.9 * 4.5 * 4.8 mm ) series
	MP24 ( 11.4 * 5.0 * 3.8 mm )	MP25 ( 11.4 * 5.0 * 4.8 mm ) series
Frequency Range & Crystal Cut	3.2.000 ~ 48.000 MHz , AT-cut , Fundamental Mode ( see Table 1 ) 27.000 ~ 70.000 MHz , AT-cut , 3rd overtone ( see Table 1 ) 24.000 ~ 48.000 MHz , BT-cut , Fundamental Mode ( see Table 1 )	
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance	
Drive Level	100μ W typical ( 500μ W max. )	
Frequency Tolerance	± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C	
Frequency Stability	See Table 2	
Aging	ΔF / F : ±3 ppm / year ( max. )	
Storage Temperature Range	- 50°C to 105°C	

Table 1

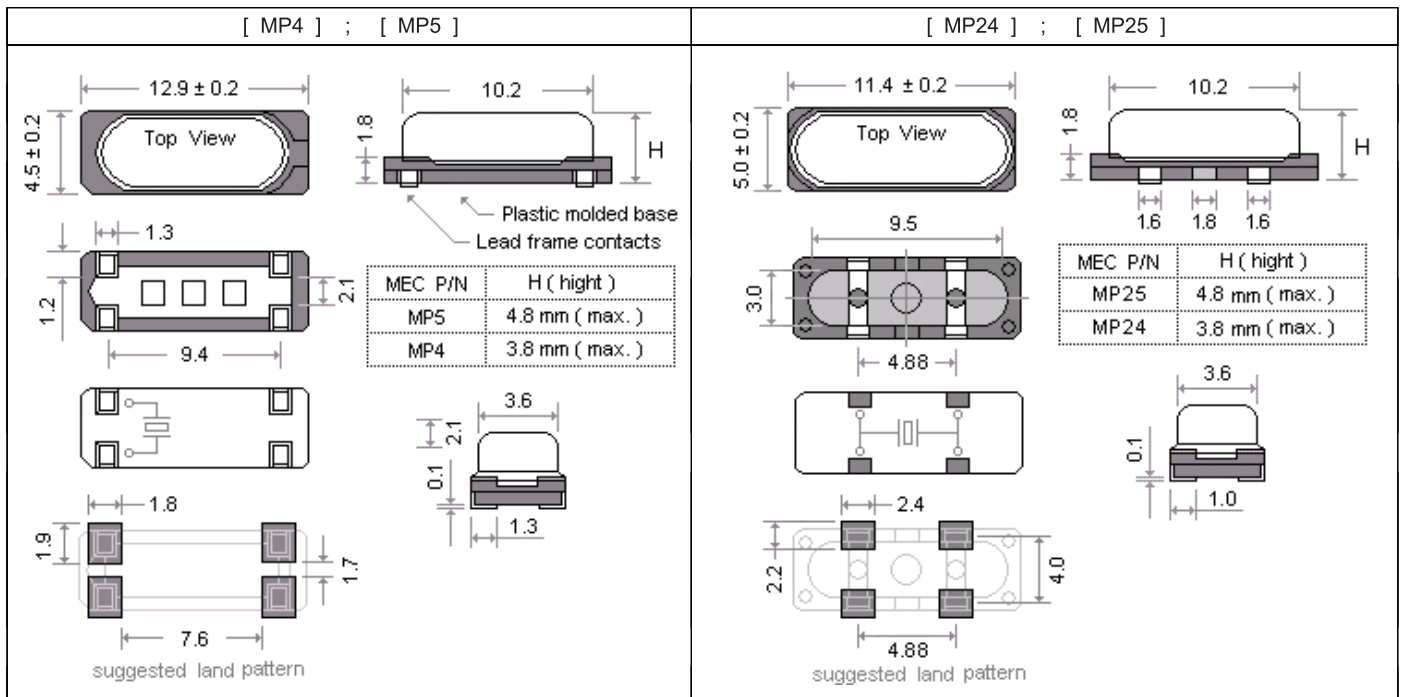
ESR ( Equivalent Series Resistance )					
Freq. (MHz)	Osc. Mode	E.S.R	Freq. (MHz)	Osc. Mode	E.S.R
3.2 ~ 3.4	AT , Fund.	300 Ω	24.0 ~ 48.0	BT , Fund.	40 Ω
3.5 ~ 6.0	AT , Fund.	120 Ω	27.0 ~ 30.0	AT , 3rd	150 Ω
6.1 ~ 10.0	AT , Fund.	60 Ω	30.1 ~ 50.0	AT , 3rd	100 Ω
10.1 ~ 30.0	AT , Fund.	40 Ω	50.1 ~ 70.0	AT , 3rd	80 Ω

Table 2

Frequency stability vs Operating temperature range						
Stability code	Temp. (°C) \ ppm	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○
I	-40 to 85°C		○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )



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# HUS

[ 10.7 \* 4.3 \* 3.5 mm ]

# HUSL

[ 10.7 \* 4.3 \* 2.5 mm ]

Thru - Hole Crystals

Fund.

3rd O.T.

Min.

3.2MHz

Max.

70MHz

## Features

### Specifications

- Available in HUSL ( 2.5mm height ) & HUS ( 3.5mm height )
- Low cost and light weight
- High frequency fundamental mode is available in BT cut



RoHS Compliance

## General Specifications

Item / Type	HUSL ( 10.7 * 4.3 * 2.5mm )	HUS ( 10.7 * 4.3 * 3.5mm ) series
Frequency Range & Crystal Cut	3.2.000 ~ 48.000 MHz , AT-cut , Fundamental Mode ( see Table 1 ) 27.000 ~ 70.000 MHz , AT-cut , 3rd overtone ( see Table 1 ) 24.000 ~ 48.000 MHz , BT-cut , Fundamental Mode ( see Table 1 )	
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance	
Drive Level	100μ W typical ( 500μ W max. )	
Frequency Tolerance	± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C	
Frequency Stability	See Table 2	
Aging	ΔF / F : ±3 ppm ( max. )	
Storage Temperature Range	- 50°C to 105°C	

Table 1

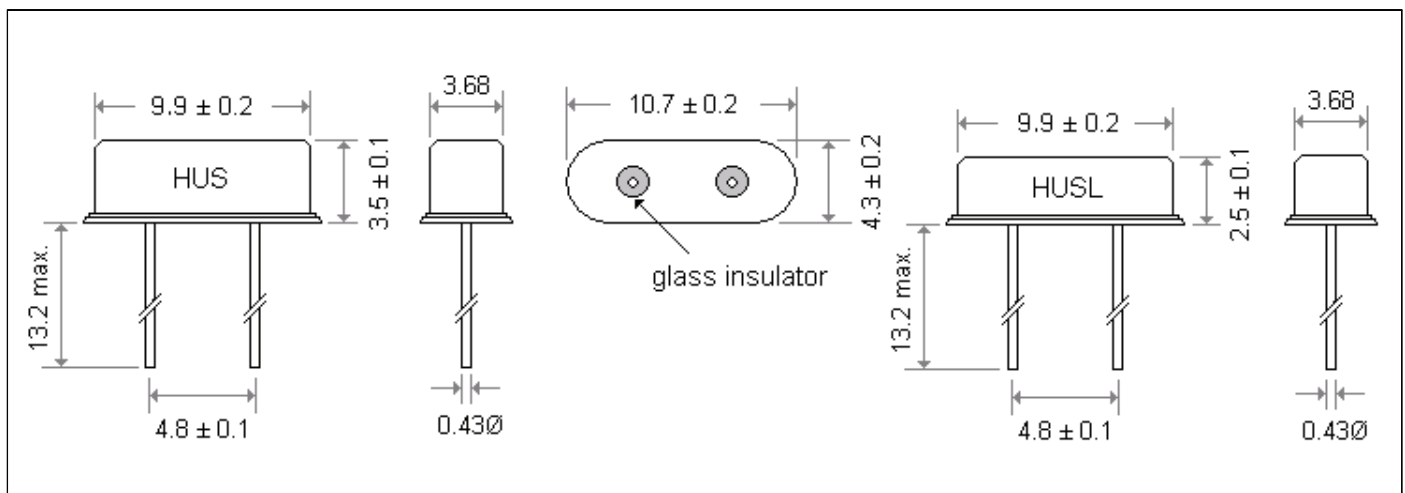
ESR ( Equivalent Series Resistance )					
Freq.(MHz)	Osc. Mode	E.S.R.	Freq.(MHz)	Osc. Mode	E.S.R.
3.2 ~ 3.4	AT , Fund.	300 Ω	24.0 ~ 48.0	BT , Fund.	40 Ω
3.5 ~ 6.0	AT , Fund.	120 Ω	27.0 ~ 30.0	AT , 3rd	150 Ω
6.1 ~ 10.0	AT , Fund.	60 Ω	30.1 ~ 50.0	AT , 3rd	100 Ω
10.1 ~ 30.0	AT , Fund.	40 Ω	50.1 ~ 70.0	AT , 3rd	80 Ω

Table 2

Frequency stability vs Operating temperature range						
Stability code	Temp. (°C) \ ppm	± 10	± 15	± 20	± 25	± 30
X	-10 to 60°C	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○
I	-40 to 85°C		○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )



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# U1, U5

[ 7.8 \* 3.2 \* 8.0 ( 6.0 ) mm ]

# U1SL

[ 7.8 \* 2.6 \* 8.0 mm ]

Thru - Hole Crystals

Fund.

3rd O.T.

5th O.T.

Min.

1.0MHz

Max.

200MHz

## Features

### Specifications

- A round shaped AT-Cut crystal plate inside.
- Available up to 200 MHz using a 5th overtone crystal mode
- Annealed and pre-aged for low frequency drift over a long-term operation



Crystal Units

## General Specifications

Item / Type	U1	U1SL	U5	U1MJ	U5MJ
Frequency Range	U1 & U1MJ : 1.0 ~ 1.2MHz , 4.0 ~ 200.0MHz ; U5 : 10.0 ~ 200MHz				
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance				
Drive Level	100μ W typical ( 500μ W max. )				
Frequency Tolerance	AT-cut : ± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C SL-cut : ± 50 ppm at 25°C				
Frequency Stability	See Table 2				
Aging	ΔF / F : ±3 ppm / year ( max. )				
Storage Temperature Range	- 50°C to 105°C				

Table 1

U1 & U1MJ ESR ( Equivalent Series Resistance )					
Freq.(MHz)	Osc. Mode	E.S.R.	Freq.(MHz)	Osc. Mode	E.S.R.
1.0 ~ 1.2	SL, Fund.	5K Ω	11.0 ~ 12.9	AT, Fund.	40 Ω
6.0 ~ 6.9	AT, Fund.	100 Ω	13.0 ~ 45.0	AT, Fund.	25 Ω
7.0 ~ 7.9	AT, Fund.	90 Ω	30.0 ~ 50.0	AT, 3rd	40 Ω
8.0 ~ 8.9	AT, Fund.	80 Ω	50.1 ~ 100.0	AT, 3rd	50 Ω
9.0 ~ 10.9	AT, Fund.	90 Ω	80.0 ~ 200.0	AT, 5th	80 Ω

U5 & U5MJ ESR ( Equivalent Series Resistance )					
Freq.(MHz)	Osc. Mode	E.S.R.	Freq.(MHz)	Osc. Mode	E.S.R.
10.0 ~ 11.9	AT, Fund.	60 Ω	90.1 ~ 135.0	AT, 3rd	40 Ω
12.0 ~ 14.9	AT, Fund.	50 Ω	90.1 ~ 159.0	AT, 5th	100 Ω
15.0 ~ 35.0	AT, Fund.	30 Ω	160.0 ~ 200.0	AT, 5th	80 Ω
35.1 ~ 90.0	AT, 3rd	60 Ω			

Table 2

Frequency stability vs Operating temperature range									
Stability code	Temp. (°C) \ ppm	± 5	± 10	± 15	± 20	± 25	± 30	± 50	± 100 (SL-cut )
X	-10 to 60°C	○	○	○	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○	○	○	○
I	-40 to 85°C		▲	○	○	○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )

Dip type ( U1, U1SL, U5 )				Metal jacket ( U1MJ, U5MJ )			
	H	T1	T2		H	W	
U1	8.0 ± 0.2	2.2 ± 0.2	3.2 ± 0.2	U1MJ	8.0 ± 0.2	11.8 ± 0.2	
U1SL	8.0 ± 0.2	1.8 ± 0.2	2.6 ± 0.2	U5MJ	6.0 ± 0.2	9.8 ± 0.2	
U5	6.0 ± 0.2	2.2 ± 0.2	3.2 ± 0.2				

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**H49**

[ 10.7 \* 4.5 \* 13.6 mm ]

**49T**

[ 10.7 \* 4.5 \* 11.2 mm ]

Thru - Hole Crystals

Fund.

3rd O.T.

5th O.T.

Min.

1.0MHz

Max.

160MHz

## Features

## Specifications

- Tight tolerance and stability. Ideal for communication equipment
- Available up to 200 MHz using a 5th overtone crystal mode
- RoHS compliant versions are also available.



RoHS Compliance

## General Specifications

Item / Type	H49	H49MJ	49T	49TMJ
Frequency Range	1.0 ~ 1.3MHz , 1.8 ~ 200.0MHz ( see Table 1 )		3.1 ~ 200.0MHz ( see Table 1 )	
Load Capacitance	Series or Parallel ( 8 to 32 pF ) resonance			
Drive Level	100μ W typical ( 500μ W max. )			
Frequency Tolerance	AT-cut: ± 5 ppm , ± 10 ppm , ± 20 ppm or ± 30 ppm at 25°C SL-cut: ± 50 ppm at 25°C			
Frequency Stability	See Table 2			
Aging	ΔF / F : ± 2 ppm / year ( max. )			
Storage Temperature Range	- 50°C to 105°C			

Table 1

H49 ; 49T ESR ( Equivalent Series Resistance )							
Freq. ( MHz )	Hold Type	crystal cut and osc. Mode	E.S.R.	Freq. ( MHz )	Hold Type	crystal cut and osc. Mode	E.S.R.
1.0 ~ 1.3	H49	SL , Fund.	5K Ω	7.1 ~ 10.0	H49 , 49T	AT , Fund.	35 Ω
1.8 ~ 3.0	H49	AT , Fund.	400 Ω	10.1 ~ 30.0	H49 , 49T	AT , Fund.	25 Ω
3.1 ~ 3.5	H49	AT , Fund.	150 Ω	30.1 ~ 45.0	H49 , 49T	AT , Fund.	20 Ω
3.6 ~ 5.0	H49 , 49T	AT , Fund.	100 Ω	24.0 ~ 100.0	H49 , 49T	AT , 3rd	60 Ω
5.1 ~ 7.0	H49 , 49T	AT , Fund.	50 Ω	80.0 ~ 160.0	H49 , 49T	AT , 5th	70 Ω

Table 2

Frequency stability vs Operating temperature range									
Stability code	Temp. (°C) \ ppm	± 5	± 10	± 15	± 20	± 25	± 30	± 50	± 100 (SL-cut)
X	-10 to 60°C	○	○	○	○	○	○	○	○
Y	-20 to 70°C	▲	○	○	○	○	○	○	○
I	-40 to 85°C	○	○	○	○	○	○	○	○

○ : available ; ▲ : contact Mercury

## Outline Dimensions ( Unit : mm )

Dip type ( H49 , 49T )				Jacket type ( H49MJ , 49TMJ )																		
		<table border="1"> <thead> <tr> <th></th> <th>H</th> </tr> </thead> <tbody> <tr> <td>H49</td> <td>13.6 ± 0.2</td> </tr> <tr> <td>49T</td> <td>11.2 ± 0.2</td> </tr> </tbody> </table>			H	H49	13.6 ± 0.2	49T	11.2 ± 0.2			<table border="1"> <thead> <tr> <th></th> <th>H</th> <th>W</th> </tr> </thead> <tbody> <tr> <td>H49MJ</td> <td>13.8 ± 0.2</td> <td>17.1 ± 0.2</td> </tr> <tr> <td>49TMJ</td> <td>11.4 ± 0.2</td> <td>14.7 ± 0.2</td> </tr> </tbody> </table>			H	W	H49MJ	13.8 ± 0.2	17.1 ± 0.2	49TMJ	11.4 ± 0.2	14.7 ± 0.2
	H																					
H49	13.6 ± 0.2																					
49T	11.2 ± 0.2																					
	H	W																				
H49MJ	13.8 ± 0.2	17.1 ± 0.2																				
49TMJ	11.4 ± 0.2	14.7 ± 0.2																				

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# Mercury Crystal Oscillator Products Lineup

- [Clock Oscillators](#) ( XOs )
- [VCXOs](#) ( Voltage Controlled XOs )
- [TCXOs](#) ( Temperature Compensated XOs )
- [VCTCXOs](#) ( Voltage Controlled Temperature Compensated XOs )
- [OCXOs](#) ( Oven Controlled XOs )



## Clock Oscillator Selection Guide

Output Wave	Product	Supply	Product Description / Features	
Output Logic	Series	Voltage		
Square Wave  <b>CMOS</b>	H	1.8 / 2.5 / 3.3 / 5.0	General purpose clock oscillators. 20.0 KHz to 160.0 MHz .	
	SWO	1.8 / 2.5 / 3.3 / 5.0	General purpose clock oscillators. 0.3 to 125.0 MHz .	
	H	F	2.5 / 3.3	125.01 MHz to 320.0 MHz , phase jitter : 0.4 ps(typ.) , period jitter RMS : 3.0 ps(typ.) .
		V	3.3	125.01 MHz to 200.0 MHz , phase jitter : 2.3 ps(typ.) , period jitter RMS : 4.0 ps(typ.) . Low cost .
		W	3.3	125.01 MHz to 800.0 MHz , phase jitter : 2.6 ps(typ.) , period jitter RMS : 4.3 ps(typ.) .
	HM	3.3	<b>EMI Reduction Spread Spectrum Clcok Oscillators</b> ( at least 10 dBc. ) 5.0 MHz to 220.0 MHz , Drop-in replacements for all types of packages including 5x7 and 5x3.2 .	
	HK	1.8 / 2.5 / 3.3	Ultra Low Current. Consumes less than one tenth of current when compared with other 0.25 KHz to 50.0 MHz ,equivalent HCMOS oscillators .	
LPO	3.3 / 5.0	Low Power oscillators designed for battery-operated devices. Current consumption in the uA range . 3.0V to 15.0V supply.1Hz to 160 KHz includes popular 32.768 KHz. X-cut tuning fork crystals used .		
Square Wave <b>HCSL</b>	HC	K	3.3 10.0 MHz to 220.0 MHz , phase jitter : 0.2 ps(typ.) ,Phase Noise : 100K (144dBc/Hz) , 10M (155dBc/Hz)	
Square Wave <b>PECL</b>	HP	K	3.3 10.0 MHz to 220.0 MHz , phase jitter : 0.2 ps(typ.) ,Phase Noise : 100K (144dBc/Hz) , 10M (155dBc/Hz)	
		F	3.3 38.0 MHz to 640.0 MHz , phase jitter : 0.4 ps(typ.) , period jitter RMS : 3.0 ps(typ.) .	
		W	3.3 0.75 MHz to 800.0 MHz , phase jitter : 2.35 ps(typ.) , period jitter RMS : 4.3 ps(typ.) .	
Square Wave <b>LVDS</b>	HD	K	3.3 10.0 MHz to 220.0 MHz , phase jitter : 0.2 ps(typ.) ,Phase Noise : 100K (144dBc/Hz) , 10M (155dBc/Hz)	
		F	3.3 38.0 MHz to 640.0 MHz , phase jitter : 0.4 ps(typ.) , period jitter RMS : 3.0 ps(typ.) .	
		W	3.3 0.75 MHz to 800.0 MHz , phase jitter : 2.35 ps(typ.) , period jitter RMS : 4.3 ps(typ.) .	
Sine Wave	HSR	2.8 / 3.0 / 5.0	10.0 MHz to 52.0 MHz , Output level 1.0 V p-p into 10K $\Omega$ // 10 pF load .	
	HS	3.3	10.0 MHz to 800.0 MHz , Output level is greater than 0 dBm into 50 $\Omega$ load .	
		5.0	10.0 MHz to 156.0 MHz , Output level is greater than 0 dBm into 50 $\Omega$ load .	

## VCXO Selection Guide

Output Wave	Product	Supply	Product Description / Features
Output Logic	Series	Voltage	
Square Wave  <b>CMOS</b>	G	1.8 / 3.3 / 5.0	General purpose VCXOs. 0.5 to 50.0 MHz .
	G	F	3.3 Up to 640 MHz. PLL-based. 0.4 ps Phase Jitter. -128 dBc/Hz at 100 KHz offset (for 155.520 MHz).
		V	3.3 Up to 200 MHz. PLL-based. -121 dBc/Hz at 100 KHz offset (for 155.520 MHz). Low cost and high performance.
		W	3.3 Up to 800 MHz and low jitter. PLL-based. RMS Phase jitter (12 KHz ~20 MHz) 2.6 ps. -121 dBc/Hz at 100 KHz offset (for 155.520 MHz).
Square Wave <b>PECL</b>	GP	F	3.3 38.0 MHz to 640.0 MHz , phase jitter : 0.4 ps(typ.) , period jitter RMS : 3.0 ps(typ.) .
		W	3.3 0.75 MHz to 800.0 MHz , phase jitter : 2.6 ps(typ.) , period jitter RMS : 4.3 ps(typ.) .
Square Wave <b>LVDS</b>	GD	F	3.3 38.0 MHz to 640.0 MHz , phase jitter : 0.4 ps(typ.) , period jitter RMS : 3.0 ps(typ.) .
		W	3.3 0.75 MHz to 800.0 MHz , phase jitter : 2.6 ps(typ.) , period jitter RMS : 4.3 ps(typ.) .
Sine Wave	GSR	2.8 / 3.0 / 5.0	10.0 MHz to 40.0 MHz , Output level 1.0 V p-p into 10K $\Omega$ // 10 pF load .
	GS	3.3	10.0 MHz to 800.0 MHz , Output level > 0 dBm into 50 $\Omega$ load .
		5.0	10.0 MHz to 160.0 MHz , Output level > 0 dBm into 50 $\Omega$ load .

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# Mercury Crystal Oscillators : TCXOs , VCTCXOs and OCXOs

## TCXO ( M series ) & VCTCXO ( VM series ) Selection Guide ; Note " \_\_ " is the package code

Output Wave Output Logic	Product Series	Supply Voltage	Available Frequency / Product Description / Features
<b>Clipped Sine</b>	(V) M__S	3.0 / 3.3 / 5.0	DC-blocked AC coupled 0.8V p-p and 1.0V p-p clipped sine wave output . except VM53S and VM32S
<b>Square Wave  CMOS</b>	(V) M__T	3.0 / 3.3 / 5.0	General purpose TCXOs & VCTCXOs. Up to 156 MHz . 20.0 KHz ~ 50.0 KHz is also available .
	(V) MB__T	2.8 / 3.0 / 3.3	" B " group . Frequency Range : 1.0 ~ 200.0 MHz , Quick Turn , Short Lead time . From 1 day to 1 week .
	(V) MV__T	3.3	" V " group . Frequency Range : 27.0 ~ 200.0 MHz , Integrated Phase Jitter : 2.3 ps typical , 4.0 ps max.
	(V) MW__T	3.3	" W " group . Frequency Range : 200.01 ~ 800.0 MHz , Integrated Phase Jitter : 2.6 ps typical , 4.0 ps max.
Square Wave <b>LVPECL</b>	(V) MW__P	3.3	Differential PECL square wave , " W " group . Freq. Range : 10.0 ~ 800 MHz Integrated Phase Jitter : 2.6 ps typical , 4.0 ps max.
Square Wave <b>LVDS</b>	(V) MW__D	3.3	Differential LVDS square wave , " W " group . Freq. Range : 12.0 ~ 800 MHz Integrated Phase Jitter : 2.6 ps typical , 4.0 ps max.

## OCXO Selection Guide

Output Wave Output Logic	Product Series	Supply Voltage	Product Description / Features : Please refer to page 69 ~ 70 for product series selections.			
<b>Sine Wave  50 ohm load</b>	<b>OC_E</b>	3.3	+3.3 V [ OC14E , OC24E offers only ] : 1.25 MHz to 40.0 MHz			
			AT - cut Freq. Stability			
			Temperature Range	0°C to +60°C	-20°C to +70°C	-40°C to +85°C
			Frequency Stability	± 0.75 ~ ± 0.2 ppm	± 0.15 ~ ± 0.3 ppm	± 0.25 ~ ± 0.5 ppm
			5.0	+5.0 V : 1.25 MHz to 100.0 MHz		
				AT - cut Freq. Stability		
		Temperature Range		0°C to +60°C	-20°C to +70°C	-40°C to +85°C
		Frequency Stability		± 0.02 ~ ± 0.05 ppm	± 0.05 ~ ± 0.1 ppm	± 0.1 ~ ± 0.2 ppm
		SC - cut Freq. Stability				
		Temperature Range		0°C to +60°C	-20°C to +70°C	-40°C to +85°C
		Frequency Stability	± 0.01 ppm	± 0.01 ~ ± 0.02 ppm	± 0.3 ppm	
		12.0	+12.0 V 10.0 MHz to 100.0 MHz			
AT - cut Freq. Stability						
Temperature Range	0°C to +60°C		-20°C to +70°C	-40°C to +85°C		
Frequency Stability	± 0.02 ~ ± 0.05 ppm		± 0.05 ~ ± 0.1 ppm	± 0.1 ~ ± 0.2 ppm		
SC - cut Freq. Stability						
Temperature Range	0°C to +60°C		-20°C to +70°C	-40°C to +85°C		
Frequency Stability	± 0.01 ppm	± 0.01 ~ ± 0.02 ppm	± 0.3 ppm			
<b>Square Wave  CMOS</b>	<b>OC_T</b>	3.3	+3.3 V [ OC14T , OC24T offers only ] : 1.25 MHz to 100.0 MHz			
			AT - cut Freq. Stability			
			Temperature Range	0°C to +60°C	-20°C to +70°C	-40°C to +85°C
			Frequency Stability	± 0.075 ~ ± 0.2 ppm	± 0.15 ~ ± 0.3 ppm	± 0.25 ~ ± 0.5 ppm
			5.0	+5.0 V : 1.25 MHz to 100.0 MHz		
				AT - cut Freq. Stability		
		Temperature Range		0°C to +60°C	-20°C to +70°C	-40°C to +85°C
		Frequency Stability		± 0.02 ~ ± 0.05 ppm	± 0.05 ~ ± 0.1 ppm	± 0.1 ~ ± 0.2 ppm
		SC - cut Freq. Stability				
		Temperature Range		0°C to +60°C	-20°C to +70°C	-40°C to +85°C
		Frequency Stability	± 0.01 ppm	± 0.01 ~ ± 0.02 ppm	± 0.3 ppm	
		12.0	+12.0 V 1.25 MHz to 100.0 MHz			
AT - cut Freq. Stability						
Temperature Range	0°C to +60°C		-20°C to +70°C	-40°C to +85°C		
Frequency Stability	± 0.02 ~ ± 0.05 ppm		± 0.05 ~ ± 0.1 ppm	± 0.1 ~ ± 0.2 ppm		
SC - cut Freq. Stability						
Temperature Range	0°C to +60°C		-20°C to +70°C	-40°C to +85°C		
Frequency Stability	± 0.01 ppm	± 0.01 ~ ± 0.02 ppm	± 0.3 ppm			

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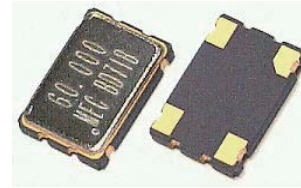
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### Applications

- CPU , Graphics , Multimedia A / V clocks
- MPEG / DVD / HDTV clocks
- Laser engine pixel / set - top clocks
- OC-3 , OC-12 , OC-48 and OC-192 clocks
- SONET / SDH / ATM clocks
- Fast Ethernet and Gigabit Ethernet clocks
- NTSC / PAL encoder / decoder clocks
- PLL / synthesizer clocks
- Fibre channel and ADSL clocks



RoHS Compliance



General Specifications [ TA = +25°C , V<sub>DD</sub>= at specified voltage , Load : 15 pF ]

Model		" H " series				
Input Voltage ( V <sub>DD</sub> )		+ 1.8V D.C.±5%	+ 2.5V D.C.±5%	+ 3.3 V D.C.±5%	+ 5.0V D.C.±10%	
		code is " 18 "	code is " 25 "	code is " 3 "	code is " 5 "	
Frequency Range		1.8 MHz ~ 60 MHz	0.3 MHz ~ 125 MHz	20KHz ~ 130 MHz		20 KHz ~ 160 MHz
Output Wave Form		CMOS		T T L / CMOS		T T L / CMOS
Output Logic High " 1 "	T T L			2.4 V ( min. )		2.4 V ( min. )
	CMOS	1.62 V ( min. )	2.25 V ( min. )	2.97 V ( min. )		4.5 V ( min. )
Output Logic Low " 0 "	T T L			0.4 V ( max. )		0.4 V ( max. )
	CMOS	0.18 V ( max. )	0.25 V ( max. )	0.33 V ( max. )		0.5 V ( max. )
Frequency Stability <sup>(1)</sup> Codes		Frequency Stability over Operating Temperature Range	± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " . For example : " C20 " ±20 ppm over -10°C to +70°C ; " I20 " ± 20 ppm over -40°C to +85°C
		Commercial ( -10°C to +70°C )	A	B	C	
		Industrial ( -40°C to +85°C )	D	E	F	
Output Load	T T L	2 ~ 10 (LS) T T L gates				
	CMOS	15 pF typical; 30 pF load for frequencies up to 70 MHz ; Contact Mercury for 50 pF load				
Rise Time ( Tr )	T T L	10 n sec.(max.) ; 3 n sec.(typical) . Measured between 0.4V <sub>DC</sub> ↔ 2.4V <sub>DC</sub> ( RL=390Ω ; CL = 15pF )				
Fall Time ( Tf )	CMOS	10 n sec.(max.) ; 3 n sec.(typical) . Measured between 10% to 90% wave form ( CL=15pF )				
Duty Cycle	T T L	40% (min.) , 60%(max.) Measured at +1.4V				
	CMOS	40% (min.) , 60%(max.) Measured at 50% of wave form [ 50% ± 5% is also available , add " S " at the end of the part number				
Start -Up Time (Ts)		10 m sec. (max.) ; 5 m sec. (typical)				
Current Consumption		10 ~ 45 mA ( frequency dependent )				
Storage Temperature		- 50°C to 100°C				
Aging		±5 ppm per year (max.)				
Tri-State Option.		Output is high impedance when " 0 " is applied to pin 1 . Disable time is 150 n sec. max. Add " T " in part number for Tri-State option				

H22

H32

H53

SWO

SMD

TTL / CMOS

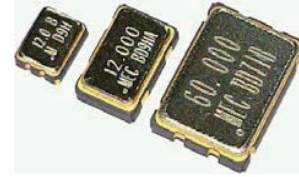
1.2  
V1.5  
V1.8  
V2.5  
V3.3  
V5.0  
VMin.  
25.0 KHzMax.  
160 MHz

## Applications

- CPU , Graphics , Multimedia A / V clocks
- MPEG / DVD / HDTV clocks
- Laser engine pixel / set - top clocks
- OC-3 , OC-12 , OC-48 and OC-192 clocks
- SONET / SDH / ATM clocks
- Fast Ethernet and Gigabit Ethernet clocks
- NTSC / PAL encoder / decoder clocks
- PLL / synthesizer clocks
- Fibre channel and ADSL clocks



RoHS Compliance

General Specifications [ TA = +25°C , V<sub>DD</sub>= at specified voltage , Load : 15 pF ]

Model	" H22 " ; " H32 " ; " H53 " and " SWO " series [ Output Logic : T T L / CMOS ]								
	" H22 " series		" H32 " series		" H53 " series		" SWO " series		
Dimensions	2.5 x 2.0 x 0.95 mm		3.2 x 2.5 x 1.0 mm		5.0 x 3.2 x 1.2 mm		7.0 x 5.0 x 1.4 mm		
Available Freq. Range by Voltage	1.2 V	312.0 KHz ~ 60.0 MHz	1.2 V	312.0 KHz ~ 60.0 MHz	1.2 V	312.0 KHz ~ 60.0 MHz	1.2 V	312.0 KHz ~ 60.0 MHz	
	1.5 V		1.5 V		1.5 V		1.5 V		
	1.8 V	156.0 KHz ~ 130.0 MHz	1.8 V	25.0 KHz ~ 101.0 KHz	1.8 V	156.0 KHz ~ 160.0 MHz	1.8 V	25.0 KHz ~ 101.0 KHz	
	2.5 V		2.5 V		2.5 V		2.5 V		
	3.3 V		3.3 V	3.3 V	3.3 V		3.3 V		
	5.0 V		5.0 V	5.0 V	5.0 V		5.0 V		
Supply Voltage ( V <sub>DD</sub> )	+ 1.2V D.C.±5%		+ 1.5V D.C.±5%		+ 1.8V D.C.±5%		+ 2.5V D.C.±5%		
	code is " 10 "		code is " 15 "		code is " 18 "		code is " 25 "		
High "1" ( 90% of V <sub>DD</sub> min. )	0.9 V min.		1.35 V min.		1.62 V min.		2.25 V min.		
Logic Low "0" ( 10% of V <sub>DD</sub> max. )	0.1 V max.		0.15 V max.		0.18 V max.		0.25 V max.		
Current Consumption	[ 1.0 ~ 1.5 MHz ] 4 mA max.		[ 0.3 ~ 1.5 MHz ] 4 mA max.		[ 1.0 ~ 1.5 MHz ] 5 mA max.		[ 0.3 ~ 1.5 MHz ] 5 mA max.		
	[ 1.5 ~ 20 MHz ] 4 mA max.		[ 1.5 ~ 20 MHz ] 4 mA max.		[ 1.5 ~ 20 MHz ] 8 mA max.		[ 1.5 ~ 20 MHz ] 8 mA max.		
	[ 20.0 ~ 50.0 MHz ] 4 mA max.		[ 20.0 ~ 50.0 MHz ] 4 mA max.		[ 20.0 ~ 50.0 MHz ] 15 mA max.		[ 20.0 ~ 50.0 MHz ] 15 mA max.		
	[ 50.1 ~ 160.0 MHz ] 12 mA max.		[ 50.1 ~ 160.0 MHz ] 12 mA max.		[ 50.1 ~ 160.0 MHz ] 22 mA max.		[ 50.1 ~ 160.0 MHz ] 35 mA max.		
Tri-state Function on pad No. 1	Pad No. 1	Tri - state		Tri - state		Tri - state		Tri - state	
	Package	H32 , H53 , SWO		H32 , H53 , SWO		H22 , H32 , H53 , SWO		H22 , H32 , H53 , SWO	
Rise Time ( Tr ) / Fall Time ( Tf )	6 n sec. ( max. )		6 n sec. ( max. )		7 n sec. ( max. )		7 n sec. ( max. )		
	Measured between 10% ↔ 90% of wave form ( CL = 15pF )								
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range		± 25 ppm		± 50 ppm		± 100 ppm		
	Commercial ( -10°C to +70°C )		A		B		C		
	Industrial ( -40°C to +85°C )		D		E		F		
Load	15 pF ; ( 30 pF and 50 pF load are also available for +3.3V and +5.0V V <sub>DD</sub> )								
Duty Cycle ( at 50% of wave form )	Standard: 50% ± 10% ; Option: 50% ± 5% . Please add "-S" at the end of the part number for ± 5% .								
Start -up Time ( Ts )	1.0 ~ 32.0 MHz : 5 m sec. ( max. ) ; 32.0 ~ 160.0 MHz : 10 m sec. ( max. )								
Storage Temperature	- 50°C to 100°C								
Aging	± 3 ppm per year ( max. )								

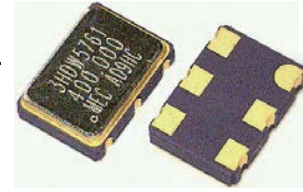
Note : <sup>(1)</sup> Inclusive of 25°C tolerance , operating temperature range , ±10% input voltage variation , load change , aging , shock and vibration.Mercury [www.mercury-crystal.com](http://www.mercury-crystal.com)

■ Taiwan : Tel (886)-2-2406-2779 / sales-tw@mercury-crystal.com ■ U.S.A: Tel: (1)-909-466-0427 / sales-us@mercury-crystal.com ■ China: Tel: (86)-512-5763-8100 / sales-cn@mecxtal.com

### Applications

For high frequency (>125.0 MHz) LVC MOS output clock oscillators, Mercury offers HF, HW and HW series.

- " HF " series: Best performance among the three series. Phase jitter is less than 1 ps.
- " HW " and " HV " use a high-Q fundamental crystal and a multiplier circuit for low cost applications.
- All have moderate jitter. HW is up to 800 MHz. HV series is up to 200 MHz .



### General specifications of all available packages , at Ta=+25°C , CL=15pF

Model	" HF " series		" HW " series			" HV " series	
Technology	High Q fundamental crystal + ultra low jitter multiplier circuit		High Q fundamental crystal + low jitter multiplier circuit			High Q fundamental crystal + low jitter multiplier circuit	
Output Logic	LVC MOS						
Available Frequency Range	125.01 MHz ~ 200.0 MHz ( 15 pF load ) 125.01 MHz ~ 320.0 MHz ( 10 pF load )		125.01 MHz ~ 800.0 MHz			125.01 MHz ~ 200.0 MHz	
Supply Voltage V <sub>DD</sub>	+ 2.5V D.C.±5%	+3.3 V <sub>DD</sub> ± 5%	+ 3.3 V D.C.±5%			+ 3.3 V D.C.±5%	
Supply Voltage Code	" 25 "	" 3 "	" 3 "			" 3 "	
Output Logic " High " , " 1 "	90% of V <sub>DD</sub> min.						
Output Logic " Low " , " 0 "	10% of V <sub>DD</sub> max.						
Integrated Phase Jitter (12 KHz to 20 MHz)	0.4 ps typical; 0.5 ps max. For 156.250 MHz		2.6 ps typical; 4 ps max. For 155.520 MHz			2.3 ps typical; 4 ps max. For 155.520 MHz	
Period Jitter RMS ; Decoupling capacitor between V <sub>DD</sub> and ground	3 ps typical; 5 ps max. For 156.250 MHz		4.3 ps typical. For 155.520 MHz			4.0 ps typical. For 155.520 MHz	
Period Jitter ( peak-to-peak ; Decoupling capacitor between V <sub>DD</sub> and ground )	20 ps typical; 30 ps max. For 156.250 MHz		27 ps typical. For 155.520 MHz			27 ps typical. For 155.520 MHz	
Current Consumption ( 15 pF load )	45 mA max.		45 mA max.			45 mA max.	
Rise Time / Fall Time	0.7 ns typical ( 0.3 V ↔ 3.0V, 15 pF load )		2.4 ns typical ( 0.3 V ↔ 3.0V, 15 pF load )			2.4 ns typical ( 0.3 V ↔ 3.0V, 15 pF load )	
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range		± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " For example : " C20 " ± 20 ppm over -10°C to +70°C ; " I20 " ± 20 ppm over -40°C to +85°C	
	Commercial ( -10°C to +70°C )		A	B	C		
	Industrial ( -40°C to +85°C )		D	E	F		
Load	15 pF						
Start-up Time	10 m sec. ( max. )						
Duty Cycle	50% ± 5% ( measured at 50% V <sub>DD</sub> )						
Input Static Discharge Protection	2 KV ( min. )						
Storage Temperature	-55°C to + 100°C						
Aging at Ta=+25°C	± 3 ppm max. first year ; ± 2 ppm max. per year thereafter						
Tri - State Function	5761 on pad No. 1	Output ( pad 4 ) is normal if Tri-state pad is no connection or connected to logic HIGH Output ( pad 4 ) is high impedance if Tri-state pad is connected to logic LOW.					
	5762 on pad No. 2						
Phase Noise ( typical )	Offset	Frequency: 156.250 MHz	Frequency: 155.520 MHz			Frequency: 155.520 MHz	
	10 Hz	-62 dBc / Hz	-65 dBc / Hz			-65 dBc / Hz	
	100 Hz	-92 dBc / Hz	-95 dBc / Hz			-95 dBc / Hz	
	1 KHz	-120 dBc / Hz	-120 dBc / Hz			-120 dBc / Hz	
	10 KHz	-132 dBc / Hz	-125 dBc / Hz			-128 dBc / Hz	
	100 KHz	-128 dBc / Hz	-121 dBc / Hz			-122 dBc / Hz	
	1 MHz	-140 dBc / Hz	-120 dBc / Hz			-120 dBc / Hz	
	10 MHz	-150 dBc / Hz	-140 dBc / Hz			-140 dBc / Hz	

<sup>(1)</sup> Inclusive of 25°C tolerance, operating temperature range, ±10% input voltage variation, load change, aging shock and vibration

# Clock Oscillators [ TTL / CMOS ]

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

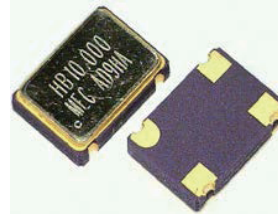
[ Please refer to page 3 for product series selections . ]

<p>[ H22 ]</p> <p>MEC</p> <p>1</p> <p>3</p> <p>4</p> <p>2</p> <p>1</p> <p>0.95 max</p> <p>Land Pattern</p> <p>1.625</p> <p>1.225</p> <p>0.9</p> <p>1.0</p> <p>Pad Connections :            Pad 1 : No Connection [ 1.0V , 1.2V ]            Tri - state [ 1.8V , 2.5V , 3.3V , 5.0V ]            Pad 2 : Ground            Pad 3 : Output            Pad 4 : Supply Voltage</p>	<p>[ H32 ]</p> <p>MEC</p> <p>1</p> <p>3</p> <p>4</p> <p>2</p> <p>1</p> <p>1.0 ± 0.1</p> <p>Land Pattern</p> <p>2.2</p> <p>1.75</p> <p>1.0</p> <p>1.2</p> <p>Pad Connections :            Pad 1 : No Connection [ 1.0V , 1.2V ]            Tri - state [ 1.8V , 2.5V , 3.3V , 5.0V ]            Pad 2 : Ground            Pad 3 : Output            Pad 4 : Supply Voltage</p>						
<p>[ H_53 ]</p> <p>MEC</p> <p>1</p> <p>3</p> <p>4</p> <p>2</p> <p>1</p> <p>1.2 ± 0.1</p> <p>Land Pattern ( reference )</p> <p>2.54</p> <p>1.5</p> <p>1.6</p> <p>2.5</p> <p>2.54</p> <p>1.0 ± 0.1</p> <p>3.2 ± 0.1</p> <p>5.0 ± 0.1</p> <p>Pad Connections :            Pad 1 : Enable / Disable            Pad 2 : Ground            Pad 3 : Output            Pad 4 : Supply Voltage</p>	<p>[ SWO 1 , H 57 1 ]</p> <p>MEC</p> <p>1</p> <p>3</p> <p>4</p> <p>2</p> <p>1</p> <p>1.4 ± 0.1</p> <p>Land Pattern ( reference )</p> <p>5.08</p> <p>1.8</p> <p>4.2</p> <p>5.08</p> <p>5.08</p> <p>1.0 ± 0.1</p> <p>7.0 ± 0.2</p> <p>5.0 ± 0.2</p> <p>Pad Connections :            Pad 1 : Enable / Disable            Pad 2 : Ground            Pad 3 : Output            Pad 4 : Supply Voltage</p>						
<p>[ H42 , HF42 , HW42 , HV42 ] , [ H43 , HF43 , HW43 , HV43 ]</p> <p>MEC</p> <p>1</p> <p>2</p> <p>3</p> <p>4</p> <p>1</p> <p>4</p> <p>5.08</p> <p>1.4 ± 0.1</p> <p>0.6</p> <p>H</p> <p>0.7</p> <p>1.3 ± 0.1</p> <p>7.0 ± 0.1</p> <p>1.0 ± 0.1</p> <p>9.6 ± 0.2</p> <p>11.4 ± 0.2</p> <p>Pad Connections :            Pad 1 : No connection            Pad 2 : Ground            Pad 3 : Output            Pad 4 : Supply voltage</p> <table border="1"> <thead> <tr> <th>MEC P/N</th> <th>H ( height )</th> </tr> </thead> <tbody> <tr> <td>H42</td> <td>2.5 ± 0.2</td> </tr> <tr> <td>H43</td> <td>3.0 ± 0.2</td> </tr> </tbody> </table>	MEC P/N	H ( height )	H42	2.5 ± 0.2	H43	3.0 ± 0.2	<p>[ HF5761 , HW5761 ] , [ HF5762 , HW5762 , HV5762 ]</p> <p>MEC</p> <p>1</p> <p>2</p> <p>3</p> <p>4</p> <p>5</p> <p>6</p> <p>4</p> <p>2.6 typ.</p> <p>Land Pattern</p> <p>5.08</p> <p>1.8</p> <p>4.2</p> <p>5.08</p> <p>5.08</p> <p>1.2 ± 0.1</p> <p>7.0 ± 0.2</p> <p>5.0 ± 0.2</p> <p>H = height            HF, HW: 1.9 mm max.            HV: 1.8 mm max.</p> <p>Pad Connections :            pad 1 : Tri - state ( H_5761 ) or No connection            pad 2 : Tri - state ( H_5762 ) or No connection            pad 3 : Ground            pad 4 : Output            pad 5 : No connection            pad 6 : Supply Voltage</p>
MEC P/N	H ( height )						
H42	2.5 ± 0.2						
H43	3.0 ± 0.2						
<p>[ H14 , HF14 , HW14 , HV14 ]</p> <p>MEC</p> <p>1</p> <p>7</p> <p>8</p> <p>14</p> <p>10.7 ± 0.1</p> <p>5.3 ± 0.1</p> <p>7.6 ± 0.1</p> <p>14</p> <p>15.2 ± 0.1</p> <p>6.0 ± 0.2</p> <p>6.3 max.</p> <p>0.8</p> <p>10.7 ± 0.1</p> <p>4-∅1.8 glass stand-off</p> <p>Pin Connections :            Pin 1 : (1) No connection            (2) Output disabled when low            Pin 7 : Ground            Pin 8 : Output            Pin 14 : Supply voltage</p>	<p>[ H8 , HF8 , HW8 , HV8 ]</p> <p>MEC</p> <p>1</p> <p>4</p> <p>5</p> <p>8</p> <p>7.6 ± 0.1</p> <p>7.6 ± 0.1</p> <p>5.5 ± 0.2</p> <p>6.3 max.</p> <p>0.8</p> <p>12.8 ± 0.2</p> <p>12.8 ± 0.2</p> <p>10.8</p> <p>10.8</p> <p>3-∅1.6 glass stand-off</p> <p>Pin Connections :            Pin 1 : (1) No connection            (2) Output disabled when low            Pin 4 : Ground            Pin 5 : Output            Pin 8 : Supply voltage</p>						

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Applications

- Short lead time. From 1 day to 1 week
- Low jitter. Peak-to-peak period jitter is 70 ps typical
- Low phase noise: -114 dBc/Hz at 1 KHz offset (133 MHz)
- Custom frequencies can easily be configured
- 1.8V, 2.5V or 3.3V supply voltages.



General specifications of all available packages , at Ta=+25°C , CL=15pF

Model	" HB " series				
Output Logic	LVCMOS				
Supply Voltage V <sub>DD</sub>	1.8 V <sub>DD</sub> ± 10%	+2.5 V <sub>DD</sub> ± 10%	+3.3 V <sub>DD</sub> ± 10%		
Available Frequency Range	1.0 ~ 110.0 MHz	1.0 ~ 166.0 MHz	1.0 ~ 200.0 MHz		
Output Logic " High " , " 1 "	90% of V <sub>DD</sub> min.				
Output Logic " Low " , " 0 "	10% of V <sub>DD</sub> max.				
Supply Current [ 25 MHz ]	2.5 mA max. [ 15pF ]	3.0 mA max. [ 15pF ]	4.0 mA max. [ 15pF ]		
PLL Off : Supply Current [ 25 MHz ]	2.0 mA max. [ 15pF ]	2.5 mA max. [ 15pF ]	4.0 mA max. [ 15pF ]		
Supply Current [ 200 MHz ]	8.0 mA max. [ 15pF ]	13.0 mA max. [ 15pF ]	20.0 mA max. [ 15pF ]		
PLL On : Supply Current [ 200 MHz ]	8.5 mA max. [ 15pF ]	12.5 mA max. [ 15pF ]	20.0 mA max. [ 15pF ]		
Rise Time / Fall Time	4.0 ns typ. [ 25.0 MHz , PLL off ] 1.5 ns typ. [ 200.0 MHz , PLL off ]	3.0 ns typ. [ 25.0 MHz , PLL off ] 1.5 ns typ. [ 200.0 MHz , PLL off ]	3.0 ns typ. [ 25.0 MHz , PLL off ] 1.5 ns typ. [ 200.0 MHz , PLL off ]		
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range	± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " For example : " C20 " ± 20 ppm over -10°C to +70°C ; " I20 " ± 20 ppm over -40°C to +85°C
	Commercial ( -10°C to +70°C )	A	B	C	
	Industrial ( -40°C to +85°C )	D	E	F	
Load	15 pF				
Start-up Time	5 m sec. ( max. )				
Duty Cycle	50% ± 5% ( measured at 50% V <sub>DD</sub> )				
Input Static Discharge Protection	2 KV ( min. )				
Tri-state Function on pad No. 1	Tri-state on pad 1 is standard for SWO, H53, H32 and H22 series. Output ( pad 3 ) is normal if pad 1 is no connection or connected to logic HIGH Output ( pad 3 ) is high impedance if pad 1 is connected to logic LOW. Disable time is 150 n sec. max. Enable time is 150 n sec max.				
Storage Temperature	-55°C to + 150°C				
Aging at Ta=+25°C	± 3 ppm max. first year ; ± 2 ppm max. per year thereafter				

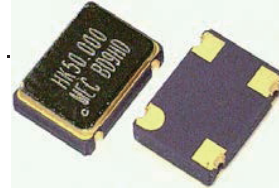
<sup>(1)</sup> Inclusive of 25°C tolerance, operating temperature range, ±10% input voltage variation, load change, aging shock and vibration

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

[ HB32 ]	[ HB53 ]	[ HB57 ]
<p>Pin connections : pin 1 : Enable / Disable pin 2 : Ground pin 3 : Output pin 4 : Supply Voltage</p>	<p>Pin connections : pin 1 : Enable / Disable pin 2 : Ground pin 3 : Output pin 4 : Supply Voltage</p>	<p>Pin connections : pin 1 : Enable / Disable pin 2 : Ground pin 3 : Output pin 4 : Supply Voltage</p>

Applications

- Designed for hand-held consumer electronic devices requiring a low current consumption .
- Less than 1.7 mA current consumption for 27 MHz at 1.8V and less than 2 uA at disabled mode .
- Phase noise is -130 dBc/Hz at 10 KHz offset .
- 1.8 V, 2.5V or 3.3V supply voltage

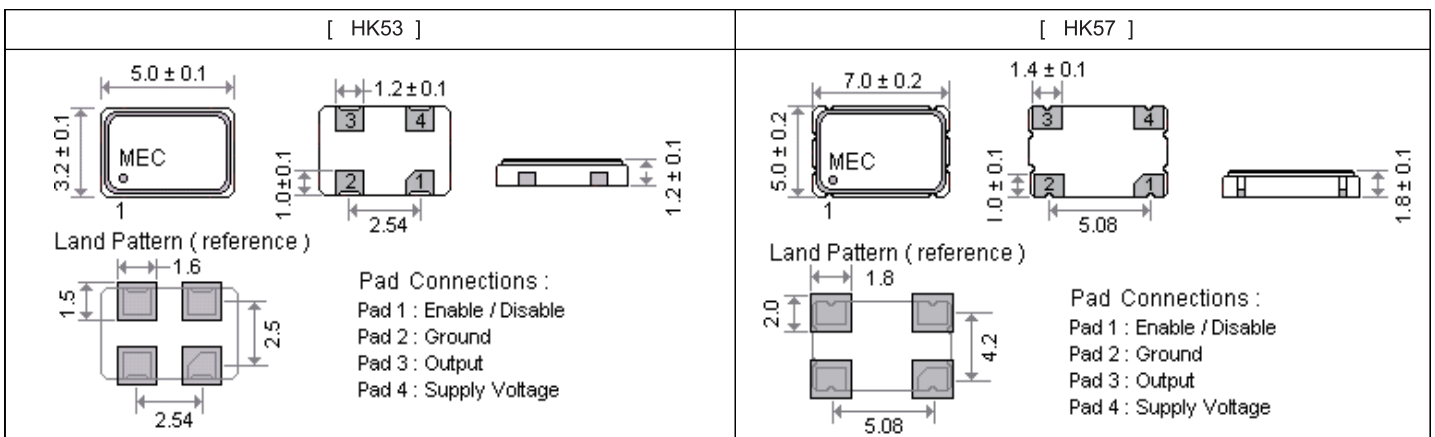


General specifications of all available packages , at Ta=+25°C , CL=15pF

Model	HK53			HK57			
Frequency Range	0.37 ~ 50.0 MHz			0.25 ~ 50.0 MHz			
Input Voltage Range ( V <sub>DD</sub> )	+1.8 V ± 10%	+2.5 V ± 10%	+3.3 V ± 10%	+1.8 V ± 10%	+2.5 V ± 10%	+3.3 V ± 10%	
Output Voltage High " 1 " ( V <sub>OH</sub> )	+ 1.4 V ( min. )	+ 2.0 V ( min. )	+ 2.4 V ( min. )	+ 1.4 V ( min. )	+ 2.0 V ( min. )	+ 2.4 V ( min. )	
Output Voltage Low " 0 " ( V <sub>OL</sub> )	+ 0.2 V ( max. )	+ 0.3 V ( max. )	+ 0.4 V ( max. )	+ 0.2 V ( max. )	+ 0.3 V ( max. )	+ 0.4 V ( max. )	
Current Consumption mA , ( typical )	10.000 MHz	---	---	0.7 mA	1.0 mA	1.5 mA	
	13.500 MHz	---	---	0.9 mA	1.3 mA	1.9 mA	
	16.000 MHz	1.1 mA	1.6 mA	2.2 mA	1.1 mA	1.6 mA	2.2 mA
	20.945 MHz	1.4 mA	1.9 mA	2.6 mA	1.4 mA	1.9 mA	2.6 mA
	25.000 MHz	1.6 mA	2.2 mA	3.1 mA	1.6 mA	2.2 mA	3.1 mA
	27.000 MHz	1.7 mA	2.4 mA	3.3 mA	1.7 mA	2.4 mA	3.3 mA
	30.000 MHz	2.0 mA	2.8 mA	3.8 mA	2.0 mA	2.8 mA	3.8 mA
	38.000 MHz	2.3 mA	3.3 mA	4.5 mA	2.3 mA	3.3 mA	4.5 mA
50.000 MHz	2.7 mA	4.0 mA	5.0 mA	2.7 mA	4.0 mA	5.0 mA	
Rise Time ( Tr ) / Fall Time ( Tf )	4 n sec. ( typical ) when measured from ( 10% V <sub>DD</sub> ↔ 90% V <sub>DD</sub> )						
Fanout ( Drive Capability )	12 mA ( typical )						
Duty Cycle ( at 50% of wave form )	50% ± 5% . measured at +1.4V V <sub>D.C.</sub>						
Start -up Time ( Ts )	10 m sec. ( typical ) ; V <sub>DD</sub> reaches 1.62 V						
Load	15 pF						
Voltage Sensitivity	± 0.8 ppm ( typical ) with 10% variation of V <sub>DD</sub>						
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range	± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " . For example : " C20 " ± 20 ppm over -10°C to +70°C ; " I20 " ± 20 ppm over -40°C to +85°C		
	Commercial ( -10°C to +70°C )	A	B	C			
	Industrial ( -40°C to +85°C )	D	E	F			
Storage Temperature	- 50°C to 100°C						
Aging	± 5 ppm per year ( max. )						
Tri-state Function on pad No. 1	When connected to ground : Output is disabled ( oscillator is off ) When not connected or connected to logic high : Clock output Disable time is 10 m sec. ( typical ) Enable time ( when ground is removed from pad 1 ) is 10 m sec. max.						

Note : <sup>(1)</sup> Inclusive of 25°C tolerance , operating temperature range , ±10% input voltage variation , load change , aging , shock and vibration.

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs [ Please check the hold type and part No. with page 3 . ]



# Clock Oscillators [ T T L / CMOS ]

## Part Number Format and Examples

		[ 1 ]	[ 2 ]		[ 4 ]	[ 5 ]		[ 6 ]
		Supply Voltage	Holder Type	G	Frequency Stability	T		Center Frequency
Examples	(1)	18	SWO	-	B	T	-	25.000
	(2)	5	H14	G	-	C30	-	10.000
	(3)	3	HDW5761	-	E	-	-	156.250

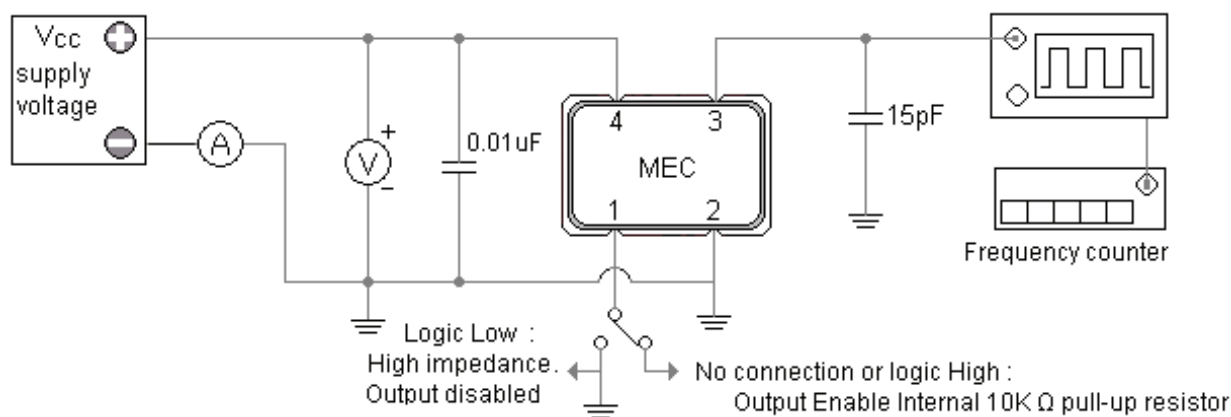
Ex (1): 18SWO - BT - 25.000 [ 1.8V , SWO type , ±50ppm from -10°C to 70°C , Tri-state , 25.000MHz , ]

Ex (2): 5H14G - C30 - 10.000 [ 5.0V , H14 type , RoHS , ±30ppm from -10°C to 70°C , 10.000MHz ]

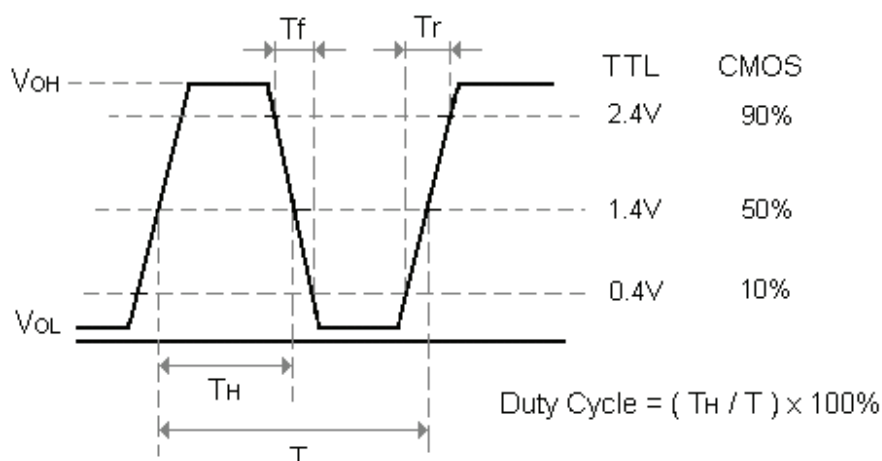
Ex (3): 3HDW621G - E - 156.250 [ 3.3V , ( HDW62 type , Tri-state on pin 1 ) , RoHS , ±50ppm from -40°C to 85°C , 156.250MHz ]

[1]	Supply voltage , " 1 " for +1.0V ; " 12 " for +1.2V ; " 18 " for +1.8V ; " 25 " for +2.5V ; " 28 " for +2.8V ; " 3 " for +3.3V ; " 5 " for +5.0V
[2]	Holder Type
[3]	Please add "G" after the " type code " for RoHS compliant ( Does not apply to SWO , H_53 , H_32 , H22 , H_576_ , H_534 series )
[4]	-10°C ~ 70 °C " A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C " , for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85 °C " D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I " , for example " I20 " : represents ±20ppm over -40 to +85°C
[5]	" T " for Tri-state , Leave this space blank if no connection on pin 1 or pad 1 .
[6]	Frequency in MHz
	Assigned by Mercury if customer spec , (1) : S ---- duty cycle ± 5% , ex : " - S " ; (2) : 50p ---- output load 50pF , ex : " - 50p "

### T T L / HCMOS Square Wave Test Circuit



### T T L / HCMOS Output Wave Form



**Mercury** [www.mercury-crystal.com](http://www.mercury-crystal.com)

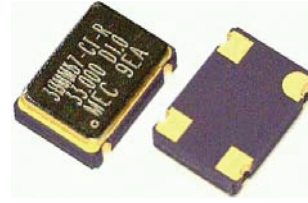
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■ U.S.A: Tel: (1)-909-466-0427 / sales-us@mercury-crystal.com
■ China: Tel: (86)-512-5763-8100 / sales-cn@mecxtal.com

# EMI Reduction Spread Spectrum Clock Oscillators

A Drop-in Replacement Solution For Your EMI / EMC Compliance Problem.

The principle sources of the EMI problems come from the system clocks. Therefore, rather than patching the problem with ferrite beads, EMI filters, ground plane and metal shielding, the most efficient and economical way to reduce the peak radiation energy is to use a spread spectrum clock oscillator.

Compared with conventional clock oscillator, Mercury's HM series spread spectrum (dithered) clock oscillator can reduce EMI as much as -12dB. The beauty is that it is a drop-in replacement for your existing 5x7mm or 5x3.2mm clock oscillator. No need to re-spin the board.



Clock Oscillators

3HM57 and 3HM53 reduces EMI and shortens time to market.

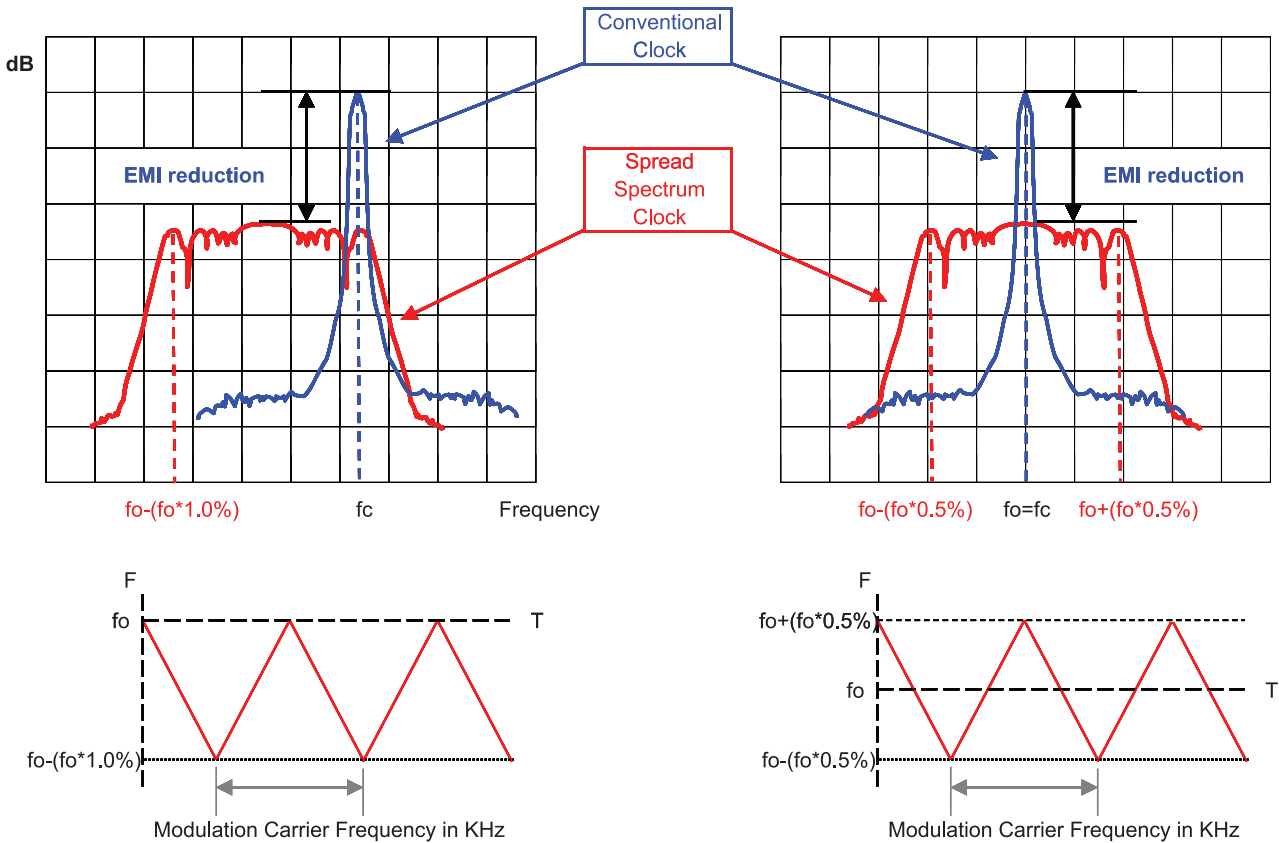
**Applications :**

- Printers; Multiple function printers (MPCs)
- Digital copiers; PDAs
- Networking; LAN / WAN; Routers
- Storage systems (CD-ROM, VCD, DVD and HDD)
- Scanner; Modems; projectors
- Hand-held ID readers
- Embedded systems; Electrical musical instrument
- Automotive; GPS car navigation systems
- LCD PC monitors / LCD TVs
- ADSL; PCMCIA
- Still Digital cameras (SDCs)

**Modulation Types :** [ Output amplitude (dB) vs frequency span (MHz) ]

Down spread "D". "D1" as an example

Center spread "C". "C0.5" as an example



**Spread Spectrum Clock (SSC) :**

Unlike a conventional clock, the mode energy of a spread spectrum clock is spread over a wider bandwidth, resulting from the **frequency modulation** technique. The modulation carrier frequency is in the KHz range which makes the modulation process transparent to the oscillator frequency. The controlled modulation process can be all on one side of the nominal frequency (**down spread**) or 50% higher and 50% lower (**center spread**) of the nominal frequency. Down spread is preferred if over-clocking is a problem to the system.

Mercury [www.mercury-crystal.com](http://www.mercury-crystal.com)

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General specifications of all available packages , at Ta=+25°C , CL=15pF

Group	R			Y				P		
Model , Frequency Range	(1) 3HM57 - R ( 7.0 * 5.0 * 1.8 mm ) ( 3.5 ~ 165.0 MHz )			(1) 3HM57 - Y ( 7.0 * 5.0 * 1.8 mm ) ( 8.0 ~ 165.0 MHz )				3HM57 - P ( 7.0 * 5.0 * 1.8 mm ) ( 13.0 ~ 220.0 MHz )		
	(2) 3HM53 - R ( 5.0 * 3.2 * 1.2 mm ) ( 6.0 ~ 160.0 MHz )			(2) 3HM53 - Y ( 5.0 * 3.2 * 1.2 mm ) ( 10.0 ~ 160.0 MHz )						
Spread Type	Total%	Down Spread	Center Spread	Type	Total%	Down Spread	Center Spread	Total%	Down Spread	Center Spread
Spread Percentage	0.5%	-0.5% ( D0.5 )	± 0.25% ( C0.25 )	3HM53	1.0%	-1.0% ( D1.0 )	± 0.5% ( C0.5 )	0.5%	-0.5% ( D0.5 )	± 0.25% ( C0.25 )
		Not available if Tri-state chosen			3.0%	-3.0% ( D3.0 )	± 1.5% ( C1.5 )	0.75%	-0.75% ( D0.75 )	± 0.375% ( C0.375 )
	1.0%	-1.0% ( D1.0 )	± 0.5% ( C0.5 )	3HM57	1.0%	-1.0% ( D1.0 )	± 0.5% ( C0.5 )	1.25%	-1.25% ( D1.25 )	± 0.625% ( C0.625 )
		3.0%	-3.0% ( D3.0 )		± 1.5% ( C1.5 )	2.0%	-2.0% ( D2.0 )	± 1.0% ( C1.0 )	2.0%	-2.0% ( D2.0 )
	3.0%	-3.0% ( D3.0 )	± 1.5% ( C1.5 )	3HM57	3.0%	-3.0% ( D3.0 )	± 1.5% ( C1.5 )	3.0%	-3.0% ( D3.0 )	± 1.5% ( C1.5 )
		3.75%	-3.75% ( D3.75 )		± 1.875% ( C1.875 )	3.75%	-3.75% ( D3.75 )	± 1.875% ( C1.875 )		
EMI Reduction ( EMI reduction applies to the whole spectrum. )	-7 dBc (min.) , 100MHz at C0.25 -9 dBc (min.) , 100MHz at C0.5 -15 dBc (min.) , 100MHz at C1.5 ( dBc : with respect to no modulation )			-9 dBc (min.) , 100MHz at C0.5 -12 dBc (min.) , 100MHz at C1.0 -15 dBc (min.) , 100MHz at C1.5 ( dBc : with respect to no modulation )				EMI reduction (dB) = 10 Log Total%"x"SSC Frequency (MHz)" / 0.12 See 125MHz example on next page		
Modulation Carrier Frequency ( Dither rate )	6.9 KHz (min.) ; 55.5 KHz (max.) Frequency dependent . Call for details			12 KHz (min.) ; 42 KHz (max.) Frequency dependent . Call for details				25.3 KHz (min.) ; 58.6 KHz (max.) Frequency dependent . Call for details		
Output Logic	CMOS ( Square Wave )			CMOS ( Square Wave )				CMOS ( Square Wave )		
Input Voltage ( V <sub>DD</sub> )	V <sub>DD</sub> = +3.3V D.C. ±5%			V <sub>DD</sub> = +3.3V D.C. ±5%				V <sub>DD</sub> = +3.3V D.C. ±5%		
Frequency Stability <sup>(1)</sup> Codes ( exclude modulation )	Frequency Stability over Operating Temperature Range			± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " represents . For example : " C20 " ± 20ppm over -10°C to +70°C ; " I20 " ± 20 ppm over -40°C to +85°C			
	Commercial ( -10°C to +70°C )			A	B	C				
	Industrial ( -40°C to +85°C )			D	E	F				
Output Logic " High " , " 1 "	2.4V (min.) ; 3.2V (typ.) [ at 90% V <sub>DD</sub> ]			2.0V (min.) ; 3.2V (typ.) [ at 90% V <sub>DD</sub> ]				2.4V (min.) ; [ at 80% V <sub>DD</sub> ]		
Output Logic " Low " , " 0 "	0.5V (max.) ; 0.2V (typ.) [ at 10% V <sub>DD</sub> ]			0.8V (max.) ; 0.2V (typ.) [ at 10% V <sub>DD</sub> ]				0.4V (max.) ; [ at 20% V <sub>DD</sub> ]		
Rise Time / Fall Time	4n sec. (max.) [ 10% V <sub>DD</sub> ↔ 90% V <sub>DD</sub> ]			4n sec. (max.) [ 10% V <sub>DD</sub> ↔ 90% V <sub>DD</sub> ]				1.3 n sec. (max.) [ 20% V <sub>DD</sub> ↔ 80% V <sub>DD</sub> ]		
Load	15pF			15pF				15pF		
Start-up Time	2 m sec. (typ.) ; 5 m sec. ( max. )			2 m sec. (typ.) ; 5 m sec. ( max. )				2 m sec. (typ.) ; 5 m sec. ( max. )		
Current Consumption	10.0 ~ 50.000 MHz : 10mA (typ.) 50.0 ~ 100.0 MHz : 18mA (typ.) 100.0 ~ 160.0 MHz : 35mA (typ.)			10.0 ~ 50.000 MHz : 10mA (typ.) 50.0 ~ 125.0 MHz : 27mA (typ.) , 44mA (max.)				25mA (typ.) ; Frequency dependent		
Duty Cycle	50% ± 5% ( CL=15pF ; at 50% V <sub>DD</sub> )			50% ± 5% ( CL=15pF ; at 50% V <sub>DD</sub> )				50% ± 5% ( CL=15pF ; at 50% V <sub>DD</sub> )		
Cycle-to-Cycle Jitter	±250 ps (typ.) ; ±300 ps (max.)			±100 ps (typ.) ; ±150 ps (max.)				±100 ps (typ.)		
Static Discharge Voltage	>2,000V ( per MIL-STD-883 , method 3015 )			>2,000V ( per MIL-STD-883 , method 3015 )				>2,000V ( per MIL-STD-883 , method 3015 )		
Storage Temperature	-65°C to + 150°C			-65°C to + 150°C				-65°C to + 150°C		
Aging	± 5 ppm per year (max.) ; Ta = +25°C			± 5 ppm per year (max.) ; Ta = +25°C				± 5 ppm per year (max.) ; Ta = +25°C		
Packaging	16mm tape and reel . 1000pcs per reel			16mm tape and reel . 1000pcs per reel				16mm tape and reel . 1000pcs per reel		
Pin 1 Option	Tri-State enable high. Output is high impedance when taken low . Enable / disable time: 100 ms max.							Do not make connection to this pad . No Tri-State option available.		

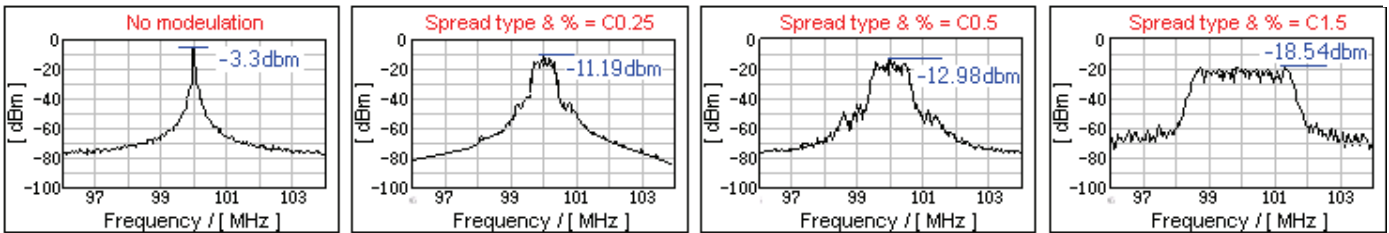
# EMI Reduction Spread Spectrum Clock Oscillators

## Part Number Format and Example

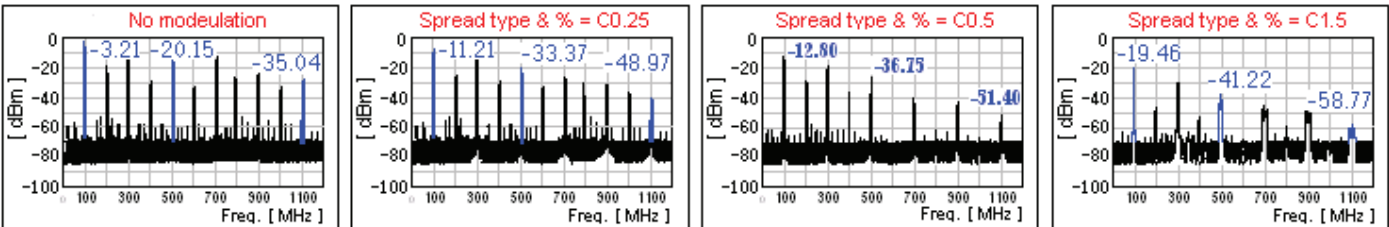
	[1]	[2]	[3]		[4]	[5]		[6]	[7]		[8]
	Supply Voltage	Holder Type	G	-	Frequency Stability	T	-	Center Frequency	Group Type	-	Spread type Percentage
Examples	(1) 3	HM 576		-	B	T	-	10.000	R	-	C1.5
	(2) 3	HM 53		-	F	T	-	75.000	Y	-	D1.0
	(3) 3	HM 14	G	-	C30		-	100.000	P	-	D3.0

[1]	Supply voltage , " 3 " for +3.3V ; " 5 " for +5.0V
[2]	Holder Type
[3]	Please add " G " after the " type code " for RoHS compliant ( Does not apply to HM53 , HM57 series ) .
[4]	-10°C ~ 70 °C " A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C " , for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85 °C " D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I " , for example " I20 " : represents ±20ppm over -40 to +85°C
[5]	" T " for Tri-state , Leave this space blank if no connection on pin 1 or pad 1 .
[6]	Frequency in MHz
[7]	Group " R " , " P " or " Y "
[8]	Spread type & percentage ; " C " for center spread , " D " for down spread

## EMI Test Data : 3HM57-B-100.000R , 100.0MHz Group " R " , Modulation Carrier Frequency = 34.687KHz



## Whole Spectrum EMI Data : 3HM57-B-100.000R , 100.0MHz Group " R " , Modulation Carrier Frequency = 34.687KHz

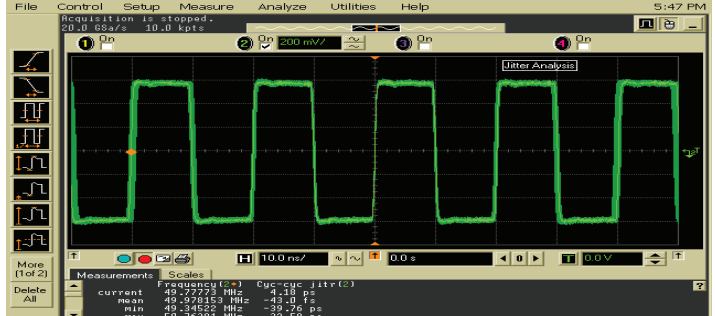
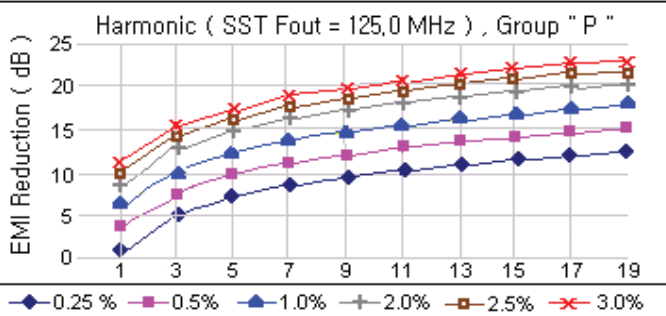


### EMI Test Data

125.0 MHz at various spread percentages.  
Modulation Carrier Frequency : 48.8 KHz

### Jitter Test Data

" P " group , cycle to cycle jitter .  
32.59 ps ( min. ) ; 39.76 ps ( max. )



**Main mode :**  

$$\text{EMI reduction (dB)} = 10 \text{ Log} \left( \frac{\text{total \%} * \text{frequency (MHz)}}{0.12} \right)$$
**3rd Harmonic :**  

$$\text{EMI reduction (dB)} = 10 \text{ Log} \left( \frac{\text{total \%} * \text{frequency (MHz)} * 3}{0.12} \right)$$
**5th Harmonic :**  

$$\text{EMI reduction (dB)} = 10 \text{ Log} \left( \frac{\text{total \%} * \text{frequency (MHz)} * 5}{0.12} \right)$$

Sample rate:20.0 G Sa/sec.;No. of sample :1000;Edge Direction: Rising edges

For more technical information please visit the following web site :

[www.mercury-crystal.com](http://www.mercury-crystal.com) and download our technical note

TN-020 ( Title : Low EMI Spread Spectrum Clock Oscillators )

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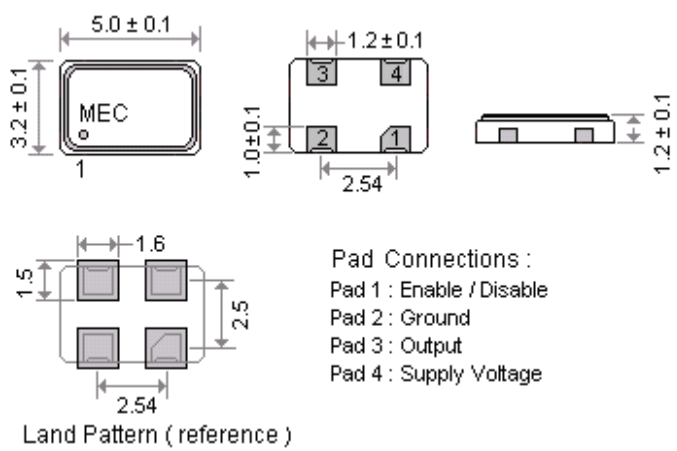
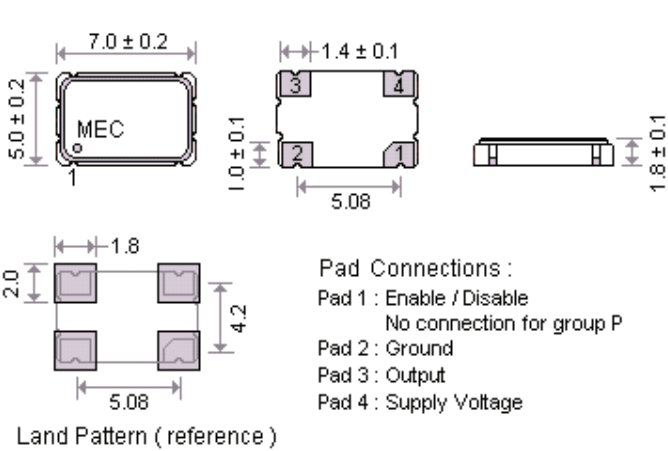
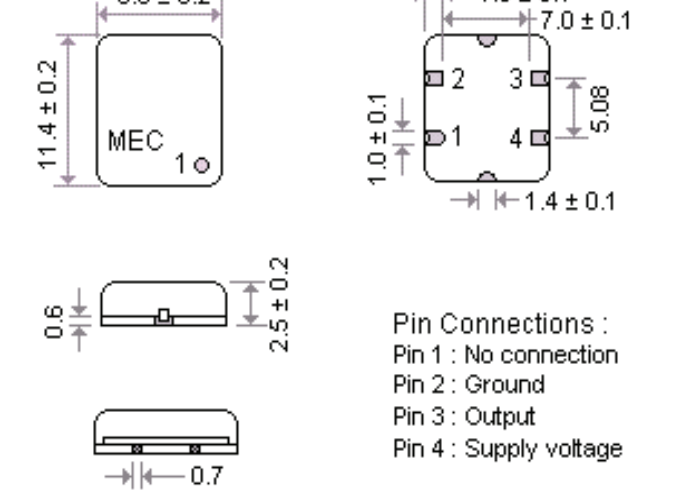
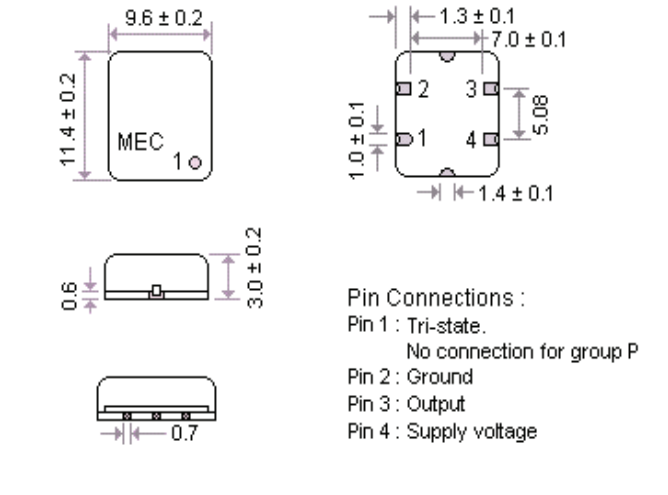
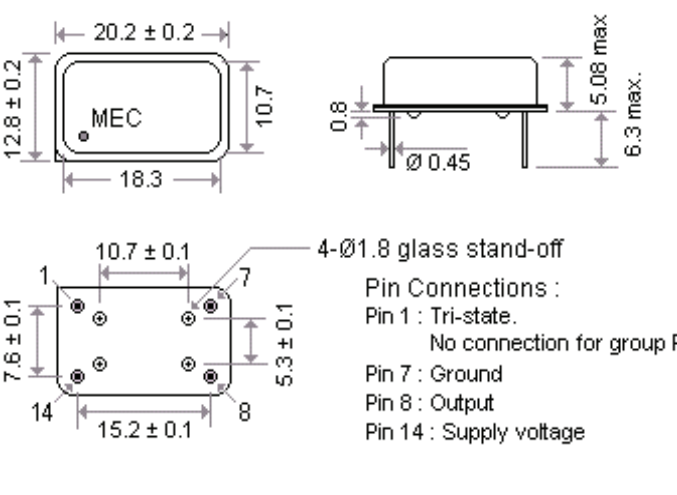
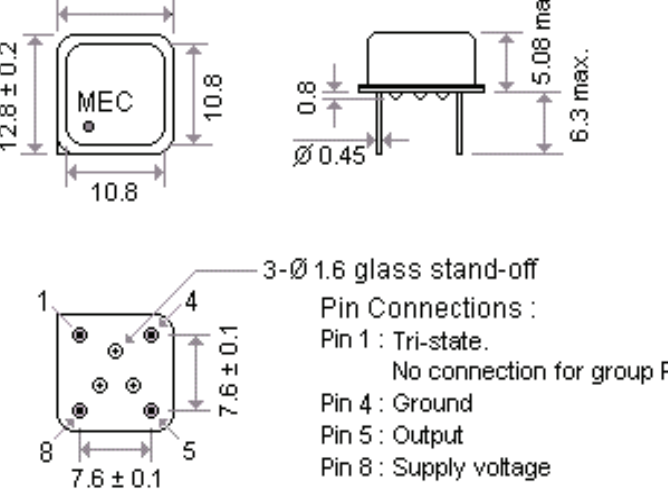
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# EMI Reduction Spread Spectrum Clock Oscillators

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

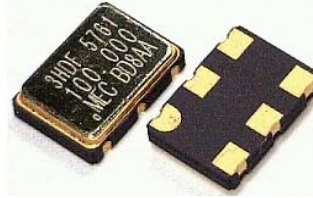
[ HM 53 ]	[ HM 57 ]
 <p>Pad Connections :            Pad 1 : Enable / Disable            Pad 2 : Ground            Pad 3 : Output            Pad 4 : Supply Voltage</p> <p>Land Pattern ( reference )</p>	 <p>Pad Connections :            Pad 1 : Enable / Disable            No connection for group P            Pad 2 : Ground            Pad 3 : Output            Pad 4 : Supply Voltage</p> <p>Land Pattern ( reference )</p>
[ HM 42 ]	[ HM 43 ]
 <p>Pin Connections :            Pin 1 : No connection            Pin 2 : Ground            Pin 3 : Output            Pin 4 : Supply voltage</p>	 <p>Pin Connections :            Pin 1 : Tri-state.            No connection for group P            Pin 2 : Ground            Pin 3 : Output            Pin 4 : Supply voltage</p>
[ HM 14 ]	[ HM 8 ]
 <p>4-Ø1.8 glass stand-off</p> <p>Pin Connections :            Pin 1 : Tri-state.            No connection for group P            Pin 7 : Ground            Pin 8 : Output            Pin 14 : Supply voltage</p>	 <p>3-Ø1.6 glass stand-off</p> <p>Pin Connections :            Pin 1 : Tri-state.            No connection for group P            Pin 4 : Ground            Pin 5 : Output            Pin 8 : Supply voltage</p>

Mercury [www.mercury-crystal.com](http://www.mercury-crystal.com)

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### Applications

- HPK femto second integrated phase jitter ( 200 fs typical , 12 KHz to 20 MHz ) .
- HPK superior phase noise ( -138 dBc/Hz at 10 KHz and -144 dBc/Hz at 100 KHz offset )
- 2.5 V or 3.3 V supply voltage .



RoHS Compliance

General specifications , at Ta=+25°C , CL=15pF

Model	HPK			HPF			HPW	
Output Logic	LVPECL Differential							
Supply Voltage V <sub>DD</sub>	+2.5 V <sub>DD</sub> ± 5%		+3.3 V <sub>DD</sub> ± 5%		+2.5 V <sub>DD</sub> ± 5%		+3.3 V <sub>DD</sub> ± 5%	
Supply Voltage Code	" 25 "		" 3 "		" 25 "		" 3 "	
Available Frequency Range	min.	13.5 MHz	10.0MHz		38.0 MHz			750 KHz
	max.	220.1MHz			640.0 MHz			800.0 MHz
Integrated Phase Jitter (12 KHz to 20 MHz)	0.2 ps typical; 0.5 ps max. For 156.250 MHz			0.4 ps typical; 0.5 ps max. For 156.250 MHz			2.6 ps typical; 4 ps max. For 155.520 MHz	
Current Consumption ( 15 pF load )	30 mA typical , 50 mA max.			38 MHz ~ 100 MHz: 65 mA max 100.01 MHz ~ 320 MHz: 80 mA max. 320.01 MHz ~ 640 MHz: 90 mA max..			< 24 MHz: 25 mA max 24.01 MHz ~ 96 MHz: 65 mA max. 96.01 MHz~700 MHz: 100 mA max..	
Rise Time / Fall Time ( 20%↔80% of the PECL wave form )	0.3 ns typical , 0.5 ns max.			0.4 ns typical , 0.6 ns max.			0.6 ns typical , 1.5 ns max.	
SSB Phase Noise [ dBc / Hz ( typical ) ]	Offset	62.5 MHz	156.250 MHz	Offset	156.250 MHz		Offset	155.520 MHz
	10 Hz	-50	-50	10 Hz	-62		10 Hz	-60
	100 Hz	-82	-80	100 Hz	-92		100 Hz	-90
	1 KHz	-116	-115	1 KHz	-120		1 KHz	-115
	10 KHz	-138	-135	10 KHz	-132		10 KHz	-125
	100 KHz	-144	-142	100 KHz	-128		100 KHz	-119
	1 MHz	-149	-147	1 MHz	-140		1 MHz	-120
	10 MHz	-155	-152	10 MHz	-150		10 MHz	-140
Output Logic " High " , " 1 "	V <sub>DD</sub> - 1.025 min. Termination: R <sub>L</sub> =50 Ω to ( V <sub>DD</sub> - 2.0V ). See test circuit below.							
Output Logic " Low " , " 0 "	V <sub>DD</sub> - 1.620 max. Termination: R <sub>L</sub> =50 Ω to ( V <sub>DD</sub> - 2.0V ). See test circuit below.							
Output Voltage Swing	595 mV min. , 750 mV typ. , 930 mV max.							
Load	50 Ω into Vcc - 2V or Thevenin equivalent							
Start-up Time	5.0 ms typical , 10 m sec. ( max.)							
Duty Cycle	50% ± 5% ( measured at V <sub>DD</sub> -1.25V )							
Input Static Discharge Protection	2 KV (min.)							
Storage Temperature	-55°C to + 150°C							
Aging at Ta = +25°C	± 3 ppm max. first year ; ± 2 ppm max. per year thereafter							
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range			± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " represents . For example : " C20 " ± 20 ppm over -10°C to +70°C ; " I20 " ± 20 ppm over -40°C to +85°C	
	Commercial ( -10°C to +70°C )			A	B	C		
	Industrial ( -40°C to +85°C )			D	E	F		
Tri - State Function. 5761 on pad No. 1 5762 on pad No. 2	No Connection	Differential PECL and complimentary PECL outputs .						
	Disable	Both outputs are disabled ( high impedance ) when the Tri-state pad taken below 0.45*Vcc referenced to ground ( threshold ) Oscillator is always On . Only buffer stage is disabled . Disable current : 50 uA max. ( at 0.0V ) , Disable time : 10 ns (max.)						
	Enable	At disabled mode , both outputs are enabled when Tri-state pad is taken above 0.45*Vcc referenced to ground ( threshold ) ; Enable time : 10ns + one period of the output frequency (max.)						

### Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

HPK 5361	HP_5761
<p>pad 1 : Tri - state pad 2 : No connections pad 3 : Ground pad 4 : Output pad 5 : Complimentary output pad 6 : Supply Voltage</p>	<p>pad 1 : Tri - state ( H_ 5761 ) pad 2 : No connections pad 3 : Ground pad 4 : Output pad 5 : Complimentary output pad 6 : Supply Voltage</p>

**HD****LVDS**  
Differential

K group

F group

W group

SMD

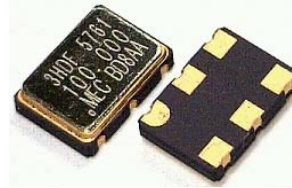
2.5V

3.3V

Min.  
750KHzMax.  
800MHz

## Applications

- HDK femto second integrated phase jitter ( 200 fs typical , 12 KHz to 20 MHz ) .
- HDK superior phase noise ( -138 dBc/Hz at 10 KHz and -144 dBc/Hz at 100 KHz offset )
- 2.5 V or 3.3 V supply voltage .



General specifications , at Ta=+25°C , CL=15pF

Model	HDK			HDF			HDW	
Output Logic	LVDS Differential							
Supply Voltage V <sub>DD</sub>	+2.5 V <sub>DD</sub> ± 5%		+3.3 V <sub>DD</sub> ± 5%		+2.5 V <sub>DD</sub> ± 5%		+3.3 V <sub>DD</sub> ± 5%	
Supply Voltage Code	" 25 "		" 3 "		" 25 "		" 3 "	
Available Frequency Range	min.	13.5 MHz	10.0MHz	38.0 MHz				750 KHz
	max.	220.1MHz			640.0 MHz			800.0 MHz
Integrated Phase Jitter (12 KHz to 20 MHz)	0.2 ps typical; 0.5 ps max. For 156.250 MHz			0.4 ps typical; 0.5 ps max. For 156.250 MHz			2.6 ps typical; 4 ps max. For 155.520 MHz	
Current Consumption ( 15 pF load )	16 mA typical , 27 mA max.			38 MHz ~ 100 MHz: 65 mA max 100.01 MHz ~ 320 MHz: 80 mA max. 320.01 MHz ~ 640 MHz: 90 mA max..			< 24 MHz: 25 mA max 24.01 MHz ~ 96 MHz: 65 mA max. 96.01 MHz~700 MHz: 100 mA max..	
Rise Time / Fall Time (20%↔80% of the PECL wave form )	0.2 ns typical , 0.4 ns max.			0.4 ns typical , 0.6 ns max.			0.6 ns typical , 1.5 ns max.	
SSB Phase Noise [ dBc / Hz ( typical ) ]	Offset	62.5 MHz	156.250 MHz	Offset	156.250 MHz	Offset	155.520 MHz	
	10 Hz	-50	-50	10 Hz	-62	10 Hz	-60	
	100 Hz	-82	-80	100 Hz	-92	100 Hz	-90	
	1 KHz	-116	-115	1 KHz	-120	1 KHz	-115	
	10 KHz	-138	-135	10 KHz	-132	10 KHz	-125	
	100 KHz	-144	-142	100 KHz	-128	100 KHz	-119	
	1 MHz	-149	-147	1 MHz	-140	1 MHz	-120	
	10 MHz	-155	-152	10 MHz	-150	10 MHz	-140	
Output Logic " High " , " 1 "	1.43V typical. ; 1.6V max. , RL = 100 Ω ,							
Output Logic " Low " , " 0 "	0.9V typical. ; 1.1V max. , RL = 100 Ω ,							
Output Voltage Swing	250 mV min. , 350 mV typ. , 450 mV max. , RL = 100 Ω ,							
Load	100 Ω between output and complimentary output							
Start-up Time	5.0 ms typical , 10 m sec. ( max.)							
Duty Cycle	50% ± 5% ( measured at V <sub>DD</sub> -1.25V )							
Input Static Discharge Protection	2 KV (min.)							
Storage Temperature	-55°C to + 150°C							
Aging at Ta = +25°C	± 3 ppm max. first year ; ± 2 ppm max. per year thereafter							
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range		± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " represents . For example : " C20 " ± 20 ppm over -10°C to +70°C ; " I20 " ± 20 ppm over -40°C to +85°C		
	Commercial ( -10°C to +70°C )		A	B	C			
	Industrial ( -40°C to +85°C )		D	E	F			
Tri - State Function. 5761 on pad No. 1 5762 on pad No. 2	No Connection	Differential PECL and complimentary PECL outputs .						
	Disable	Both outputs are disabled ( high impedance ) when the Tri-state pad taken below 0.45*Vcc referenced to ground ( threshold ) Oscillator is always On . Only buffer stage is disabled . Disable current : 50 uA max. ( at 0.0V ) , Disable time : 10 ns (max.)						
	Enable	At disabled mode , both outputs are enabled when Tri-state pad is taken above 0.45*Vcc referenced to ground ( threshold ) ; Enable time : 10ns + one period of the output frequency (max.)						

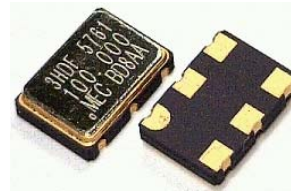
## Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

HCK5361	HD_5761
<p>pad 1 : Tri - state pad 2 : No connections pad 3 : Ground pad 4 : Output pad 5 : Complimentary output pad 6 : Supply Voltage</p>	<p>pad 1 : Tri - state ( H_5761 ) pad 2 : No connections pad 3 : Ground pad 4 : Output pad 5 : Complimentary output pad 6 : Supply Voltage</p>

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**HCK****HCSL**  
Differential**SMD****Jitter**  
0.2 ps**NON - PLL****K group****2.5V****3.3V**Min.  
**40 MHz**Max.  
**200 MHz****Applications**

- Femto second integrated phase jitter ( 200 fs typical , 12 KHz to 20 MHz ) .
- Superior phase noise ( -138 dBc/Hz at 10 KHz and -144 dBc/Hz at 100 KHz offset )
- High performance with surprisingly low price .
- 2.5 V or 3.3 V supply voltage .



General specifications , at Ta=+25°C , CL=15pF

Model		<b>HCK</b>					
Output Logic		<b>HCSL Differential</b>					
Available Frequency Range		13.5 MHz ~ 220.0 MHz					
Supply Voltage V <sub>DD</sub>		+2.5 V <sub>DD</sub> ± 5%		+3.3 V <sub>DD</sub> ± 5%			
Supply Voltage Code		" 25 "		" 3 "			
Integrated Phase Jitter ( 12 KHz to 20 MHz )		0.2 ps typical ; For 155.520 MHz					
Output Logic " High " : , " 1 " , V <sub>OH</sub>		660 mV min. , 740 mV typ. , 850 mV max.					
Output Logic " Low " , " 0 " , V <sub>OL</sub>		-150 mV min. , 0 mV typ. , 150 mV max.					
Output Voltage Swing		620 mV min. , 700 mV typ. , 780 mV max.					
Load		50 Ω to ground on each output					
Current Consumption ( 15 pF load )		< 90 MHz: 17 mA typ. , 27 mA max 90.1 MHz ~ 160 MHz : 25 mA max. 160.1 MHz ~ 200 MHz : 30 mA max..					
Rise Time / Fall Time ( RL=100 Ω , CL=10 pF )		0.15 ns typical , 0.4 ns max. ( 20%↔81% of the HCSL wave form )					
SSB Phase Noise [ dBc / Hz ( typical ) ]		Offset	125.0 MHz		156.250 MHz		
		10 Hz	-50		-50		
		100 Hz	-82		-80		
		1 KHz	-116		-115		
		10 KHz	-138		-135		
		100 KHz	-144		-142		
		1 MHz	-149		-147		
		10 MHz	-155		-152		
Start-up Time		5 ms typical ; 10 m sec. ( max. )					
Duty Cycle		50% ± 5%. Measured at 1.25V					
Storage Temperature		-55°C to + 105°C					
Aging at Ta = +25°C		± 3 ppm max. first year ; ± 2 ppm max. per year thereafter					
Frequency Stability <sup>(1)</sup> Codes		Frequency Stability over Operating Temperature Range		± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " represents . For example : " C20 " ± 20 ppm over -10°C to +70°C ; " I20 " ± 20 ppm over -40°C to +85°C
		Commercial ( -10°C to +70°C )		A	B	C	
		Industrial ( -40°C to +85°C )		D	E	F	
Tri - State Function. 5761 on pad No. 1 5762 on pad No. 2		No Connection	Differential output wave and complimentary wave form outputs .				
		Disable	Both outputs are disabled ( high impedance ) when the Tri-state pad is taken below 0.45*Vcc referenced to ground ( threshold ) Oscillator is always On . Only buffer stage is disabled . Disable current : 50 uA max. ( at 0.0V ) , Disable time : 10 ns ( max. )				
		Enable	At disabled mode , both outputs are enabled when tri-state pad is taken above 0.45 vcc referenced to ground ( threshold ) . Enable time : 10ns + one period of the output frequency ( max. )				

**Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs**

HCK5361	HCK5761
<p>pad 1 : Tri - state pad 2 : No connections pad 3 : Ground pad 4 : Output pad 5 : Complimentary output pad 6 : Supply Voltage</p>	<p>pad 1 : Tri - state ( H<sub>L</sub> 5761 ) pad 2 : No connections pad 3 : Ground pad 4 : Output pad 5 : Complimentary output pad 6 : Supply Voltage</p>

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Part Number Format and Example

[ 1 ]	[ 2 ]	[ 3 ]	[ 4 ]	[ 5 ]	[ 6 ]
Supply Voltage	Holder Type	1 or 2	G	Frequency Stability	Center Frequency

Example	(1)	3	HPF576	2	A	100.000
	(2)	3	HDW62	1	G	200.000

Ex (1): 3HPF5762 - A - 100.000 [ +3.3V , H\_ 576 type , PECL output , F characteristics , Tri-state on pad # 2 , ±25 ppm from -10°C to 70°C , 100.000MHz ]

Ex (2): 3HDW621 - E - 200.000 [ +3.3V , H\_ 576 type , LVDS output , W characteristics , Tri-state on pad # 1 , ±50 ppm from -40°C to 85°C , 200.000MHz ]

[ 1 ]	Supply voltage , " 3 " for +3.3V	
[ 2 ]	Holder Type	
[ 3 ]	" 1 " : Tri-state function on pad # 1 , " 2 " : Tri-state function on pad # 2	
[ 4 ]	Please add " G " after the " type code " for RoHS compliance , H_576_ is RoHS compliant and lead free product. Please omit " G ".	
[ 5 ]	-10°C ~ 70 °C	" A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C " , for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85 °C	" D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I " , for example " I20 " : represents ±20ppm over -40 to +85°C
[ 6 ]	Frequency in MHz	
	Assigned by Mercury if customer spec , (1) : S ---- duty cycle ± 5% , ex : " - S " ; (2) : 50p ---- output load 50pF , ex : " - 50p "	

<p>HCSL Square Wave Test Circuit</p>	<p>LVDS Square Wave Test Circuit</p>
<p>PECL Square Wave Test Circuit</p>	<p>LVDS Square Wave Output Wave Form</p>

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Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs [ Please refer to page 3 for product series selections. ]

<p>[ HPF5761 , HPW5761 ] , [ HDF5761 , HDW5761 ]</p> <p>Pad Connections :          pad 1 : Tri - state ( H_ 5761 )          pad 2 : No connections          pad 3 : Ground          pad 4 : Output          pad 5 : Complimentary output          pad 6 : Supply Voltage</p>	<p>[ HPF5762 , HPW5762 ] , [ HDF5762 , HDW5762 ]</p> <p>Pad Connections :          pad 1 : No connections          pad 2 : Tri - state ( H_ 5762 )          pad 3 : Ground          pad 4 : Output          pad 5 : Complimentary output          pad 6 : Supply Voltage</p>
<p>[ HPF621 , HPW621 ] , [ HDF621 , HDW621 ]</p> <p>Pad Connections :          Pad 1 : Tri - state          Pad 2 : No Connection          Pad 3 : Ground          Pad 4 : Output          Pad 5 : Complimentary Output          Pad 6 : Supply Voltage</p>	<p>[ HPF622 , HPW622 ] , [ HDF622 , HDW622 ]</p> <p>Pad Connections :          Pad 1 : No Connection          Pad 2 : Tri - state          Pad 3 : Ground          Pad 4 : Output          Pad 5 : Complimentary Output          Pad 6 : Supply Voltage</p>
<p>[ HPF14 , HPW14 ] , [ HDF14 , HDW14 ]</p> <p>Gull - wing SMD is also available .</p> <p>Pin Connections :          Pin 1 : Complimentary output          Pin 7 : Ground          Pin 8 : Output          Pin 14 : Supply voltage</p>	<p>[ HPF8 , HPW8 ] , [ HDF8 , HDW8 ]</p> <p>Gull - wing SMD is also available .</p> <p>Pin Connections :          Pin 1 : Complimentary output          Pin 4 : Ground          Pin 5 : Output          Pin 8 : Supply voltage</p>

**HS**

50Ω load

**HSR**

10KΩ//10pF load

True Sine Wave

Thru-Hole

SMD

2.8V

3.3V

5.0V

Min.

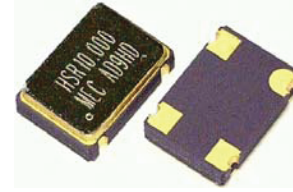
10MHz

Max.

52MHz

## Applications

- True Sine wave clock oscillators in 3.2x5.0 mm and 5.0x7.0 mm SMD. First in the market .
- High purity and low total harmonic distortion. Ideal for audio modulation applications .
- For VCXOs with sine wave output, please refer to GS series



RoHS Compliance

## General specifications of all available packages , at Ta=+25°C

Model	" HS " series			" HSR " series		
Load	50Ω. (Internally AC coupled)			10 KΩ // 10 pF load		
Output Wave Form	True Sine Wave					
Input Voltage ( V <sub>DD</sub> )	+3.3V D.C.±5%	+5.0V D.C.±10%	+2.8V D.C.±5%	+3.3V D.C.±5%	+5.0V D.C.±10%	
Frequency Range	10.0 ~ 800.0MHz	10.0 ~ 156.0MHz	10.0MHz ~ 52.0MHz			
Output Level	Standard: +3.0 dBm min. Tolerance: ± 1 dB Maximum Power: +7 dBm ( User to specify )	Standard: +5.0 dBm min. Tolerance: ± 1 dB Maximum Power: +13 dBm ( User to specify )	1.0 V p-p typical			
Current Consumption	10 MHz : 9 mA ( typ. ) 100 MHz : 18 mA ( typ. ) 150 MHz : 19 mA ( typ. )	10 MHz : 18 mA ( typ. ) 100 MHz : 34 mA ( typ. ) 150 MHz : 36 mA ( typ. )	1.0 mA	1.5 mA	1.2 mA	
Harmonics	< - 30dBc (frequency dependent)			< - 25dBc (frequency dependent)		
Start -up Time	6.0 m Sec.( typ. )			2.0 m Sec.( typ. )		
Storage Temperature	- 50°C to 100°C			- 55°C to 125°C		
Pin 1 option	Tri-state , Output disable when taken low			No Tri-state option		
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range	± 25 ppm	± 50 ppm	± 100 ppm	If non-standard please enter the desired stability after the " C " or " I " . represents . For example : " C20 " : ±20 ppm over -10°C to +70°C " I20 " : ±20 ppm over -40°C to +85°C	
	Commercial ( -10°C to +70°C )	A	B	C		
	Industrial ( -40°C to +85°C )	D	E	F		
Sub-Harmonics	None					
Aging	± 5 ppm per year (max.)					

Note : (1) Inclusive of 25°C tolerance , operating temperature range , ±10% input voltage variation , load change , aging , shock and vibration.

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# True Sine Wave Clock Oscillators [ ( HS series ) / ( HSR series ) ]

## Part Number Format and Example

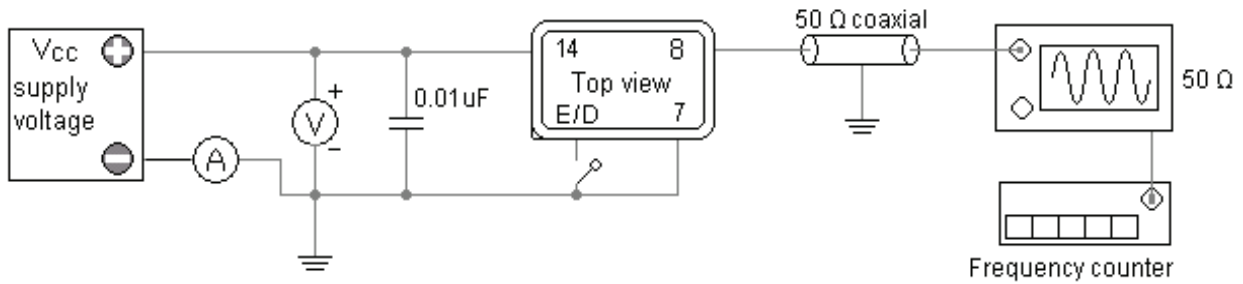
	[1]	[2]	[3]		[4]		[5]		[6]
	Supply Voltage	Holder Type	G	-	Frequency Stability	-	Center Frequency	-	Output Power [ HS series only ]
Example	(1) 3	HS14	G	-	A	-	100.000	-	5
	(2) 5	HSR53		-	E	-	20.000	-	

Ex (1) : 3HS14G - A - 100.000 - 5 [ +3.3V , True Sine wave , 50 Ω load , RoHS , ±25ppm from -10°C to 70°C , 100.000MHz , Output power is 5dBm ±1dB

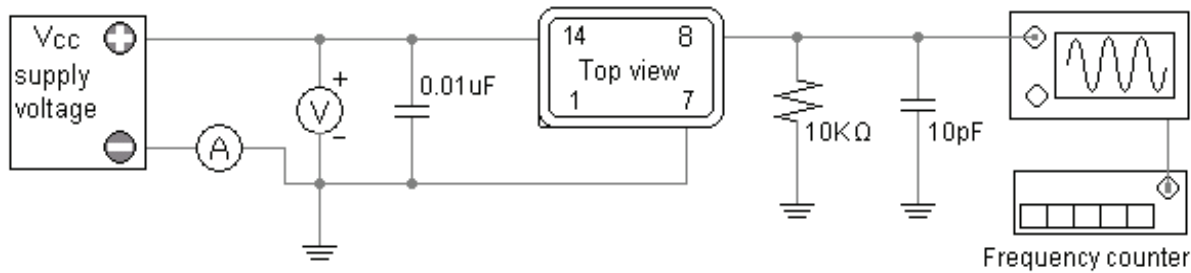
Ex (2) : 5HSR53 - E - 20.000 [ +5.0V , True Sine wave , 10 KΩ // 10 pF load , ±50ppm from -40°C to 85°C , 20.000MHz ]

[1]	Supply voltage , " 28 " for +2.8V ; " 3 " for +3.3V ; " 5 " for +5.0V	
[2]	Holder Type	
[3]	Please add " G " after the " type code " for RoHS compliance . Omit " G " if not required.	
[4]	-10°C ~ 70°C	" A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C " , for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85°C	" D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I " , for example " I20 " : represents ±20ppm over -40 to +85°C
[5]	Frequency in MHz	
[6]	Output power in dBm ( HS series only )	

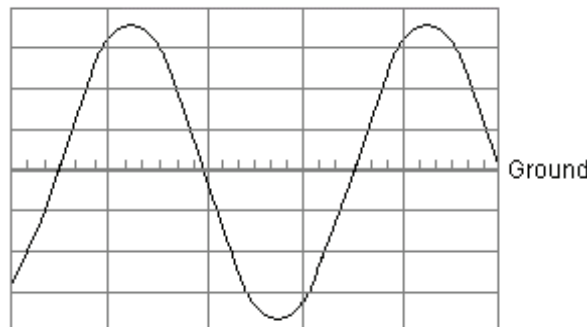
50 Ω Load Test Circuit : For " HS " series



10 KΩ // 10 pF Load Test Circuit : For " HSR " series



Output Wave Form



# True Sine Wave Clock Oscillators [ ( HS series ) / ( HSR series ) ]

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

<p>[ HSR57 ]</p> <p>Land Pattern ( reference )</p> <p>Pad Connections :          Pad 1 : No connection          Pad 2 : Ground          Pad 3 : Output          Pad 4 : Supply Voltage</p>	<p>[ HSR53 ]</p> <p>Land Pattern ( reference )</p> <p>Pad Connections :          Pad 1 : No connection          Pad 2 : Ground          Pad 3 : Output          Pad 4 : Supply Voltage</p>
<p>[ HSR42 ]</p> <p>Pin Connections :          Pin 1 : No connection          Pin 2 : Ground          Pin 3 : Output          Pin 4 : Supply voltage</p>	<p>[ HSR43 ]</p> <p>Pin Connections :          Pin 1 : No connection          Pin 2 : Ground          Pin 3 : Output          Pin 4 : Supply voltage</p>
<p>[ HS14 ] , [ HSR14 ]</p> <p>Pin Connections :          Pin 1 : No connection          Pin 7 : Ground          Pin 8 : Output          Pin 14 : Supply voltage</p>	<p>[ HSR 8 ]</p> <p>Pin Connections :          Pin 1 : No connection          Pin 4 : Ground          Pin 5 : Output          Pin 8 : Supply voltage</p>
<p>[ HS24 ] , [ HSR24 ]</p> <p>Pin Connections :          Pin 1 : No connection          Pin 7 : Ground          Pin 8 : Output          Pin 14 : Supply voltage</p>	<p>[ HSR18 ]</p> <p>Pin Connections :          Pin 1 : No connection          Pin 4 : Ground          Pin 5 : Output          Pin 8 : Supply voltage</p>

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# LPO

Low Power

CMOS

Thru-Hole

3.3V

5.0V

12V

15V

Min.  
1 Hz

Max.  
160 KHz

## Applications

- Frequency Range : Hz and KHz range using a tuning fork crystal.
- Current consumption:  $\mu\text{A}$  range
- LPO (Low Power Oscillator), such as 32.768 KHz, provides a time base for a real time clock.
- Low current consumption.
- Suitable for battery-operated devices such as data logging and portable test equipment.

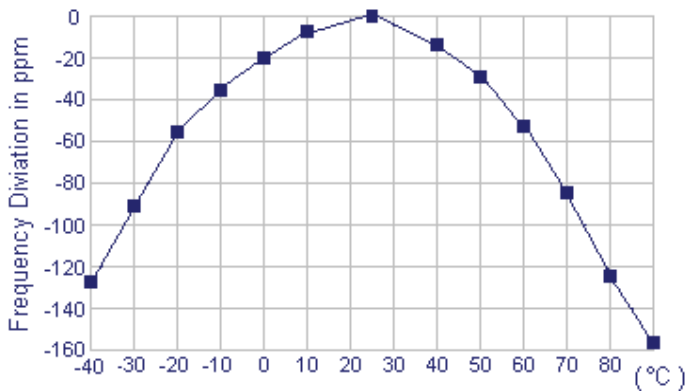


## General specifications of all available packages , at $T_a=+25^\circ\text{C}$ , $C_L=15\text{pF}$

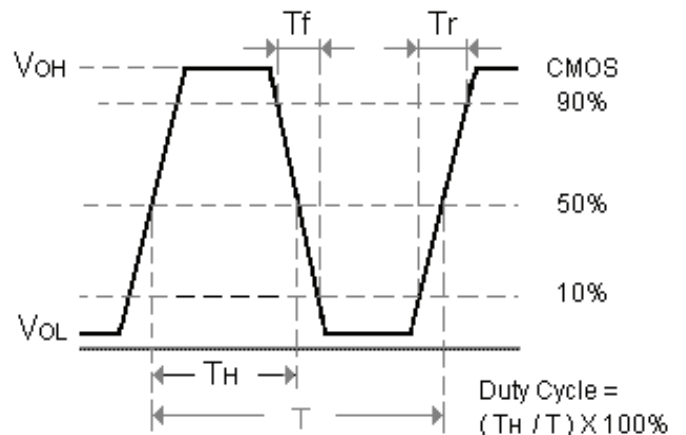
Model	LPO	
Input Voltage ( VDD )	+3.3V D.C.±5%	+5.0V D.C.±10%
	+3.0V to +15V is also available	
Frequency Range	1Hz to 160KHz	
Output Wave Form	CMOS ( square wave )	
Frequency Tolerance ( at $25^\circ\text{C}$ )	± 10 ppm ( Tolerance Code is " P " )	± 50 ppm ( Tolerance Code is " B " )
	± 25 ppm ( Tolerance Code is " A " )	± 100 ppm ( Tolerance Code is " C " )
Frequency Stability <sup>(1)</sup>	-100 ppm ( typ. ) over $0^\circ\text{C}$ to $+70^\circ\text{C}$	
	-160 ppm ( typ. ) over $-40^\circ\text{C}$ to $+85^\circ\text{C}$	
Current Consumption	26 $\mu\text{A}$ ( typ. )	45 $\mu\text{A}$ ( typ. )
Output Logic High " 1 "	2.97 V ( min. )	4.5 V ( min. )
Output Logic Low " 0 "	0.33V ( max. )	0.5 V ( max. )
Rise Time ( Tr ) & Fall Time ( Tf )	0.5 $\mu\text{sec}$ ( typ. ) ; 1.0 $\mu\text{sec}$ . ( max. )	25 $\mu\text{sec}$ ( typ. ) ; 50 $\mu\text{sec}$ . ( max. )
Fan-out	2 CMOS gates	
Start-up Time	450 m Sec.( max. )	
Duty Cycle	50%±5% ( typ. ) ; 50%±10% ( max. )	
Storage Temperature	$-50^\circ\text{C}$ to $100^\circ\text{C}$	
Aging	±5 ppm per year ( max. )	

Note : <sup>(1)</sup> Inclusive of  $25^\circ\text{C}$  tolerance , operating temperature range , ±10% input voltage variation , load change , aging , shock and vibration.

Typical Frequency Stability vs Temperature Curve



CMOS Output Wave Form



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# Low Power Clock Oscillator [ ( LPO series ) CMOS square wave ]

## Part Number Format and Example

[ 1 ]	[ 2 ]	[ 3 ]	[ 4 ]	[ 5 ]
Supply Voltage	Holder Type	G	Frequency Tolerance ( 25 °C )	Center Frequency

Example	(1) 3	LPO14	G	—	P	—	32.768K
	(2) 5	LPO8	G	—	A	—	25.600K

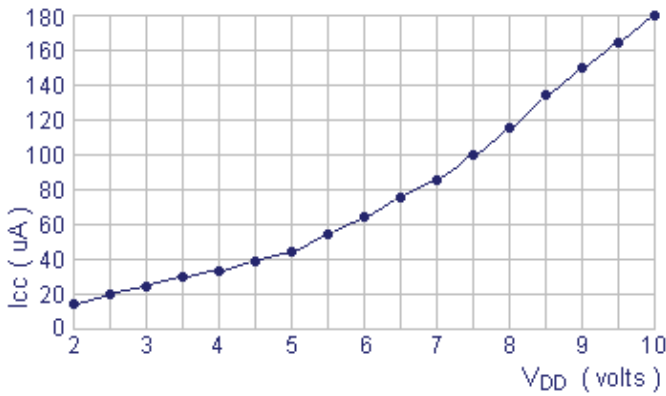
Ex (1) : 3LPO14G - P - 32.768K [ +3.3V input voltage , full size package 4 pins Dip type , RoHS compliant , ±10ppm frequency tolerance , 32.768 KHz ]

Ex (2) : 5LPO8G - A - 25.600K [ +5.0V input voltage , half size 4 pin package Dip type , RoHS compliant , ±25 ppm frequency tolerance , 25.600 KHz ]

[ 1 ]	Supply voltage , " 3 " for +3.3V ; " 5 " for +5.0V			
[ 2 ]	Holder Type			
[ 3 ]	Please add " G " after the " type code " for RoHS compliance . Omit " G " if not required.			
[ 4 ]	± 10 ppm ( Code is " P " )	± 25 ppm ( Code is " A " )	± 50 ppm ( Code is " B " )	± 100 ppm ( Code is " C " )
[ 5 ]	Frequency in KHz			

Current Consumption ( I<sub>cc</sub> ) vs Supply Voltage V<sub>DD</sub> , Measured with 10pF Load .

LPO 32.768 KHz model " 4 "



To calculate the frequency deviation at one particular temperature

To calculate the frequency deviation at one particular temperature (Tx) with respect to the frequency at +25°C:

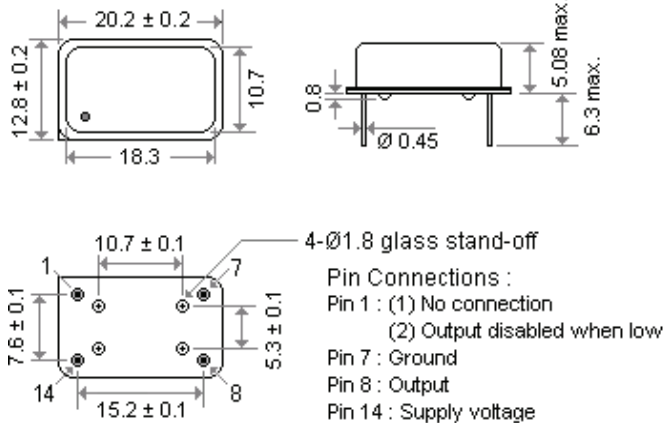
$$\Delta f / f (\text{ppm}) = -0.035 * (Tx - 25)^2$$

Example: At +60°C, the frequency drift is -42.8 ppm

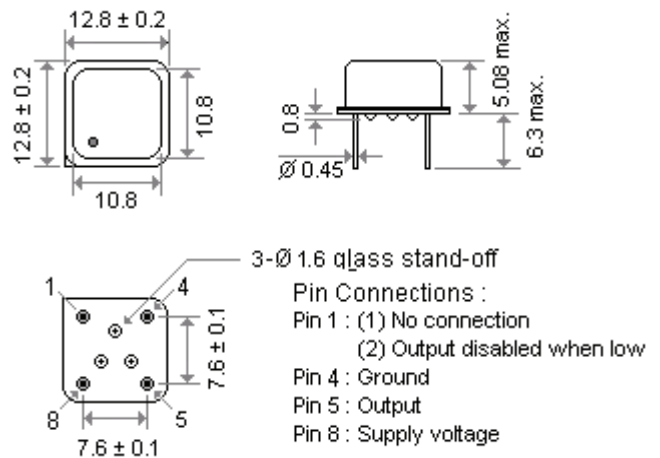
$$\Delta f / f (\text{ppm}) = -0.035 * (60 - 25)^2 = -42.8$$

## Outline Dimensions ( Unit : mm )

[ LPO 14 ]



[ LPO 8 ]



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# VCXO " G "

Thru-Hole

SMD

TTL / CMOS

1.8V

2.5V

3.3V

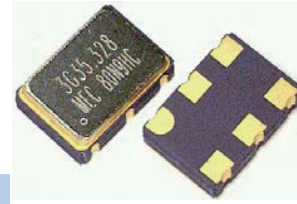
5.0V

Min.  
625KHz

Max.  
50MHz

Unlike regular clock oscillators that have a fixed output frequency, the output frequency of VCXOs (also known as " frequency modulators ") can be tuned  $\pm 50 \sim \pm 200$ ppm up or down from the nominal frequency by varying the control voltage on the voltage control pin . A varactor , a voltage variable capacitance tuning diode , is used to achieve this purpose .

Applications include ( PLL ) phase lock loop , SONET / ATM , set -top boxes , MPEG , audio -video modulations , video game consoles and HDTV sets. ONET , 10GbE , Fibre Channel , wireless repeaters , transponders , HDTV , FPGAs , data acquisition



RoHS Compliance

General Specifications of " G " series , [ TA = +25°C , V<sub>DD</sub>= at specified voltage , Load : 15 pF ]

Model		" G " series					
Input Voltage ( V <sub>DD</sub> )		V <sub>DD</sub> = +1.8V D.C.±5%	V <sub>DD</sub> = +2.5V D.C.±5%	V <sub>DD</sub> = +3.3V D.C.±5%	V <sub>DD</sub> = +5.0V D.C.±10%		
Frequency Range		16.0 MHz ~ 50.0 MHz ( Fundamental crystal used )	0.625 MHz ~ 50.0 MHz ( Fundamental crystal used )	0.625 MHz ~ 50.0 MHz ( Fundamental crystal used )	1.0 MHz ~ 50.0 MHz ( Fundamental crystal used )		
Output Wave Form		T T L / CMOS	T T L / CMOS	T T L / CMOS	T T L / CMOS		
Initial Freq. Accuracy ( at 25 °C )		To tune to the nomial frequency with Vc = 0.9V ± 0.15V	To tune to the nomial frequency with Vc = 1.25V ± 0.2V	To tune to the nomial frequency with Vc = 1.65V ± 0.2V	To tune to the nomial frequency with Vc = 2.5V ± 0.2V		
Output Logic High " 1 "	T T L	-----	-----	2.4 V ( min. )	2.4 V ( min. )		
	CMOS	1.62 V ( min. )	2.25 V ( min. )	2.97 V ( min. )	4.5 V ( min. )		
Output Logic Low " 0 "	T T L	-----	-----	0.4 V ( max. )	0.4 V ( max. )		
	CMOS	0.183 V ( max. )	0.25 V ( max. )	0.33 V ( max. )	0.5 V ( max. )		
Frequency Deviation Range		Standard : ± 80 ppm ( min. )	Standard : ± 80 ppm ( min. )	Standard : ± 80 ppm ( min. )	Standard : ± 80 ppm ( min. ) ; ± 200 ppm ( min. ) available		
Control Voltage Center		0.9 V <sub>DC</sub>	1.25 V <sub>DC</sub>	1.65 V <sub>DC</sub>	2.5 V <sub>DC</sub>		
Control Voltage Range		0.0V to 1.8V	0.25 V to 2.25 V	0.3V to 3.0V	0.5V to 4.5V		
Frequency Stability <sup>(1)</sup> Codes		Frequency Stability over Operating Temperature Range		± 25 ppm	± 50 ppm	± 100 ppm	
		Commercial " C " ( -10°C to +70°C )		A	B	C	If non-standard please enter the desired stability after the " C " or " I " For example : " C20 " : ± 20 ppm over -10°C to +70°C ; " I20 " : ± 20 ppm over -40°C to +85°C
		Industrial " I " ( -40°C to +85°C )		D	E	F	
Output Load	T T L	2 T T L gates					
	CMOS	15 pF					
Rise Time ( Tr )	T T L	6 nSec.(max.) ; 4 nSec.(typical) . Measured between 0.4V to 2.4V.					
Fall Time ( Tf )	CMOS	6 nSec.(max.) ; 4 nSec.(typical) . Measured between 20% to 80% of wave form ( CL=15pF )					
Duty Cycle	T T L	50% ± 10% (standard) , 50% ± 5% ( optional, add " - S " as suffix to part number ) . Measured at +1.4V.					
	CMOS	50% ± 10% (standard) , 50% ± 5% ( optional, add " - S " as suffix to part number ) . Measured at 50% of wave form.					
Integrated Phase Jitter		1 ps max. ( 12 KHz to 20 MHz ) .					
Periode Jitter RMS		2.0 pS ( typ. )					
Periode Jitter Peak-to-Peak		14 pS max.					
Phase Noise [ typical of 27.0 MHz ] [ at 3.3V ]	10 Hz	100 Hz	1k Hz	10K Hz	100K Hz	1 MHz	
	-40 dBc/Hz	-104 dBc/Hz	-132 dBc/Hz	-147 dBc/Hz	-152 dBc/Hz	-150 dBc/Hz	
Start-up Time ( Ts )		10 mSec. (max.) ; 5 mSec. (typical)					
Current Consumption		10 ~ 45 mA ( Frequency dependent). For 27 MHz: 10 mA typical at +3.3 V <sub>DD</sub> and 20 mA typical. at 5.0 V <sub>DD</sub> .					
Linearity		6% typical ; 10% max.					
Modulation Bandwidth		10 KHz min. Measured at -3 dB with Vcontrol at 1.65V or at 2.5V					
Input Impedance		1 MΩ typical					
Slope Polarity ( Transfer Function )		Monotonic and Positive : Increasing control voltage always increases output frequency ,					
Aging		± 3 ppm per year (max.)					
Tri - State Control Characteristics		Tri-State enable high. No connection or V <sub>DD</sub> -0.5V min. is applied to Tri-State pin to enable output. Ground+0.5V max. to disable output ( high impedance ).					

Note : <sup>(1)</sup> Inclusive of 25°C tolerance , operating temperature range , ±10% input voltage variation , load change , aging , shock and vibration.

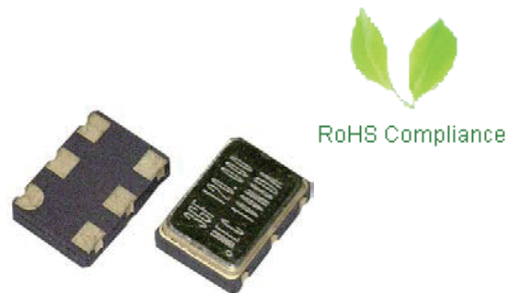
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<b>GF</b> phase jitter < 1.0 ps	<b>GW</b> phase jitter < 4.3 ps	<b>GV</b> phase jitter < 4.0 ps	Thru-Hole	SMD	TTL / CMOS	3.3V	Min. 50.01MHz	Max. 800MHz
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**Applications**

- GF, GW & GV CMOS VCXOs are available with frequencies above 50MHz.
- " GF " series: Best performance among the three. Phase jitter is less than 0.5 ps.
  - " GW " and " GV " high-Q fundamental crystals and multiplier circuits with moderate jitter.
- " GW " is available up to 800MHz and " GV " up to 200MHz and at a low cost .



**General specifications of GF, GW and GV series , at Ta=+25°C , CL=15pF**

Model	" GF " series		" GW " series		" GV " series	
Technology	High Q fundamental crystal + ultra low jitter multiplier circuit		High Q fundamental crystal + low jitter multiplier circuit		High Q fundamental crystal + low jitter multiplier circuit	
Output Logic	LVCMOS					
Available Frequency Range	50.01 MHz ~ 640.0 MHz		200.1 MHz ~ 800.0 MHz		50.01 MHz ~ 200.0 MHz	
Supply Voltage V <sub>DD</sub>	+3.3 V <sub>DD</sub> ± 5%		+3.3 V <sub>DD</sub> ± 5%		+3.3 V <sub>DD</sub> ± 5%	
Supply Voltage Code	" 3 "		" 3 "		" 3 "	
Output Logic High " 1 "	90% of V <sub>DD</sub> min.					
Output Logic Low " 0 "	10% of V <sub>DD</sub> max.					
Integrated Phase Jitter (12 KHz to 20 MHz)	0.4 ps typical; 0.5 ps max. for 156.250 MHz		2.6 ps typical; 4 ps max. for 155.520 MHz		2.3 ps typical; 4 ps max. for 155.520 MHz	
Period Jitter RMS ; Decoupling capacitor between V <sub>DD</sub> and	3 ps typical; 5 ps max. for 156.250 MHz		4.3 ps typical. for 155.520 MHz		4.0 ps typical. for 155.520 MHz	
Period Jitter ( peak-to-peak ; Decoupling capacitor between V <sub>DD</sub> and ground )	20 ps typical; 30 ps max. for 156.250 MHz		27 ps typical. for 155.520 MHz		27 ps typical. for 155.520 MHz	
Phase Noise ( typical ) , Vcon : GND	Offset	Frequency: 156.250 MHz	Frequency: 155.520 MHz		Frequency: 155.520 MHz	
	10 Hz	-62 dBc / Hz	-65 dBc / Hz		-65 dBc / Hz	
	100 Hz	-92 dBc / Hz	-95 dBc / Hz		-95 dBc / Hz	
	1 KHz	-120 dBc / Hz	-120 dBc / Hz		-120 dBc / Hz	
	10 KHz	-132 dBc / Hz	-125 dBc / Hz		-128 dBc / Hz	
	100 KHz	-128 dBc / Hz	-121 dBc / Hz		-122 dBc / Hz	
	1 MHz	-140 dBc / Hz	-120 dBc / Hz		-120 dBc / Hz	
	10 MHz	-150 dBc / Hz	-140 dBc / Hz		-140 dBc / Hz	
Current Consumption (15 pF load)	< 100 MHz : 30 mA max. 100 MHz ~ 320 MHz : 40 mA max.		200 MHz ~ 800 MHz : 50 mA max.		40 mA max.	
Rise Time / Fall Time	0.7 ns typical ( 0.3 V ↔ 3.0V, 15 pF load )		2.4 ns typical ( 0.3 V ↔ 3.0V, 15 pF load )		2.4 ns typical ( 0.3 V ↔ 3.0V, 15 pF load )	
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range		± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " For example : " C20 " : ± 20 ppm over -10°C to +70°C ; " I20 " : ± 20 ppm over -40°C to +85°C
	Commercial " C " ( -10°C to +70°C )		A	B	C	
	Industrial " I " ( -40°C to +85°C )		D	E	F	
Frequency Deviation range	Standard : ±80 ppm ( min. )					
Load	15 pF					
Start-up Time	10 m sec. ( max.)					
Duty Cycle	50% ± 5% ( measured at 50% V <sub>DD</sub> )					
Linearity	6% typical ; 10% max.					
Modulation Bandwidth (at -3 dB)	25 KHz min. 0V ≤ Vcontrol ≤ 3.3V					
Input Impedance	60 KΩ min.		2 MΩ min.		2 MΩ min.	
Slope Polarity (Transfer Function)	Monotonic and Positive : Increasing control voltage always increases output frequency ,					
Tri - State Control Characteristics	Tri-State enable high. No connection or 70% of V <sub>DD</sub> min. is applied to Tri-State pad to enable output. 30% of V <sub>DD</sub> max. to disable output ( high impedance ).					
Aging at Ta=+25°C	± 3 ppm max. first year ; ± 2 ppm max. per year thereafter					

<sup>(1)</sup> Inclusive of 25°C tolerance, operating temperature range, ±10% input voltage variation, load change, aging shock and vibration

# Voltage Controlled Crystal Oscillators [ CMOS output ]

## Part Number Format and Examples

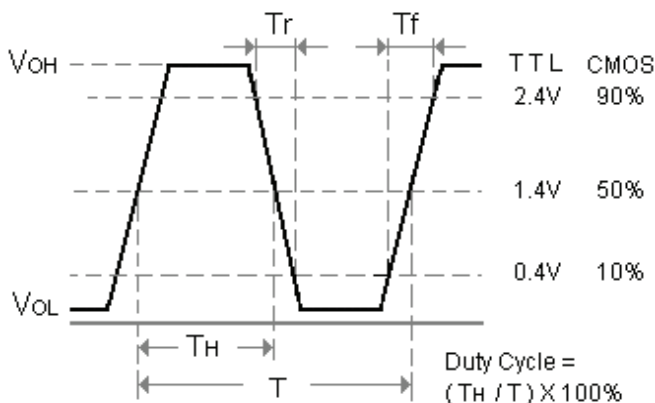
	[1]	[2]	[3]	[4]		[5]	[6]		[7]
	Supply Voltage	Holder Type	G	Frequency Stability	—	Pulling Range	Range Code	—	Center Frequency
Examples	(1) 5	G14	G	B	—	100	N	—	35.328
	(2) 3	GV576		D	—	80	T	—	27.000

Ex (1): 5G14GB - 100N - 35.328 [ +5.0V , full size 4 pin Dip type , RoHS , ±50ppm ( -10°C to 70°C ) , pulling : ±100 ppm ( min. ) , 35.328 MHz ]

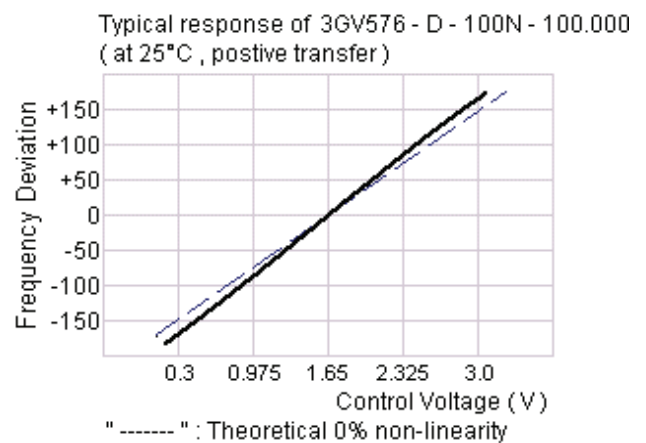
Ex (2): 3GV576D - 80T - 27.000 [ +3.3V , G\_576 type ( V characteristics ) , ±25ppm ( -40°C to 85°C ) , pulling : ±80 ppm ( typical ) , 27.000 MHz ]

[1]	Supply voltage , " 18 " for +1.8V ; " 25 " for +2.5V ; " 3 " for +3.3V ; " 5 " for +5.0V	
[2]	Holder Type	
[3]	Please add " G " after the " type code " for RoHS compliant equivalent ( Does not apply to G_534 , G_536 , G_576 series ).	
[4]	-10°C ~ 70°C	" A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C " , for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85°C	" D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I " , for example " I20 " : represents ±20ppm over -40 to +85°C
[5]	Frequency Pulling Range	3.3V From ±30ppm ~ ±150ppm , control Voltage range : 0.3V ~ 3.0 ; control voltage center : ± 1.65 V 5.0V From ±70ppm ~ ±200ppm , control Voltage range : 0.5V ~ 4.5V ; control voltage center : ± 2.5 V
	[6]	Pulling Range Code " M " stands for maximum ; " N " stands for minimum ; " T " stands for typical ( tolerance is ± 20% )
[7]	Center Frequency in MHz	

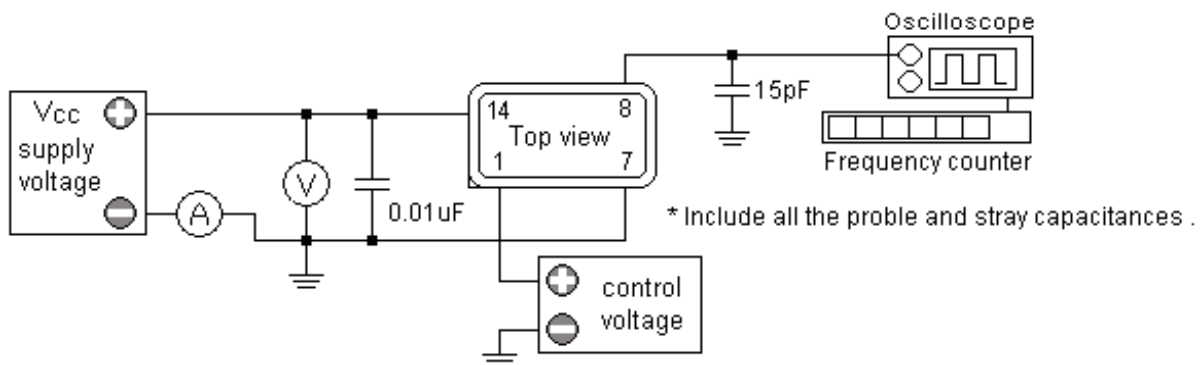
T T L / CMOS Output Wave Form



Transfer Function



T T L / CMOS Square Wave Test Circuit



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# Voltage Controlled Crystal Oscillators [ CMOS output ]

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

[ Please refer to page 5 for product series selections . ]

<p style="text-align: center;">[ G324 ]</p> <p style="text-align: center;">Pad Connections :                  Pad 1 : Control Voltage                  Pad 2 : Ground                  Pad 3 : Output                  Pad 4 : Supply Voltage</p>	<p style="text-align: center;">[ G534 ]</p> <p style="text-align: center;">Pad Connections :                  Pad 1 : Control Voltage                  Pad 2 : Ground                  Pad 3 : Output                  Pad 4 : Supply Voltage</p>						
[ G536 ] - - - ( available in 1.8 V , 3.3 V , 5.0 V only )	[ G576 , GV576 ]						
<p style="text-align: center;">Pad Connections :                  Pad 1 : Control Voltage                  Pad 2 : Tri - state                  Pad 3 : Ground                  Pad 4 : Output                  Pad 5 : No Connection                  Pad 6 : Supply Voltage</p>	<p style="text-align: center;">Pad Connections :                  Pad 1 : Control Voltage                  Pad 2 : Tri - state                  Pad 3 : Ground                  Pad 4 : Output                  Pad 5 : Tri - state                  Pad 6 : Supply Voltage</p> <p style="text-align: center;">[ Pad 2 and pad 5 are internally connected ]</p>						
[ G42 , GF42 , GW42 , GV42 ] ; [ G43 , GF43 , GW43 , GV43 ]	[ GF576 , GW576 ]						
<p style="text-align: center;">Pad Connections :                  Pad 1 : Control Voltage ( rounded pad )                  Pad 2 : Ground                  Pad 3 : Output                  Pad 4 : Supply voltage</p> <table style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th style="text-align: left;">MEC P/N</th> <th style="text-align: left;">H ( height )</th> </tr> </thead> <tbody> <tr> <td>G42</td> <td>→ 2,5 ± 0,2</td> </tr> <tr> <td>G43</td> <td>→ 3,0 ± 0,2</td> </tr> </tbody> </table>	MEC P/N	H ( height )	G42	→ 2,5 ± 0,2	G43	→ 3,0 ± 0,2	<p style="text-align: center;">Pad Connections :                  Pad 1 : Control Voltage                  Pad 2 : Tri - state                  Pad 3 : Ground                  Pad 4 : Output                  Pad 5 : No Connection                  Pad 6 : Supply Voltage</p>
MEC P/N	H ( height )						
G42	→ 2,5 ± 0,2						
G43	→ 3,0 ± 0,2						
[ G14 , GF14 , GW14 , GV14 ]	[ G8 , GF8 , GW8 , GV8 ]						
<p style="text-align: center; color: blue;">Gull - wing SMD is also available .</p> <p style="text-align: center;">Pin Connections :                  Pin 1 : Control Voltage                  Pin 7 : Ground                  Pin 8 : Output                  Pin 14 : Supply voltage</p>	<p style="text-align: center; color: blue;">Gull - wing SMD is also available .</p> <p style="text-align: center;">Pin Connections :                  Pin 1 : Control Voltage                  Pin 4 : Ground                  Pin 5 : Output                  Pin 8 : Supply voltage</p>						

VCXO

GP

LVPECL Differential

F group  
0.5 ps

W group  
4.0 ps

Thru-Hole

SMD

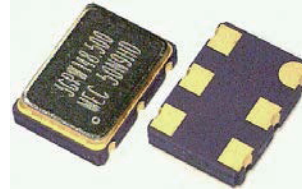
3.3V

Min.  
750KHz

Max.  
800MHz

Applications

- GPF and GPW uses high-Q fundamental crystals and low jitter multiplier circuits.
- GPF offers < 1 ps phase jitter at only a fraction of the cost of a high frequency fundamental crystal VCXO. GPW series has moderate jitter at a low cost.



RoHS Compliance

General specifications of GPF and GPW only , at Ta=+25°C , CL=15pF

Model	" GPF " series			" GPW " series		
Technology	High Q fundamental crystal + low jitter multiplier circuit			High Q fundamental crystal + multiplier circuit		
Output Logic	LVPECL Differential					
Available Frequency Range	38.0 MHz ~ 640.0 MHz			750 KHz ~ 800.0 MHz		
Supply Voltage V <sub>DD</sub>	+3.3 V <sub>DD</sub> ± 5%			+3.3 V <sub>DD</sub> ± 5%		
Supply Voltage Code	" 3 "			" 3 "		
Output Logic " High " , " 1 "	V <sub>DD</sub> -1.025 min. Termination: R <sub>L</sub> =50 Ω to (V <sub>DD</sub> -2.0V). See test circuit below.					
Output Logic " Low " , " 0 "	V <sub>DD</sub> -1.620 max. Termination: R <sub>L</sub> =50 Ω to (V <sub>DD</sub> -2.0V). See test circuit below.					
Integrated Phase Jitter (12 KHz to 20 MHz)	0.4 ps typical; 0.5 ps max. [ for 156.250 MHz ]			2.6 ps typical; 4 ps max. [ for 155.520 MHz ]		
Period Jitter (RMS ; Decoupling capacitor between V <sub>DD</sub> and ground )	3.0 ps typical; 5 ps max. [ for 156.250 MHz ]			4.3 ps typical. [ for 155.520 MHz ]		
Period Jitter ( peak-to-peak ;Decoupling capacitor between V <sub>DD</sub> and ground )	20 ps typical; 30 ps max. [ for 156.250 MHz ]			27 ps typical. [ for 155.520 MHz ]		
Current Consumption (15 pF load)	38 MHz ~ 100 MHz ----- 65 mA max 100.01 MHz ~ 320 MHz ----- 80 mA max. 320.01 MHz ~ 640 MHz ----- 90 mA max..			< 24 MHz ----- 25 mA max 24.01 MHz ~ 96 MHz ----- 65 mA max 96.01 MHz ~ 800 MHz ----- 100 mA max..		
Rise Time / Fall Time	0.55 ns max. ( 20%↔80% of the PECL wave form )			1.5 ns max. ( 20%↔80% of the PECL wave form )		
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range	± 25 ppm	± 50 ppm	± 100 ppm	If non-standard please enter the desired stability after the " C " or " I "	
	Commercial " C " ( -10°C to +70°C )	A	B	C	For example : " C20 " : ± 20 ppm over -10°C to +70°C ; " I20 " : ± 20 ppm over -40°C to +85°C	
	Industrial " I " ( -40°C to +85°C )	D	E	F		
Load	R <sub>L</sub> =50 Ω to (V <sub>DD</sub> -2.0V). See test circuit below.					
Start-up Time	10 m sec. ( max.)					
Duty Cycle	50% ± 5% ( measured at V <sub>DD</sub> -1.3V)					
Aging at Ta = +25°C	± 3 ppm max. first year ; ± 2 ppm max. per year thereafter					
Pad 1 Voltage Control Characteristics	Control Voltage Center , Range	+ 1.65 V , Vcon =+0.3V to +3.0V				
	Frequency Deviation Range	±80 ppm ( min.) . Use " N " ( minimum ) , " M " ( maximum ) , " T " ( typical,±20% ) for the desired range . Example : " 100M " represents ±120ppm ( min.) .				
	Linearity	6% typical ; 10% max.				
	Slope Polarity	Positive : Positive voltage for positive frequency shift				
	Modulation Bandwidth	25 KHz min. ( -3dB , 0V ≤ Vcontrol ≤ 3.3V )				
	Input Impedance	60 KΩ min.			2 MΩ min.	
Tri - State Function. on pad No. 2	No Connection	Differential PECL and complimentary PECL outputs .				
	Disable	Both outputs are disabled ( high impedance ) when pad No.2 is taken below 0.45*Vcc referenced to ground ( threshold ) Oscillator is always On . Only buffer stage is disabled . Disable current : 50 uA max. ( at 0.0V ) , Disable time : 10 ns (max.)				
	Enable	At disabled mode , both outputs are enabled when Tri-state pad is taken above 0.45*Vcc referenced to ground ( threshold ) ; Enable time : 10ns + one period of the output frequency (max.)				
Phase Noise : Tested with Vcontrol pin connected to ground ( typical )	Offset	Frequency: 156.250 MHz		Frequency: 155.520 MHz		
	10 Hz	-62 dBc / Hz		-60 dBc / Hz		
	100 Hz	-92 dBc / Hz		-90 dBc / Hz		
	1 KHz	-120 dBc / Hz		-115 dBc / Hz		
	10 KHz	-132 dBc / Hz		-125 dBc / Hz		
	100 KHz	-128 dBc / Hz		-119 dBc / Hz		
	1 MHz	-140 dBc / Hz		-120 dBc / Hz		
	10 MHz	-150 dBc / Hz		-140 dBc / Hz		

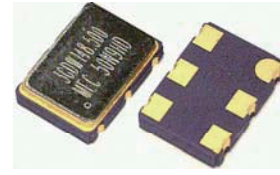
<sup>(1)</sup> Inclusive of 25°C tolerance, operating temperature range, ±10% input voltage variation, load change, aging shock and vibration

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## Applications

- GDF and GDW uses a high-Q fundamental crystal and a low jitter multiplier circuit.
- GDF offers < 1 ps phase jitter at only a fraction of the cost of a high frequency fundamental crystal VCXO. GDW series has moderate jitter at a low cost.



## General specifications of GDF and GDW only , at Ta=+25°C , CL=15pF

Model	" GDF " series				" GDW " series	
Technology	High Q fundamental crystal + low jitter multiplier circuit				High Q fundamental crystal + multiplier circuit	
Output Logic	LVDS					
Available Frequency Range	38.0 MHz ~ 640.0 MHz				750 KHz ~ 800.0 MHz	
Supply Voltage V <sub>DD</sub>	+3.3 V <sub>DD</sub> ± 5%				+3.3 V <sub>DD</sub> ± 5%	
Supply Voltage Code	" 3 "				" 3 "	
Output Logic " High " , " 1 "	1.4 V typical ; 1.6 V max.					
Output Logic " Low " , " 0 "	0.9 V min. ; 1.1 V typical.					
Differential Output Voltage, V <sub>OD</sub>	247 mV min.; 355 mV typical ; 454 mV max. Output 1 - output 2					
Differential Output Error, V <sub>OD</sub>	-50 mV min ; 50 mV max.					
Output Offset Voltage, Vos	1.125 V min. ; 1.200 V typical ; 1.375 V max.					
Offset Magnitude Error ( ΔVos )	0 mV min. ; 3 mV typical ; 25 mV max.					
Integrated Phase Jitter (12 KHz to 20 MHz)	0.4 ps typical; 0.5 ps max. [ for 156.250 MHz ]				2.6 ps typical; 4 ps max. [ for 155.520 MHz ]	
Period Jitter (RMS ; Decoupling capacitor between V <sub>DD</sub> and ground )	3.0 ps typical; 5 ps max. [ for 156.250 MHz ]				4.3 ps typical. [ for 155.520 MHz ]	
Period Jitter(peak-to-peak ;Decoupling capacitor between V <sub>DD</sub> and ground )	20 ps typical; 30 ps max. [ for 156.250 MHz ]				27 ps typical. [ for 155.520 MHz ]	
Current Consumption (15 pF load)	38 MHz ~ 100 MHz ----- 65 mA max 100.01 MHz ~ 320 MHz ----- 80 mA max. 320.01 MHz ~ 640 MHz ----- 90 mA max.				< 24 MHz ----- 25 mA max 24.01 MHz ~ 96 MHz ----- 65 mA max 96.01 MHz ~ 800 MHz ----- 100 mA max..	
Rise Time / Fall Time	0.7 ns typical , 1.0 ns max. ( 20%↔80% of the LVDS wave form )				1.5 ns max. ( 20%↔80% of the LVDS wave form )	
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range	± 25 ppm	± 50 ppm	± 100 ppm	If non-standard , please enter the desired stability after the " C " or " I " . For example : " C20 " : ± 20 ppm over -10°C to +70°C " I20 " : ± 20 ppm over -40°C to +85°C	
	Commercial "C" ( -10°C ~ +70°C )	A	B	C		
	Industrial " I " ( -40°C to +85°C )	D	E	F		
Load	50 Ω from each output					
Start-up Time	5 m sec. typical; 10 m sec. max.					
Duty Cycle	50% ± 5% ( measured at 1.25V )					
Drive Capability	100 ohms between LVDS output and complimentary LVDS output.					
Aging at Ta = +25°C	± 3 ppm max. first year ; ± 2 ppm max. per year thereafter					
Pad 1 Voltage Control Characteristics	Control Voltage Center , Range	+ 1.65 V , Vcon =+0.3V to +3.0V				
	Frequency Deviation Range	±80 ppm ( min. ) . Use " N " ( minimum ) , " M " ( maximum ) , " T " ( typical,±20% ) for the desired range . Example : " 100N " represents ±100ppm ( min. ) .				
	Linearity	6% typical ; 10% max.				
	Slope Polarity	Positive : Positive voltage for positive frequency shift				
	Modulation Bandwidth	25 KHz min. ( -3dB , 0V ≤ Vcontrol ≤ 3.3V )				
Input Impedance	60 KΩ min.				2 MΩ min.	
Tri - State Function. on pad No. 2	No Connection	Differential LVDS and complimentary LVDS outputs .				
	Disable	Both outputs are disabled ( high impedance ) when pad No.2 is taken below 0.45*Vcc referenced to ground ( threshold ) Oscillator is always On . Only buffer stage is disabled . Disable current : 50 uA max. ( at 0.0V ) , Disable time : 10 ns (max.)				
	Enable	At disabled mode , both outputs are enabled when Tri-state pad is taken above 0.45*Vcc referenced to ground ( threshold ) ; Enable time : 10ns + one period of the output frequency (max.)				
Phase Noise : Tested with Vcontrol pin connected to ground ( typical )	Offset	Frequency: 156.250 MHz			Frequency: 155.520 MHz	
	10 Hz	-62 dBc / Hz			-60 dBc / Hz	
	100 Hz	-92 dBc / Hz			-90 dBc / Hz	
	1 KHz	-120 dBc / Hz			-115 dBc / Hz	
	10 KHz	-132 dBc / Hz			-125 dBc / Hz	
	100 KHz	-128 dBc / Hz			-119 dBc / Hz	
	1 MHz	-140 dBc / Hz			-120 dBc / Hz	
10 MHz	-150 dBc / Hz			-140 dBc / Hz		

<sup>(1)</sup> Inclusive of 25°C tolerance, operating temperature range, ±10% input voltage variation, load change, aging shock and vibration

Part Number Format and Example

[1]	[2]	[3]	[4]	[5]	[6]	[7]
Supply Voltage	Holder Type Output Wave	G	Frequency Stability	Pulling Range	Range Code	Center Frequency

Examples	(1)	5	GPF14	G	B	100	N	35.328
	(2)	3	GV576		D	80	T	27.000

Ex (1) : 3GPF14GB - 100N - 155.520 [ +3.3V, G\_14 type ( F characteristics, PECL output ), RoHS, ±50ppm(-10°C to 70°C), pulling : ±100 ppm ( min. ), 155.520 MHz ]  
 Ex (2) : 3GWD576D - 80T - 125.000 [ +3.3V, G\_576 type ( W characteristics, LVDS output ), ±25ppm (-40°C to 85°C), pulling : ±80 ppm ( typical ), 125.000 MHz ]

[1]	Supply voltage, " 3 " for +3.3V	
[2]	Holder type and output wave [ " P " for PECL differential, " D " for LVDS differential ]	
[3]	Please add " G " after the " type code " for RoHS compliant equivalent ( Does not apply to G_576 series ).	
[4]	-10°C ~ 70 °C	" A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C ", for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85 °C	" D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I ", for example " I20 " : represents ±20ppm over -40 to +85°C
[5]	3.3V	From ±30ppm ~ ±150ppm, control Voltage range : 0.3V ~ 3.0 ; control voltage center : ± 1.65 V
	5.0V	From ±70ppm ~ ±200ppm, control Voltage range : 0.5V ~ 4.5V ; control voltage center : ± 2.5 V
[6]	Pulling Range Code	" M " stands for maximum ; " N " stands for minimum ; " T " stands for typical ( tolerance is ± 20% )
[7]	Center Frequency in MHz	

VCXO

PECL Square Wave Test Circuit	LVDS Square Wave Test Circuit
PECL Square Wave Output Wave Form	LVDS Square Wave Output Wave Form

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Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs [ Please refer to page 5 for product series selections. ]

<p>[ GPF576 , GPW576 ] --- PECL Differential Output</p> <p>MEC</p> <p>Bottom View</p> <p>Land Pattern</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : Tri - state          Pad 3 : Ground          Pad 4 : Output          Pad 5 : Complimentary Output          Pad 6 : Supply Voltage</p>	<p>[ GDF576 , GDW576 ] --- LVDS Differential Output</p> <p>MEC</p> <p>Bottom View</p> <p>Land Pattern</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : Tri - state          Pad 3 : Ground          Pad 4 : Output          Pad 5 : Complimentary Output          Pad 6 : Supply Voltage</p>
<p>[ GPF62 , GPW62 ] --- PECL Differential Output</p> <p>MEC</p> <p>Bottom View</p> <p>Land Pattern</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : Tri - state          Pad 3 : Ground          Pad 4 : PECL Output          Pad 5 : Complimentary PECL Output          Pad 6 : Supply Voltage</p>	<p>[ GDF62 , GDW62 ] --- LVDS Differential Output</p> <p>MEC</p> <p>Bottom View</p> <p>Land Pattern</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : Tri - state          Pad 3 : Ground          Pad 4 : LVDS Output          Pad 5 : Complimentary LVDS Output          Pad 6 : Supply Voltage</p>
<p>[ GPF63 , GPW63 ] --- PECL Differential Output</p> <p>MEC</p> <p>Bottom View</p> <p>Land Pattern</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : Tri - state          Pad 3 : Ground          Pad 4 : PECL Output          Pad 5 : Complimentary PECL Output          Pad 6 : Supply Voltage</p>	<p>[ GDF63 , GDW63 ] --- LVDS Differential Output</p> <p>MEC</p> <p>Bottom View</p> <p>Land Pattern</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : Tri - state          Pad 3 : Ground          Pad 4 : LVDS Output          Pad 5 : Complimentary PECL Output          Pad 6 : Supply Voltage</p>

VCXO

**GS**

50Ω load

**GSR**

10KΩ//10pF load

True Sine Wave

Thru-Hole

SMD

3.3V

5.0V

Min.

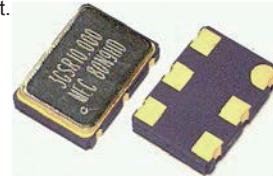
10MHz

Max.

800MHz

## Applications

- True Sine wave VCXOs in 3.2x5.0 mm and 5.0x7.0 mm SMD ("GSR" series). First in the market.
- High purity and low total harmonic distortion. Ideal for audio applications
- For Sine wave clock oscillators, please refer to "HS" and "HSR" series VCXOs.



RoHS Compliance

## General specifications of all available packages , at Ta=+25°C , CL=15pF

Model	" GS " series			" GSR " series	
Output Wave Form	True Sine Wave				
Input Voltage ( VDD)	+3.3V D.C.±5%	+5.0V D.C.±10%	+3.3V D.C.±5%	+5.0V D.C.±10%	
Frequency Range	10.0 ~ 800.0MHz	10.0 ~ 156.0MHz	10.0MHz ~ 30.0MHz		
Initial Freq. Accuracy ( at 25 °C )	Vc= 1.65V ± 0.2V	Vc= 2.5V ± 0.2V	Vc= 1.65V ± 0.2V	Vc= 2.5V ± 0.2V	
Control Voltage Center	1.65 V <sub>DC</sub>	2.5 V <sub>DC</sub>	1.65 V <sub>DC</sub>	2.5 V <sub>DC</sub>	
Control Voltage Range	0.3 V to 3.0 V	0.5 V to 4.5 V	0.3 V to 3.0 V	0.5 V to 4.5 V	
Output Level	Standard: + 3.0 dBm min. Tolerance: ± 1 dB Maximum Power: + 7 dBm User to specify	Standard: + 5.0 dBm min. Tolerance: ± 1 dB Maximum Power: + 13 dBm User to specify	1.0 V p-p typical		
Load	50Ω. (Internally AC coupled)		10 KΩ // 10 pF load		
Harmonics	< - 30dBc (frequency dependent)		< - 25dBc (frequency dependent)		
Current Consumption	10 MHz : 9 mA ( typ.)	10 MHz : 18 mA ( typ.)	1.1 mA	1.2 mA	
	100 MHz : 18 mA ( typ.)	100 MHz : 34 mA ( typ.)			
	150 MHz : 19 mA ( typ.)	150 MHz : 36 mA ( typ.)			
Frequency Deviation Range	± 80 ppm ( min. )		± 50 ppm typical		
Input Impedance	> 10 K Ω		> 0.5 M Ω		
Modulation Bandwidth ( at -3 dB)	10 KHz min.		25 KHz min.		
Frequency Stability <sup>(1)</sup> Codes	Frequency Stability over Operating Temperature Range	± 25 ppm	± 50 ppm	± 100 ppm	If non-standard please enter the desired stability after the " C " or " I " represents . For example : " C2 0" : ± 20 ppm over -10°C to +70°C " I20 " : ± 20 ppm over -40°C to +85°C
	Commercial ( -10°C to +70°C )	A	B	C	
	Industrial ( -40°C to +85°C )	D	E	F	
Sub-Harmonics	None				
Voltage Control Range	0.0 V to V <sub>DD</sub> with control voltage center at 50% of V <sub>DD</sub>				
Linearity	±10 % max.				
Slope Polarity	Positive. Increasing control voltage increases output frequency				
Phase Noise ( typical )	Offset	125.0 MHz as example	Offset	13.0 MHz as example	
	10 Hz	-75 dBc /Hz	10 Hz	-95 dBc /Hz	
	100 Hz	-110 dBc /Hz	100 Hz	-123 dBc /Hz	
	1 KHz	-125 dBc /Hz	1 KHz	-135 dBc /Hz	
	10 KHz	-132 dBc /Hz	10 KHz	-140 dBc /Hz	
	100 KHz	-128 dBc /Hz	100 KHz	-145 dBc /Hz	
Start -up Time	6.0 m Sec.( typ.)		2.0 m Sec.( typ.)		
Aging	± 5 ppm per year (max.)				

Note : (1) Inclusive of 25°C tolerance , operating temperature range , ±10% input voltage variation , load change , aging , shock and vibration.

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# Voltage Controlled Clock Oscillators [ ( GS series ) / ( GSR series ) ]

## Part Number Format and Example

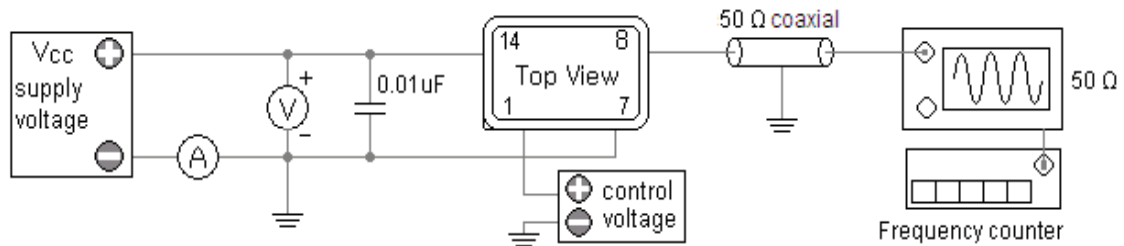
	[1]	[2]	[3]	[4]	[5]	[6]	[7]	[8]
	Supply Voltage	Holder Type	G	Frequency Stability	Pulling Range	Range Code	Center Frequency	Output Power [ HS series only ]
Example	(1)	3	GS14	G A	100.000	N	100.000	5
	(2)	5	GSR53	E	20.000	T	20.000	

Ex (1) : 3GS14GA - 80N - 100.000 - 5 [ +3.3V, G\_14 type, 50 Ω load, RoHS, ±25ppm ( -10°C to 70°C ), pulling : ±80 ppm ( min ), 100.000MHz, power : 5dBm ±1dB ]

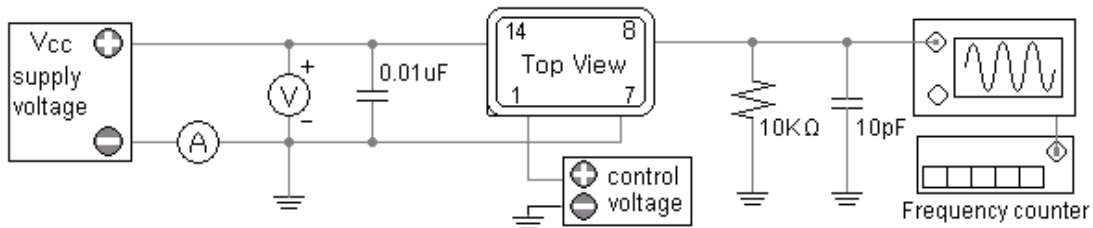
Ex (2) : 5GSR53E - 100T - 20.000 [ +5.0V, G\_53 type, 10KΩ // 10pF load, ±50ppm ( -40°C to 85°C ), pulling : ±100 ppm ( typical ), 20.000MHz ]

[1]	Supply voltage, " 3 " for +3.3V; " 5 " for +5.0V	
[2]	Holder Type	
[3]	Please add " G " after the " type code " for RoHS compliance. Omit " G " if not required.	
[4]	-10°C ~ 70 °C	" A " ± 25ppm; " B " ± 50ppm; " C " ± 100ppm; If non-standard please enter the desired stability after " C ", for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85 °C	" D " ± 25ppm; " E " ± 50ppm; " F " ± 100ppm; If non-standard please enter the desired stability after " I ", for example " I20 " : represents ±20ppm over -40 to +85°C
[5]	Frequency Pulling Range	3.3V From ±30ppm ~ ±150ppm, control Voltage range : 0.3V ~ 3.0; control voltage center : ± 1.65 V 5.0V From ±70ppm ~ ±200ppm, control Voltage range : 0.5V ~ 4.5V; control voltage center : ± 2.5 V
	Pulling Range Code	" M " stands for maximum; " N " stands for minimum; " T " stands for typical ( tolerance is ± 20% )
[7]	Center Frequency in MHz	
[8]	Output power in dBm ( HS series only )	

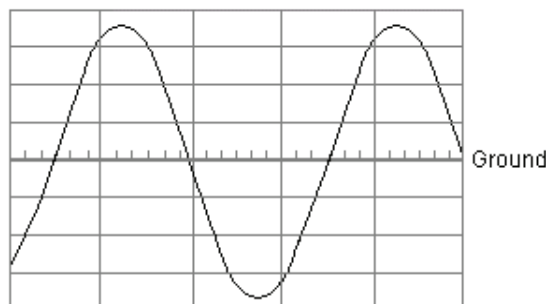
" GS " series : 50 Ω Load Test Circuit :



" GSR " series : 10 KΩ // 10 pF Load Test Circuit :



Output Wave Form



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# Voltage Controlled Clock Oscillators [ ( GS series ) / ( GSR series ) ]

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

<p>[ GSR53 ]</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : Ground          Pad 3 : Output          Pad 4 : Supply Voltage</p>	<p>[ GSR57 ]</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : No Connection          Pad 3 : Ground          Pad 4 : Output          Pad 5 : No Connection          Pad 6 : Supply Voltage</p>
<p>[ GSR42 ]</p> <p>Pad Connections :          Pad 1 : Control Voltage ( rounded pad )          Pad 2 : Ground          Pad 3 : Output          Pad 4 : Supply voltage</p>	<p>[ GSR62 ]</p> <p>Pad Connections :          Pad 1 : Control Voltage          Pad 2 : No Connection          Pad 3 : Ground          Pad 4 : Output          Pad 5 : No Connection          Pad 6 : Supply Voltage</p>
<p>[ GS14 ] , [ GSR14 ]</p> <p>Gull - wing SMD is also available .</p> <p>Pin Connections :          Pin 1 : Control Voltage          Pin 7 : Ground          Pin 8 : Output          Pin 14 : Supply voltage</p>	<p>[ GSR8 ]</p> <p>Gull - wing SMD is also available .</p> <p>Pin Connections :          Pin 1 : Control Voltage          Pin 4 : Ground          Pin 5 : Output          Pin 8 : Supply voltage</p>

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# Temperature Compensated Crystal Oscillators

Output wave form : Clipped Sine " S " [ " TCXO , M \_\_ S " and " VCTCXO , VM \_\_ S " ]

## Product Selection Guide

Product Summary :

TCXO	VCTCXO	Available Freq. Range	RoHS Compliant	Equivalent Model	Package Description	
<b>● Thru - hole Types</b>						
M38S _	VM38S _	9.6 ~ 40.000 MHz	M38GS_	VM38GS_	Dip Type	4 pins for VCTCXOs
						3 pins for TCXOs
M39S _	VM39S _	9.6 ~ 40.000 MHz	M39GS_	VM39GS_	Dip Type	4 pins for VCTCXOs
						3 pins for TCXOs
M14S _	VM14S _	9.6 ~ 40.000 MHz	M14GS_	VM14GS_	Dip Type ( 4 pins ) , Hermetically Sealed	
M15S _	VM15S _	9.6 ~ 40.000 MHz	M15GS_	VM15GS_	Dip Type ( 4 pins ) , With Trimmer	
M8S _	VM8S _	10.0 ~ 40.000 MHz	M8GS_	VM8GS_	Dip Type ( 4 pins ) , Half size , Hermetically Sealed	
<b>● Gull Wing Surface Mount Types</b>						
M47S _	VM47S _	9.6 ~ 27.000 MHz	M47GS_	VM47GS_	Gull Wing SMD Type ( 4 pins ) ,	
M24S _	VM24S _	9.6 ~ 27.000 MHz	M24GS_	VM24GS_	Gull Wing SMD Type ( 4 pins ) , Hermetically Sealed	
M25S _	VM25S _	9.6 ~ 27.000 MHz	M25GS_	VM25GS_	Gull Wing SMD Type ( 4 pins ) , With Trimmer	
M28S _	VM28S _	10.0 ~ 27.000 MHz	M28GS_	VM28GS_	Gull Wing SMD Type ( 4 pins ) , Half size , Hermetically Sealed no trimmer access .	
<b>● Leadless Surface Mount Types</b>						
M62S _	VM62S _	10.0 ~ 40.000 MHz	M62GS_	VM62GS_	6 pad FR4 substrate SMD . ( Height : 2.5 mm )	
M42S _	VM42S _	10.0 ~ 40.000 MHz	M42GS_	VM42GS_	4 pad FR4 substrate SMD. ( Height : 2.5 mm )	
M64S _	VM64S _	10.0 ~ 40.000 MHz	M64GS_	VM64GS_	6 pad FR4 substrate SMD. ( Height : 4.7 mm )	
M44S _	VM44S _	10.0 ~ 40.000 MHz	M44GS_	VM44GS_	4 pad FR4 substrate SMD. ( Height : 4.7 mm )	
M57S _	VM57S _	10.0 ~ 27.000 MHz	Same ( 1 )		4 pad ceramic substrate SMD. ( 7.0 * 5.0 * 1.9 mm )	
M53S _	VM53S _	10.0 ~ 27.000 MHz	Same ( 1 )		4 pad ceramic substrate SMD. ( 5.0 * 3.2 * 1.5 mm )	
M32S _	VM32S _	16.0 ~ 40.000 MHz	Same ( 1 )		4 pad ceramic substrate SMD. ( 2.5 * 3.2 * 1.1 mm )	

" \_ " is voltage code. Please see the table on next page.

For RoHS equivalent model please add "G" after the package code. For example: M14GS3 is RoHS equivalent model of M14S3 .

Note: Frequency tuning by the built-in mechanical trimmer is standard for all models except for (V)M57S, (V)M53S and (V)M32S.

Product Options :

● No mechanical trimmer models are available to allow for aqueous washing cycles . To order such option

Please add " 1 " after the package code . For example : M421 is M42 without mechanical trimmer .

Note: Non-hermetically sealed (VC)TCXO products are not subject to the washing cycles as the solvent will degrade the trimmer capacitor .

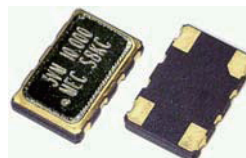
If cleaning is mandatory please choose hermetically sealed packages or no-trimmer option.

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# Temperature Compensated Crystal Oscillators

Clipped Sine Wave ; Wave form code " S " [ VM\_\_S ( VCTCXO ) ; M\_\_S ( TCXO ) ]



RoHS Compliance

General Specifications [ TA = +25°C , V<sub>DD</sub> = at specified voltage , Load : 10K ohms/10 pF ]

Frequency Range		9.6 MHz ~ 40.0 MHz.		V <sub>DD</sub> = 5.0V [ 10 ~ 26 MHz ]					
				V <sub>DD</sub> = 2.8V [ 10 ~ 40 MHz ]					
Output Wave Form		Clipped Sine wave . Wave form code is " S "							
Standard Frequency ( Partial list ) [ MHz ]		10.000	12.800	13.000	14.400	14.7456	15.360	16.367667	
		16.384	19.200	19.440	20.000	25.000	26.000	27.000	
Initial Calibration Tolerance		< ±1 ppm. At +25°C ±2°C for models with trimmer . < ±2 ppm. At +25°C ±2°C for models without trimmer .							
Frequency Stability ( ppm )		± 0.5 ppm	± 1.0 ppm	± 1.5 ppm	± 2.0 ppm	± 2.5 ppm	± 3.0 ppm	○ : available △ : please contact us X : not available	
Frequency Stability vs Temperature ( examples )		0°C to 50°C	○	○	○	○	○		
		-10°C to 60°C	△	○	○	○	○		
		-20°C to 70°C	X	○	○	○	○		
		-30°C to 75°C	X	○	○	○	○		
		-40°C to 85°C	X	○	○	○	○		
Frequency Stability		vs Aging	±1 ppm / year max. at 25C						
		vs Voltage Change	±0.2 ppm max. , for a ±5% input voltage change .						
		vs Load Change	±0.2 ppm max. , for a ±10% load condition change .						
		vs Reflow ( SMD type )	±1.0 ppm max. , 1 reflow and measured 24 hours afterwards .						
Output Voltage Level ( peak to peak )		0.8 V p-p ( min. )							
Input Voltage Range		+5.0V ( Voltage code is " 5 " ) , +3.0V ( Voltage code is " 3 " ) , +2.8V ( Voltage code is " 28 " )							
Current Consumption. ( max. )		9.6 ~ 15 MHz: 1.5 mA max.		15.01 ~ 26.0 MHz : 2.0 mA		26.01 ~ 40.0 MHz : 2.5 mA			
( MFC ) by built-in trimmer		Standard		± 3.0 ppm ( min. ) tuning Note: (V)M57 , (V)M53 and (V)M32 models have no built-in mechanical trimmer.					
		Option		No mechanical trimmer built -in (for aqueous washing cycles). To order please add " 1 " after the regular model prefix . Example: M381.					
Electrical Frequency Tuning ( EFC ) by external control voltage		Control Voltage Center		Standard: +1.5 V ± 1.0 V for all Input voltages.					
		Frequency Deviation Range		± 5.0 ppm ( min. ) , V <sub>control</sub> = +1.5 V±1.0 V					
		Slope Polarity ( Transfer Function )		Positive slope. Positive voltage for positive frequency shift.					
		Input Impedance		1.0 MΩ min.					
		Modulation Bandwidth		3 KHz min. Measured at -3 dB , V <sub>control</sub> = +1.5 V <sub>DC</sub>					
Linearity		10 % max.							
Start-Up Time.		2.0 m sec. ( typ. ) , 5.0 m sec. ( max. ) ( reach 90% amplitude and at +25°C ± 2°C )							
Output Load		10 KΩ // 10 pF ± 10%							
Output Format		DC block, AC coupled. Except ( V ) M53 and ( V ) M32 model.							
Phase Noise ( 13.0 MHz as example ) [ dBc/Hz ; typical ]		10 Hz	100 Hz	1 KHz	10 KHz	100 KHz			
		-80	-115	-135	-148	-148			
Storage Temperature		-40°C to +85°C or -55°C to +125°C ( package dependent )							

Note 1: Some specifications are package dependent. Please refer to the spec. sheet of individual packages once a package is selected .

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# Temperature Compensated Crystal Oscillators

Output wave form : **CMOS " T "** [ " TCXO , **M \_\_ T** " and " VCTCXO , **VM \_\_ T** " ] --- 20.0 KHz ~ 50.0 KHz ; 1.25 MHz ~ 800 MHz

Output wave form : **PECL " P "** [ " TCXO , **M \_\_ P** " and " VCTCXO , **VM \_\_ P** " ] --- 10.0 MHz ~ 800 MHz

Output wave form : **LVDS " D "** [ " TCXO , **M \_\_ D** " and " VCTCXO , **VM \_\_ D** " ] --- 10.0 MHz ~ 800 MHz

## Product Selection Guide

TCXO	VCTCXO	Available Freq. Range	RoHS Compliant Equivalent Model	Package Description
------	--------	-----------------------	---------------------------------	---------------------

### CMOS output wave , " T " series

● Thru - Hold Types					
M_38T_	VM_38T_	20.0 KHz ~ 50.0 KHz	M_38GT_	VM_38GT_	Dip Type ( 4 pins )
M_39T_	VM_39T_		M_39GT_	VM_39GT_	3 pin if TCXO. Package height = 7.3 mm
M_14T_	VM_14T_		M_14GT_	VM_14GT_	3 pin if TCXO. Package height = 4.7mm
M_15T_	VM_15T_		M_15GT_	VM_15GT_	Dip Type ( 4 pins ) , With Trimmer
M_8T_	VM_8T_	1.25 MHz to 800 MHz	M_8GT_	VM_8GT_	Dip Type ( 4 pins ) , Half size , Hermetically Sealed
● Gull Wing Surface Mount Types					
M_47T_	VM_47T_	20.0 KHz ~ 50.0 KHz	M_47GT_	VM_47GT_	Gull Wing Type ( 4 pins ) ,
M_24T_	VM_24T_		M_24GT_	VM_24GT_	Gull Wing Type ( 4 pins ) , Hermetically Sealed
M_25T_	VM_25T_	1.25 MHz to 800 MHz	M_25GT_	VM_25GT_	Gull Wing Type ( 4 pins ) , With Trimmer
M_28T_	VM_28T_		M_28GT_	VM_28GT_	Gull Wing Type ( 4 pins ) , Half size , Hermetically Sealed
● Leadless Surface Mount Types					
M_62T_	VM_62T_	20.0 KHz ~ 50.0 KHz	M_62GT_	VM_62GT_	6 pad FR4 substrate . ( 9.6 x 11.4 x 2.5 mm )
M_42T_	VM_42T_		M_42GT_	VM_42GT_	4 pad FR4 substrate . ( 9.6 x 11.4 x 2.5 mm )
M_572T_	VM_572T_		Same ( 1 )		4 pad ceramic substrate . ( 7.0 * 5.0 * 2.5 mm )
M_536T_	VM_536T_	1.0 ~ 200.0 MHz	Same ( 1 )		4 pad ceramic substrate . ( 3.2 * 5.0 * 2.0 mm )

### PECL Differential PECL Outout , " P " series

● Leadless Surface Mount Types				
M_5762P_	VM_5762P_	19.440 ~ 800 MHz	Same ( 1 )	6 pad ceramic substrate . ( 7.0 * 5.0 * 2.8 mm )

### LVDS Differential LVDS Output , " D " series

● Leadless Surface Mount Types				
M_5762D_	VM_5762D_	19.440 ~ 800 MHz	Same ( 1 )	6 pad ceramic substrate . ( 7.0 * 5.0 * 2.8 mm )

" \_ " is voltage code. Please see the table in next page.

For RoHS equivalent models please add "G" after the package code. For example: M14GT is RoHS equivalent of M14T .

(1) M57T, VM57T, M53T and VM53T are RoHS compliant and lead free products.

Note: Frequency tuning by built-in mechanical trimmer is standard for all models except for (V)M57T,(V)M57P,(V)M57D .

Note: Non-hermetically sealed (VC)TCXO products are not subject to the washing cycles as the solvent will degrade the trimmer capacitor .

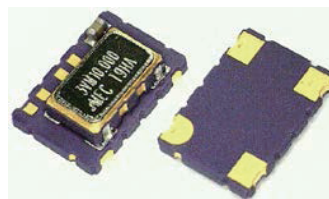
If cleaning is mandatory please choose hermetically sealed packages or no-trimmer option.

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### Features

- Wide frequency range : [ 32.768 KHz ] , [ 20.0 KHz ~ 50.0 KHz ]
- Frequency stability as tight as  $\pm 0.5$  ppm over 0°C to 50°C  
Frequency stability as tight as  $\pm 1.0$  ppm over -40°C to 85°C



General specifications of all available packages , at Ta=+25°C , CL=15pF

Frequency Range		<b>20.0 KHz ~ 52.7 KHz ; 32.768 KHz [ From KHz with divider. mA current consumption. ]</b>									
Output Wave Form		Square wave. Wave form code is " T "									
Initial Calibration Tolerance		Models with mechanical trimmer : <math>\pm 1.0 \text{ ppm}</math> . +25°C $\pm 2^\circ\text{C}</math> .Models without mechanical trimmer : <math>\pm 2.0 \text{ ppm}</math> at +25°C \pm 2^\circ\text{C}</math> .$									
Standard Frequency ( Partial list )		32.768 KHz									
Frequency Stability ( ppm )		$\pm 0.5$ ppm	$\pm 1.0$ ppm	$\pm 1.5$ ppm	$\pm 2.0$ ppm	$\pm 2.5$ ppm	$\pm 3.0$ ppm	○ : available △ : please contact us X : not available			
Frequency Stability vs Temperature ( examples )	0°C to 50°C	○	○	○	○	○	○				
	-10°C to 60°C	△	○	○	○	○	○				
	-20°C to 70°C	X	○	○	○	○	○				
	-30°C to 75°C	X	○	○	○	○	○				
	-40°C to 85°C	X	○	○	○	○	○				
Frequency Stability	vs Aging	$\pm 1.0$ ppm max. , per year at 25°C .									
	vs Voltage Change	$\pm 0.3$ ppm max. , for a $\pm 5\%$ input voltage change .									
	vs Load Change	$\pm 0.3$ ppm max. , for a $\pm 10\%$ load condition change .									
	vs Reflow ( SMD type )	$\pm 1.0$ ppm max. , 1 reflow and measured 24 hours afterwards .									
Output Logic	CMOS										
	Mechanical Frequency Tuning	Standard	$\pm 3.0$ ppm ( min. ) tuning Note: (V)M57 has no mechanical trimmer built -in.								
		Option	No mechanical trimmer built-in (for aqueous washing cycles). To order please add " 1 " after the regular model prefix . Example: M381								
Input Voltage Range	Standard	$+3.3 \text{ V}$ ( voltage code is " 33 " )				$+5.0 \text{ V}$ ( voltage code is " 5 " )					
Output Logic Levels	Logic High " 1 "	90% of $V_{DD}$ min.									
	Logic Low " 0 "	10% of $V_{DD}$ max.									
Rise and Fall Time	10.0 n sec. max. Measured at 20% $\leftrightarrow$ 80% of the wave form										
Current Consumption. ( max. ) ( Over operating temperature range . )	8.0 mA ( max. ) for 32.768 KHz at +3.3V 12.0 mA ( max. ) for 50.0 KHz at +3.3V										
Rise and Fall Time	10.0 n sec. max. Measured at 20% $\leftrightarrow$ 80% of the wave form										
Electrical Frequency Tuning ( EFC ) by external control voltage	Control Voltage Center	Standard: $+1.5 \text{ V} \pm 1.0 \text{ V}$ for all input voltages.									
	Frequency Deviation Range	$\pm 5.0$ ppm. ( min. ) with $V_{con} = +1.5 \text{ V} \pm 1.0 \text{ V}$									
	Slope Polarity ( Transfer Function )	Positive slope. Positive voltage for positive frequency shift.									
	Input Impedance	10 K $\Omega$ min.									
	Modulation Bandwidth	3 KHz min. Measured at -3 dB, $V_{control} = +1.5 \text{ V}_{DC}$ .									
	Linearity	10 % max.									
Duty Cycle	50 % $\pm$ 5 %										
Start-Up Time.	10.0m sec. ( typ. ) , 5.0m sec. ( max. ) ( reach 90% amplitude and at +25°C $\pm 2^\circ\text{C}$ )										
Output Load	15 pF										
Storage Temperature	-40°C to +85°C or -55°C to +125°C ( package dependent )										

Note 1: Some specifications are package dependent. Please refer to the spec. sheet of individual packages once a package is selected .

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**Features**

- Wide frequency range : [ 1.25 MHz ~ 156.0 MHz ]
- Frequency stability as tight as  $\pm 0.5$  ppm over 0°C to 50°C  
Frequency stability as tight as  $\pm 1.0$  ppm over -40°C to 85°C



**General specifications of all available packages , at Ta=+25°C , CL=15pF**

Frequency Range		<b>1.25 MHz ~ 156.0 MHz</b>									
Output Wave Form		Square wave. Wave form code is " T "									
Initial Calibration Tolerance		Models with mechanical trimmer : $< \pm 1.0$ ppm. +25°C $\pm 2^\circ$ C. Models without mechanical trimmer : $\pm 2.0$ ppm at +25°C $\pm 2^\circ$ C.									
Standard Frequency ( Partial list ) [ MHz ]		10.000	12.800	13.000	14.7456	16.000	16.384	19.200	19.440	19.680	
		20.000	25.000	27.000	38.880	40.000	54.000	77.760	125.000	155.520	
Frequency Stability ( ppm )		$\pm 0.5$ ppm	$\pm 1.0$ ppm	$\pm 1.5$ ppm	$\pm 2.0$ ppm	$\pm 2.5$ ppm	$\pm 3.0$ ppm	O : available $\Delta$ : please contact us X : not available			
Frequency Stability vs Temperature ( examples )		0°C to 50°C	O	O	O	O	O				
		-10°C to 60°C	$\Delta$	O	O	O	O				
		-20°C to 70°C	X	O	O	O	O				
		-30°C to 75°C	X	O	O	O	O				
		-40°C to 85°C	X	O	O	O	O				
Frequency Stability		vs Aging	$\pm 1.0$ ppm max., per year at 25°C .								
		vs Voltage Change	$\pm 0.3$ ppm max. , for a $\pm 5\%$ input voltage change .								
		vs Load Change	$\pm 0.3$ ppm max. , for a $\pm 10\%$ load condition change .								
		vs Reflow ( SMD type )	$\pm 1.0$ ppm max., 1 reflow and measured 24 hours afterwards .								
Output Voltage Level ( peak to peak )		T T L / CMOS									
Mechanical Frequency Tuning		Standard	$\pm 3.0$ ppm (min.) tuning Note: VM57 has no mechanical trimmer built-in.								
		Option	No mechanical trimmer built-in (for aqueous washing cycles). To order please add " 1 " after the regular model prefix . Example: M381T.								
Input Voltage Range		Standard		+2.8 V ( voltage code is " 28 " )	+3.0 V ( voltage code is " 3 " )	+3.3 V ( voltage code is " 33 " )	+5.0 V ( voltage code is " 5 " )				
Current Consumption. ( typical ) ( Over operating temperature range . )				2 mA @ 8.192 MHz	2 mA @ 8.192 MHz	5 mA @ 8.192 MHz					
				3 mA @ 10.0 MHz	4 mA @ 10.0 MHz	7 mA @ 10.0 MHz					
				14 mA @ 77.760 MHz	17 mA @ 77.760 MHz	32 mA @ 77.760 MHz					
				26 mA @ 155.520 MHz	35 mA @ 155.520 MHz	50 mA @ 155.520 MHz					
Output Logic Levels		Logic High " 1 "		90% of V <sub>DD</sub> min.							
		Logic Low " 0 "		10% of V <sub>DD</sub> max.							
Electrical Frequency Tuning  ( EFC ) by external  control voltage		Control Voltage Center		Standard: +1.5 V $\pm$ 1.0 V for all input voltages.							
		Frequency Deviation Range		$\pm 5.0$ ppm. ( min.) with Vcon = +1.5 V $\pm$ 1.0 V							
		Slope Polarity ( Transfer Function )		Positive slope. Positive voltage for positive frequency shift.							
		Input Impedance		50K $\Omega$ min.							
		Modulation Bandwidth		20 KHz min.							
		Linearity		$\pm 10$ % max.							
Rise Time and fall time		10.0 n sec. max.. ; 20% $\leftrightarrow$ 80% of the wave form.									
Duty Cycle		Standard: 50 % $\pm$ 10 % ; Option: 50 % $\pm$ 5 % ; Measured at 50% V <sub>DD</sub> .									
Start-Up Time.		5.0 m sec. ( typ. ) , 10.0 m sec. ( max. ) ( reach 90% amplitude and at +25°C $\pm$ 2°C )									
Output Load		15 pF									
SSB Phase Noise  at 25°C , 15pF		Offset / dBc / Hz [ typical ]		10 Hz	100 Hz	1 KHz	10 KHz	100 KHz			
		M572T33 - 10.000		-96 dBc / Hz	-122 dBc / Hz	-138 dBc / Hz	-145 dBc / Hz	-150 dBc / Hz			
		M572T33 - 77.760		-74 dBc / Hz	-105 dBc / Hz	-120 dBc / Hz	-124 dBc / Hz	-120 dBc / Hz			
		M572T33 - 155.520		-68 dBc / Hz	-96 dBc / Hz	-110 dBc / Hz	-117 dBc / Hz	-112 dBc / Hz			
Storage Temperature		-40°C to +85°C or -55°C to +125°C ( package dependent )									

Note 1: Some specifications are package dependent. Please refer to the spec. sheet of individual packages once a package is selected .

TCXO  
MB\_T

VCTCXO  
VMB\_T

Short lead time.  
Custom Frequencies.

CMOS

SMD

15pF

2.8V

3.3V

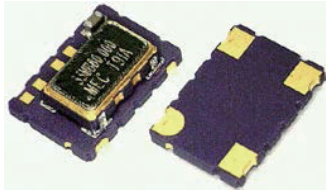
5.0V

Min.  
1.0 MHz

Max.  
200 MHz

Features

- Quick Turn , Short Lead time . From 1 day to 1 week .
- Low Jitter . Peak - to - peak period jitter is 70 ps ( typical ) .
- Low phase noise : -114 dbc / Hz at 1KHz offset ( 133.0 MHz ) .
- Custom frequencies can easily be configured .
- 2.8V , 3.0V or 3.3V supply voltages
- TCXOs or VCTCXOs CMOS square wave output .



General specifications of all available packages , at Ta=+25°C , CL=15pF

Available Frequency Range		1.0 ~ 200.0 MHz ; Custom frequencies					
Output Wave Form		Square wave [ LVCMOS ]. Wave form code is " T "					
Supply Voltage V <sub>DD</sub>		+ 2.8 V <sub>DD</sub> ± 5 %		+ 3.0 V <sub>DD</sub> ± 5 %		+ 3.3 V <sub>DD</sub> ± 5 %	
Initial Calibration Tolerance		Models with mechanical trimmer : < ±1.0 ppm. +25°C ±2°C. Models without mechanical trimmer : ±2.0 ppm at +25°C ±2°C.					
Frequency Stability ( ppm )		± 0.5 ppm	± 1.0 ppm	± 1.5 ppm	± 2.0 ppm	± 2.5 ppm	± 3.0 ppm
Frequency Stability vs Temperature ( examples )	0°C to 50°C	○	○	○	○	○	○
	-10°C to 60°C	△	○	○	○	○	○
	-20°C to 70°C	X	○	○	○	○	○
	-30°C to 75°C	X	○	○	○	○	○
	-40°C to 85°C	X	○	○	○	○	○
Frequency Stability	vs Aging	± 1.0 ppm max., per year at 25°C .					
	vs Voltage Change	± 0.3 ppm max. , for a ±5% input voltage change .					
	vs Load Change	± 0.3 ppm max. , for a ±10% load condition change .					
	vs Reflow ( SMD type )	± 1.0 ppm max., 1 reflow and measured 24 hours afterwards .					
Mechanical Frequency Tuning	Standard	± 3.0 ppm (min.) tuning Note: VM57 has no mechanical trimmer built-in.					
	Option	No mechanical trimmer built-in (for aqueous washing cycles). To order please add " 1 " after the regular model prefix . Example: M381T.					
Current Consumption [ 15 pF ]	Supply Current	8.0 mA max [ 25 MHz at +25 °C ] ; 20.0 mA max [ 200 MHz at +25 °C ]					
Output Logic Levels	Logic High " 1 "	90% of V <sub>DD</sub> min.					
	Logic Low " 0 "	10% of V <sub>DD</sub> max.					
Electrical Frequency Tuning ( EFC ) by external control voltage	Control Voltage Center	Standard: +1.5 V ± 1.0 V for all input voltages.					
	Frequency Deviation Range	± 5.0 ppm. ( min.) with Vcon = +1.5 V±1.0 V					
	Slope Polarity (Transfer Function)	Positive slope. Positive voltage for positive frequency shift.					
	Input Impedance	50K Ω min.					
	Modulation Bandwidth	20 KHz min.					
Linearity	±10 % max.						
Rise Time and fall time	10.0 n sec. max. ; 20% ↔ 80% of the wave form.						
Duty Cycle	Standard: 50 % ± 10 % ; Option: 50 % ± 5 % ; Measured at 50% V <sub>DD</sub> .						
Start-Up Time.	5.0 m sec. ( typ. ) , 10.0 m sec. ( max. ) ( reach 90% amplitude and at+25°C ± 2°C)						
Output Load	15 pF						
SSB Phase Noise at 25°C , 15pF [ typical ]	Offset	10 Hz	100 Hz	1 KHz	10 KHz	100 KHz	
	MB572T33 - 133.333	-76 dBc / Hz	-102 dBc / Hz	-114 dBc / Hz	-110 dBc / Hz	-100 dBc / Hz	
Storage Temperature	-40°C to +85°C or -55°C to +125°C ( package dependent )						

Note 1: Some specifications are package dependent. Please refer to the spec. sheet of individual packages once a package is selected .

( V ) MB53		( V ) MB572		( V ) MB42		( V ) MB62	
Pad 1	Do not connect for TCXO . Voltage control for VCTCXO .	Pad 1	Do not connect for TCXO . Voltage control for VCTCXO .	Pad 1	Do not connect for TCXO . Voltage control for VCTCXO .	Pad 1 , 2 , 4	Ground
Pad 2	Ground	Pad 2	Ground	Pad 2	Ground	Pad 3	Output
Pad 3	Output	Pad 3	Output	Pad 3	Output	Pad 5	Do not connect for TCXO . Voltage control for VCTCXO .
Pad 4	Supply Voltage	Pad 4	Supply Voltage	Pad 4	Supply Voltage	Pad 6	Supply Voltage

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TCXO  
MV\_T

VCTCXO  
VMV\_T

Low Cost .  
High Frequency .

CMOS

Thru-Hole

SMD

15pF

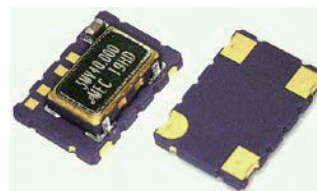
3.3V

Min.  
27.0 MHz

Max.  
200 MHz

Features

- Wide frequency range : [ 27.0 MHz ~ 200.0 MHz ]
- Frequency stability as tight as  $\pm 0.5$  ppm over 0°C to 50°C  
Frequency stability as tight as  $\pm 1.0$  ppm over -40°C to 85°C



General specifications of all available packages , at Ta=+25°C , CL=15pF

Frequency Range		<b>27.0 MHz ~ 200.0 MHz</b>						
Output Wave Form		Square wave. Wave form code is " T "						
Initial Calibration Tolerance		Models with mechanical trimmer: $< \pm 1.0$ ppm. +25°C $\pm 2^\circ$ C. Models without mechanical trimmer: $< \pm 2.0$ ppm at +25°C $\pm 2^\circ$ C.						
Standard Frequency ( Partial list ) [ MHz ]		30.000	32.768	38.880	40.000	50.000	54.000	64.000
		77.760	80.000	100.000	128.000	155.520	160.000	200.000
Frequency Stability ( ppm )		$\pm 0.5$ ppm	$\pm 1.0$ ppm	$\pm 1.5$ ppm	$\pm 2.0$ ppm	$\pm 2.5$ ppm	$\pm 3.0$ ppm	O : available $\Delta$ : please contact us X : not available
Frequency Stability vs Temperature ( examples )	0°C to 50°C	O	O	O	O	O	O	
	-10°C to 60°C	$\Delta$	O	O	O	O	O	
	-20°C to 70°C	X	O	O	O	O	O	
	-30°C to 75°C	X	O	O	O	O	O	
	-40°C to 85°C	X	O	O	O	O	O	
Frequency Stability	vs Aging	$\pm 1.0$ ppm max., per year at 25°C .						
	vs Voltage Change	$\pm 0.3$ ppm max. , for a $\pm 5\%$ input voltage change .						
	vs Load Change	$\pm 0.3$ ppm max. , for a $\pm 10\%$ load condition change .						
	vs Reflow ( SMD type )	$\pm 1.0$ ppm max., 1 reflow and measured 24 hours afterwards .						
Mechanical Frequency Tuning		$\pm 3.0$ ppm (min.) tuning Note: VMV57 have no mechanical trimmer built -in.						
Input Voltage Range		+ 3.3 V ( voltage code is " 33 " )						
Output Logic Levels	Logic High " 1 "	90% of V <sub>DD</sub> min.						
	Logic Low " 0 "	10% of V <sub>DD</sub> max..						
Rise Time and fall time		1.2 n sec. typical ( 0.3 V $\leftrightarrow$ 3.0 V, 15 pF load )						
Electrical Frequency Tuning  ( EFC ) by external  control voltage	Control Voltage Center	Standard: +1.5 V $\pm$ 1.0 V for all input voltages.						
	Frequency Deviation Range	$\pm 5.0$ ppm. ( min.) with Vcon = +1.5 V $\pm$ 1.0 V						
	Slope Polarity (Transfer Function)	Positive slope : Increasing control voltage ( Vcon ) increases output frequency .						
	Input Impedance	2 M $\Omega$ min.						
	Modulation Bandwidth	25 KHz min.						
	Linearity	10 % max.						
Current Consumption. ( max. )		40 mA ( max. ) , Frequency dependent .						
Duty Cycle		50% $\pm$ 5% measured at 50% V <sub>DD</sub>						
Start-Up Time.		5.0m sec. ( typ. ) , 10.0m sec. ( max. ) ( reach 90% amplitude and at+25°C $\pm$ 2°C)						
Output Load		15 pF						
SSB Phase Noise [ typical ]  at 25°C	Offset	10 Hz	100 Hz	1 KHz	10 KHz	100 KHz		
	VMV572T33 - 77.760	-76 dBc / Hz	-105 dBc / Hz	-116 dBc / Hz	-120 dBc / Hz	-121 dBc / Hz		
	VMV572T33 - 155.520	-65 dBc / Hz	-96 dBc / Hz	-110 dBc / Hz	-112 dBc / Hz	-115 dBc / Hz		
Storage Temperature		-55°C to +125°C						

Note 1: Some specifications are package dependent. Please refer to the spec. sheet of individual packages once a package is selected .

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TCXOs VCTCXOs

TCXO  
MW\_T

VCTCXO  
VMW\_T

High Frequency .  
200 ~ 800 MHz

CMOS

Thru-Hole

SMD

15pF

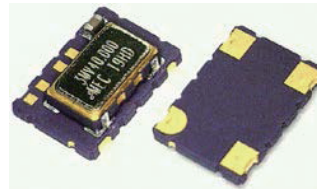
3.3V

Min.  
200 MHz

Max.  
800 MHz

Features

- Wide frequency range : [ 200.01 MHz ~ 800.0 MHz ]
- Frequency stability as tight as  $\pm 0.5$  ppm over 0°C to 50°C  
Frequency stability as tight as  $\pm 1.0$  ppm over -40°C to 85°C



General specifications of all available packages , at Ta=+25°C , CL=15pF

Frequency Range		<b>200.01 MHz ~ 800.0 MHz</b>						
Output Wave Form		Square wave. Wave form code is " T "						
Initial Calibration Tolerance		Models with mechanical trimmer: <math>\pm 1.0</math> ppm. +25°C $\pm 2^\circ\text{C}</math>.Models without mechanical trimmer: <math>\pm 2.0</math> ppm at +25°C \pm 2^\circ\text{C}</math>.$						
Standard Frequency ( Partial list ) [ MHz ]		200.000	204.8000	311.040	320.000	400.0000	409.600	622.080
Frequency Stability ( ppm )		$\pm 0.5$ ppm	$\pm 1.0$ ppm	$\pm 1.5$ ppm	$\pm 2.0$ ppm	$\pm 2.5$ ppm	$\pm 3.0$ ppm	○ : available △ : contact us X : not available
Frequency Stability vs Temperature ( examples )	0°C to 50°C	○	○	○	○	○	○	
	-10°C to 60°C	△	○	○	○	○	○	
	-20°C to 70°C	X	○	○	○	○	○	
	-30°C to 75°C	X	○	○	○	○	○	
	-40°C to 85°C	X	○	○	○	○	○	
Frequency Stability	vs Aging	$\pm 1.0$ ppm max., per year at 25°C .						
	vs Voltage Change	$\pm 0.3$ ppm max. , for a $\pm 5\%$ input voltage change .						
	vs Load Change	$\pm 0.3$ ppm max. , for a $\pm 10\%$ load condition change .						
	vs Reflow ( SMD type )	$\pm 1.0$ ppm max., 1 reflow and measured 24 hours afterwards .						
Mechanical Frequency Tuning		$\pm 3.0$ ppm (min.) tuning Note: (V)MW57T has no mechanical trimmer built-in .						
Input Voltage Range		+3.3 V ( voltage code is " 33 " )						
Output Logic Levels	Logic High " 1 "	90% of V <sub>DD</sub> min.						
	Logic Low " 0 "	10% of V <sub>DD</sub> max..						
Rise Time and fall time		1.2 n sec. typical ( 0.3 V $\leftrightarrow$ 3.0 V, 15 pF load )						
Electrical Frequency Tuning ( EFC ) by external control voltage	Control Voltage Center	Standard: +1.5 V $\pm 1.0$ V for all input voltages.						
	Frequency Deviation Range	$\pm 5.0$ ppm. ( min.) with Vcon = +1.5 V $\pm 1.0$ V						
	Slope Polarity ( Transfer Function )	Positive slope : Increasing control voltage ( Vcon ) increases output frequency .						
	Input Impedance	2 M $\Omega$ min.						
	Modulation Bandwidth	25 KHz min.						
	Linearity	10 % max.						
Current Consumption. ( max. )		65 mA ( max. ) , Frequency dependent .						
Duty Cycle		50% $\pm 5\%$ measured at 50% V <sub>DD</sub>						
Start-Up Time.		5.0m sec. ( typ. ) , 10.0m sec. ( max. ) ( reach 90% amplitude and at+25°C $\pm 2^\circ\text{C}$ )						
Output Load		15 pF						
SSB Phase Noise [ typical ] at 25°C	Offset	10 Hz	100 Hz	1 KHz	10 KHz	100 KHz		
	MW572T33 - 622.080	-50 dBc / Hz	-77 dBc / Hz	-102 dBc / Hz	-115 dBc / Hz	-108 dBc / Hz		
Storage Temperature		-55°C to +125°C						

Note 1: Some specifications are package dependent. Please refer to the spec. sheet of individual packages once a package is selected .

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TCXO  
MW\_P

VCTCXO  
VMW\_P

Low Cost.  
Wide Frequency Range.

PECL

Thru-Hole

SMD

15pF

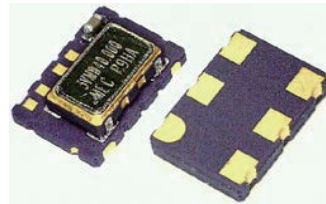
3.3V

Min.  
10 MHz

Max.  
800 MHz

Features

- Wide frequency range : [ 10.0 MHz ~ 800.0 MHz ]
- Low cost , low jitter , general purpose



General specifications of all available packages , at Ta=+25°C , CL=15pF

Frequency Range	10.0 MHz ~ 800.0 MHz							
Output Wave Form	Differential PECL square wave . Wave code is " P "							
Initial Calibration Tolerance	Models with mechanical trimmer: <math>\pm 2.0 \text{ ppm}</math> ( +25°C $\pm 2^\circ\text{C}</math> ) .$							
Standard Frequency ( Partial list )	10.000	12.800	16.000	19.440	20.000	25.000	27.000	O : available Δ : contact us X : not available
	30.000	32.000	32.768	38.880	40.000	50.000	54.000	
	64.000	65.536	77.760	80.000	100.000	128.000	155.520	
	160.000	200.000	204.800	311.040	320.000	409.600	622.080	
Frequency Stability ( ppm )	$\pm 1.0 \text{ ppm}$	$\pm 2.0 \text{ ppm}$	$\pm 2.5 \text{ ppm}$	$\pm 3.0 \text{ ppm}$	$\pm 4.0 \text{ ppm}$	$\pm 5.0 \text{ ppm}$		
Frequency Stability vs Temperature ( examples )	0°C to 50°C	O	O	O	O	O	O	
	-10°C to 60°C	Δ	O	O	O	O	O	
	-20°C to 70°C	X	O	O	O	O	O	
	-30°C to 75°C	X	O	O	O	O	O	
	-40°C to 85°C	X	X	X	Δ	Δ	O	
Frequency Stability	vs Aging	$\pm 1.0 \text{ ppm max.}$ , per year at 25°C .						
	vs Voltage Change	$\pm 0.3 \text{ ppm max.}$ , for a $\pm 5\%$ input voltage change .						
	vs Load Change	$\pm 0.3 \text{ ppm max.}$ , for a $\pm 10\%$ load condition change .						
	vs Reflow ( SMD type )	$\pm 1.0 \text{ ppm max.}$ , 1 reflow and measured 24 hours afterwards .						
Mechanical Frequency Tuning	$\pm 3.0 \text{ ppm (min.)}$ tuning Note: (V)MW57P has no mechanical trimmer built-in .							
Input Voltage Range	+3.3 V ( voltage code is " 33 " )							
Output Logic Levels	Logic High " 1 "	$V_{OH} = 2.275 \text{ V ( min. )}$ , $V_{DD} - 1.025 \text{ V ( min. )}$ , Condition : $R_L = 50\Omega$ to $V_{DD} - 2\text{V}$						
	Logic Low " 0 "	$V_{OL} = 1.680 \text{ V ( max. )}$ , $V_{DD} - 1.620 \text{ V ( max. )}$ , Condition : $R_L = 50\Omega$ to $V_{DD} - 2\text{V}$						
Rise Time and Fall Time	1.5 n sec. ( max. ) ; 20% $\longleftrightarrow$ 80% of waveform .							
Current Consumption. ( Measured with a PECL Thevenin equivalent load )	10 MHz $\leq f_{out} \leq 25 \text{ MHz}$ : 65 mA ( max. ) ; 25 MHz $\leq f_{out} \leq 100 \text{ MHz}$ : 85 mA ( max. ) 100 MHz $\leq f_{out} \leq 800 \text{ MHz}$ : 115 mA ( max. )							
Duty Cycle	50% $\pm$ 5% measured at $V_{DD} - 1.3\text{V}$							
Start-Up Time.	5.0m sec. ( typ. ) , 10.0m sec. ( max. ) ( reach 90% amplitude and at +25°C $\pm 2^\circ\text{C}$ )							
Output Load	50 $\Omega$ to $V_{DD} - 2.0 \text{ V}$							
Pad 1. Option VCTCXO only	Control Voltage Center , Range	+ 1.5 V $\pm$ 1.0V						
	Frequency Deviation Range	$\pm 5.0 \text{ ppm ( min. )}$ with $V_{con} = +1.5 \text{ V} \pm 1.0 \text{ V}$						
	Linearity	6% typical ; 10% max.						
	Slope Polarity	Positive : Positive voltage for positive frequency shift						
Tri - State Function. on pad No. 2	No Connection	Differential PECL and complimentary PECL outputs .						
	Disable	Both outputs are disabled ( high impedance ) when pad No.2 is taken below 0.45*Vcc referenced to ground ( threshold ) Oscillator is always On . Only buffer stage is disabled . Disable current : 50 uA max. ( at 0.0V ) , Disable time : 10 ns ( max. )						
	Enable	At disabled mode , both outputs are enabled when Tri-state pad is taken above 0.45*Vcc referenced to ground ( threshold ) ; Enable time : 10ns + one period of the output frequency ( max. )						
SSB Phase Noise at 25°C [ typical ]	Offset	10 Hz	100 Hz	1 KHz	10 KHz	100 KHz		
	MW5762P33 - 155.520	-65 dBc / Hz	-95 dBc / Hz	-120 dBc / Hz	-125 dBc / Hz	-121 dBc / Hz		
	MW5762P33 - 622.080	-55 dBc / Hz	-85 dBc / Hz	-109 dBc / Hz	-115 dBc / Hz	-110 dBc / Hz		
Phase Jitter ( RMS ) ( 12 KHz to 20 MHz )	2.6 ps ( typ. ) , 4.0 ps ( max. ) for 155.520 MHz							
Period Jitter [ typical ]	Frequency Range	38.880 MHz	77.760 MHz	155.520 MHz	622.080 MHz			
	[ RMS ]	2.2 ps	3.5 ps	4.3 ps	5.0 ps			
	[ Peak to peak ]	17.0 ps	25.0 ps	27.0 ps	32.0 ps			
Storage Temperature	-55°C to +125°C							

Note 1: Some specifications are package dependent. Please refer to the spec. sheet of individual packages once a package is selected .

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TCXOs VCTCXOs

TCXO  
MW\_D

VCTCXO  
VMW\_D

Low Cost .  
Wide Frequency Range.

LVDS

Thru-Hole

SMD

15pF

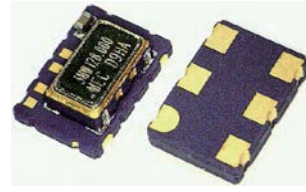
3.3V

Min.  
12 MHz

Max.  
800 MHz

Features

- Wide frequency range : [ 12.0 MHz ~ 800.0 MHz ]
- Low cost , Moderate jitter , Ideal for SONET , xDSL.



General specifications of all available packages , at Ta=+25°C , CL=15pF

Frequency Range		12.0 MHz ~ 800.0 MHz						
Output Wave Form		Differential LVDS square wave . Wave code is " D "						
Initial Calibration Tolerance		Models with mechanical trimmer: <math>\pm 2.0 \text{ ppm}</math> ( +25°C $\pm 2^\circ\text{C}</math> ) .$						
Standard Frequency ( Partial list )		10.000	12.800	16.000	19.440	20.000	25.000	27.000
		30.000	32.000	32.768	38.880	40.000	50.000	54.000
		64.000	65.536	77.760	80.000	100.000	128.000	155.520
		160.000	200.000	311.040	320.000	409.600	622.080	800.000
Frequency Stability ( ppm )		$\pm 1.0 \text{ ppm}$	$\pm 2.0 \text{ ppm}$	$\pm 2.5 \text{ ppm}$	$\pm 3.0 \text{ ppm}$	$\pm 4.0 \text{ ppm}$	$\pm 5.0 \text{ ppm}$	O : available $\Delta$ : contact us X : not available
Frequency Stability vs Temperature ( examples )		0°C to 50°C	O	O	O	O	O	
		-10°C to 60°C	$\Delta$	O	O	O	O	
		-20°C to 70°C	X	O	O	O	O	
		-30°C to 75°C	X	O	O	O	O	
		-40°C to 85°C	X	X	X	$\Delta$	O	
Frequency Stability		vs Aging	$\pm 1.0 \text{ ppm max.}$ , per year at 25°C .					
		vs Voltage Change	$\pm 0.3 \text{ ppm max.}$ , for a $\pm 5\%$ input voltage change .					
		vs Load Change	$\pm 0.3 \text{ ppm max.}$ , for a $\pm 10\%$ load condition change .					
		vs Reflow ( SMD type )	$\pm 1.0 \text{ ppm max.}$ , 1 reflow and measured 24 hours afterwards .					
Mechanical Frequency Tuning		$\pm 3.0 \text{ ppm (min.) tuning}$ Note: VMW57D have no mechanical trimmer built-in .						
Input Voltage Range		+3.3 V ( voltage code is " 33 " )						
Output Logic Levels		Logic High " 1 "	1.4 V typical ; 1.6 V min.					
		Logic Low " 0 "	0.9 V min. ; 1.1 V max.					
Output Differential Voltage, V <sub>OD</sub>		247 mV min.; 355 mV typical ; 454 mV max. Output 1 - output 2						
Output Differential Error, dV <sub>OD</sub>		-50 mV min ; 50 mV max.						
Output Offset Voltage, Vos		1.125 V min. ; 1.200 V typical ; 1.375 V max.						
Offset Magnitude Error, dVos		0 mV min. ; 3 mV typical ; 25 mV max.						
Rise Time and Fall Time		1.5 n sec. ( max. ) ; 20% $\longleftrightarrow$ 80% of waveform .						
Current Consumption. ( Measured with load )		$24 \text{ MHz} \leq f_{out} \leq 33 \text{ MHz}$ : 33 mA (max.) ; $24 \text{ MHz} \leq f_{out} \leq 96 \text{ MHz}$ : 50 mA (max.) $96 \text{ MHz} \leq f_{out} \leq 700 \text{ MHz}$ : 85 mA (max.)						
Duty Cycle		50% $\pm$ 5% measured at 1.25V						
Start-Up Time.		5.0m sec. ( typ. ) , 10.0m sec. ( max. ) ( reach 90% amplitude and at +25°C $\pm 2^\circ\text{C}$ )						
Output Load		50 $\Omega$ from each load						
Drive Capability		100 $\Omega$ between LVDS and complimentary LVDS output						
Pad 1. Option VCTCXO only		Control Voltage Center , Range	+ 1.5 V $\pm$ 1.0V					
		Frequency Deviation Range	$\pm 5.0 \text{ ppm ( min. )}$ with Vcon = +1.5 V $\pm$ 1.0 V					
		Linearity	6% typical ; 10% max.					
		Slope Polarity	Positive : Positive voltage for positive frequency shift					
Tri - State Function. on pad No. 2		No Connection	Differential LVDS and complimentary LVDS outputs .					
		Disable	Both outputs are disabled ( high impedance ) when pad No.2 is taken below 0.45*Vcc referenced to ground ( threshold ) Oscillator is always On . Only buffer stage is disabled . Disable current : 50 uA max. ( at 0.0V ) , Disable time : 10 ns (max.)					
		Enable	At disabled mode , both outputs are enabled when Tri-state pad is taken above 0.45*Vcc referenced to ground ( threshold ) ; Enable time : 10ns + one period of the output frequency (max.)					
SSB Phase Noise at 25°C ( typical )		Offset	10 Hz	100 Hz	1 KHz	10 KHz	100 KHz	
		MW5762D33 - 100.000	-75 dBc / Hz	-104 dBc / Hz	-115 dBc / Hz	-120 dBc / Hz	-122 dBc / Hz	
		MW5762D33 - 622.080	-55 dBc / Hz	-85 dBc / Hz	-109 dBc / Hz	-115 dBc / Hz	-110 dBc / Hz	
Phase Jitter ( RMS ) ( 12 KHz to 20 MHz )		2.6 ps ( typ. ) 4.0 ps (max.) for 155.520 MHz						
Period Jitter [ typical ]		Frequency Range	38.880 MHz	77.760 MHz	155.520 MHz	622.080 MHz		
		[ RMS ]	2.5 ps	4.0 ps	5.0 ps	8.0 ps		
		[ Peak to peak ]	20.0 ps	32.0 ps	28.0 ps	45.0 ps		
Storage Temperature		-55°C to +125°C						

Note 1: Some specifications are package dependent. Please refer to the spec. sheet of individual packages once a package is selected .

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# Temperature Compensated Crystal Oscillators [ TCXO " M " and VCTCXO " VM " ]

Clipped Sine " S "

CMOS " T "

PECL Differential " P "

LVDS Differential " D "

## Part Number Format and Example

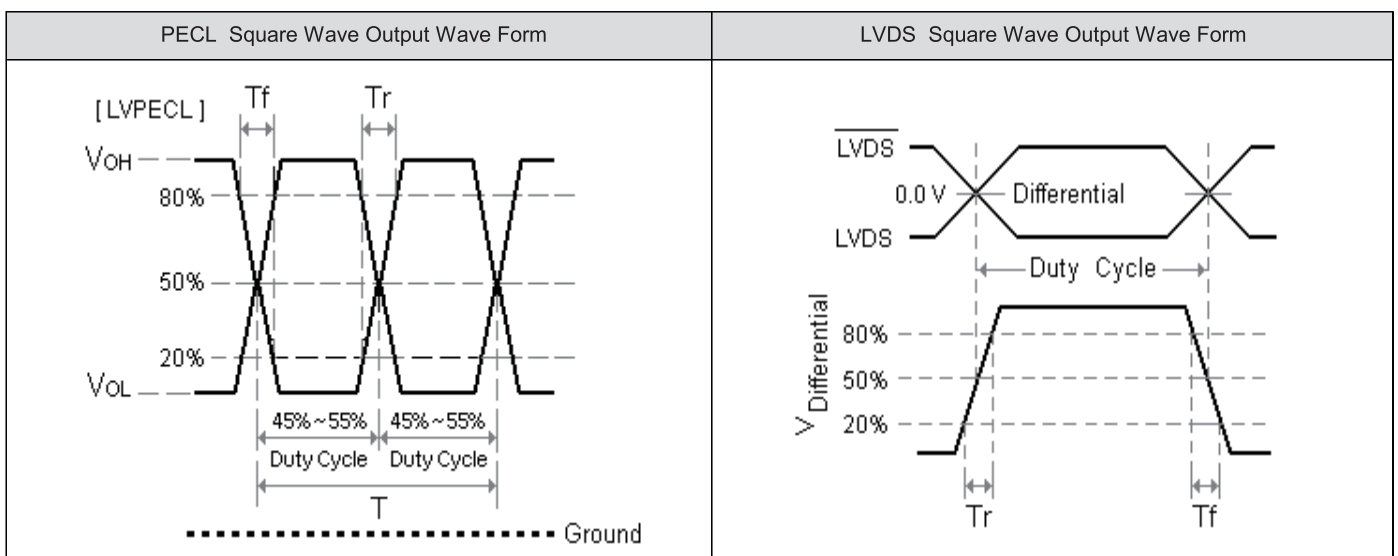
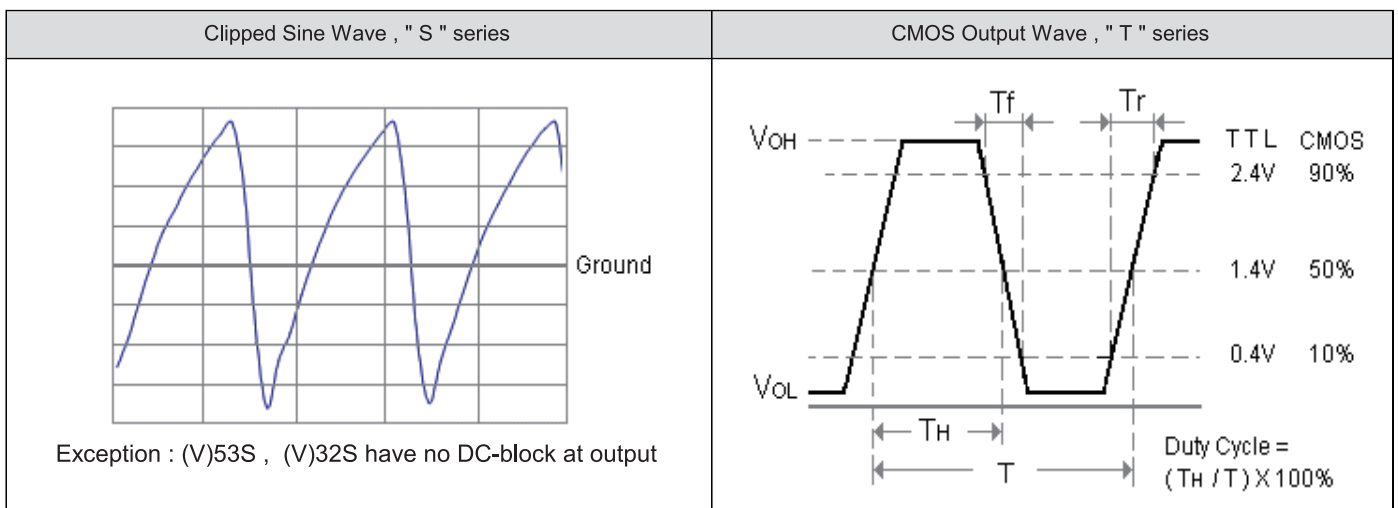
[ 1 ]	[ 2 ]	[ 3 ]	[ 4 ]	[ 5 ]	[ 6 ]	[ 7 ]
Holder Type	G	Output Wave	Supply Voltage	Center Frequency	Frequency Stability	Operating Temp. Range

Examples	(1)	VM38	G	T	5	-	10.000	-	1.5	/	-20+70
	(2)	M57		S	3	-	20.000	-	2.5	/	-30+75

Ex (1): VM38GT5 - 10.000 - 1.5 / -20+70 [ VCTCXO, VM38 type, RoHS, CMOS output, 5.0V, 10.000MHz, ±1.5ppm from -20°C to 70°C ]

Ex (2): M57S3 - 20.000 - 2.5 / -30+75 [ TCXO, M57 type, Clipped Sine Wave, 3.0V, 20.000MHz, ±2.5ppm from -30°C to 75°C ]

[ 1 ]	Holder Type " M " stands for TCXO, " VM " stands for VCTCXO
[ 2 ]	Please add " G " after the " type code " for RoHS compliant ( Does not apply to (V)M32, (V)M53, (V)M536_, (V)M57_, (V)M576_ )
[ 3 ]	" S " stands for Clipped Sine Wave ; " T " stands for Square Wave ; " D " stands for LVDS differential ; " P " stands for PECL differential ex 1 : M44T — TCXO, M44 package, CMOS output ; ex 2 : VM38P — VCTCXO, VM38 package, PECL differential
[ 4 ]	Supply voltage, " 28 " stands for +2.8V ; " 3 " stands for +3.0V ; " 33 " stands for +3.3V ; " 5 " stands for +5.0V
[ 5 ]	Center Frequency in MHz
[ 6 ]	Frequency stability in ±_ ppm ; ex 1 : ± 2.5ppm — 2.5 , ex 2 : ± 1.0ppm — 1.0
[ 7 ]	Operating temperature range in °C ex 1 : -10 °C to 60°C — -10+60 ; ex 2 : -20 °C to 70°C — -20+70 ; ex 3 : -40 °C to 85°C — -40+85



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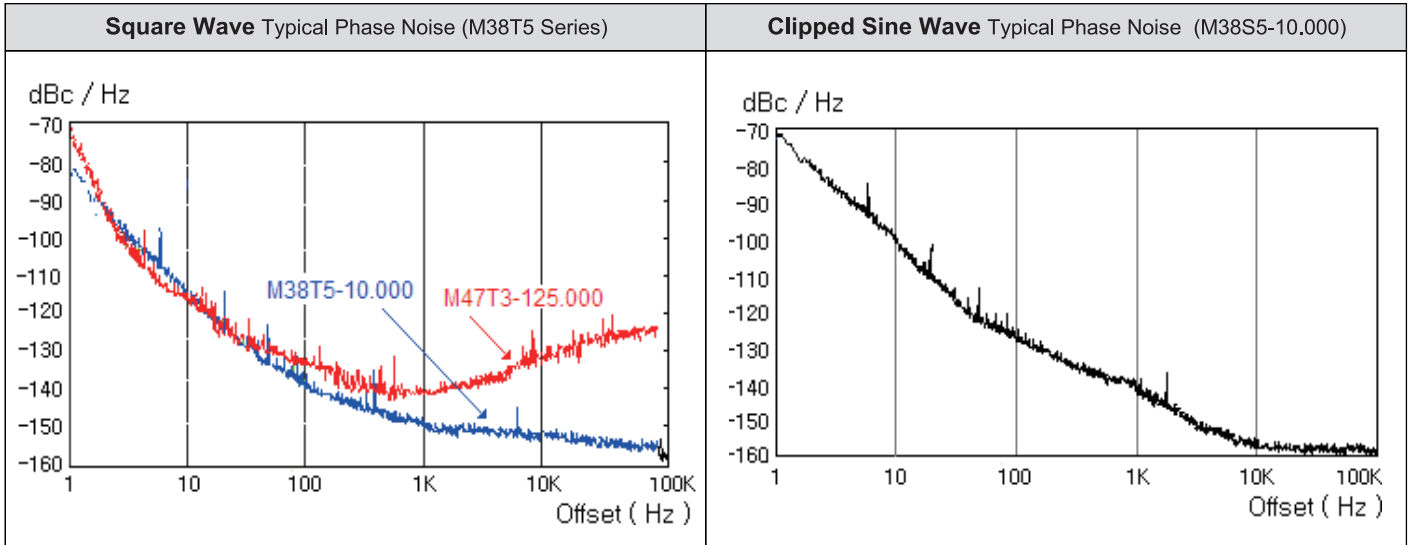
# Temperature Compensated Crystal Oscillators [ TCXO " M " and VCTCXO " VM " ]

Clipped Sine " S "

CMOS " T "

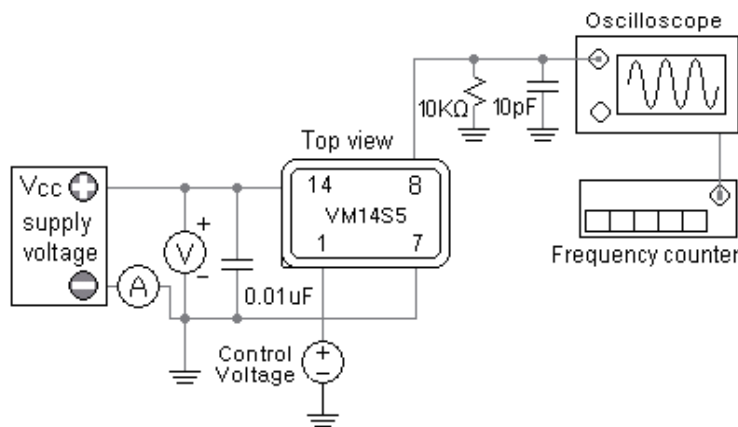
PECL Differential " P "

LVDS Differential " D "

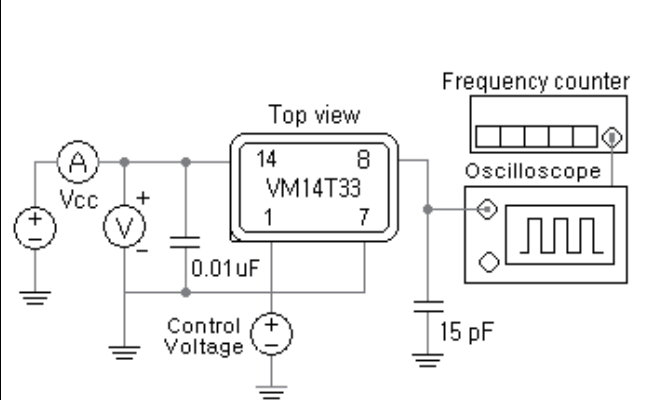


## Test Circuits

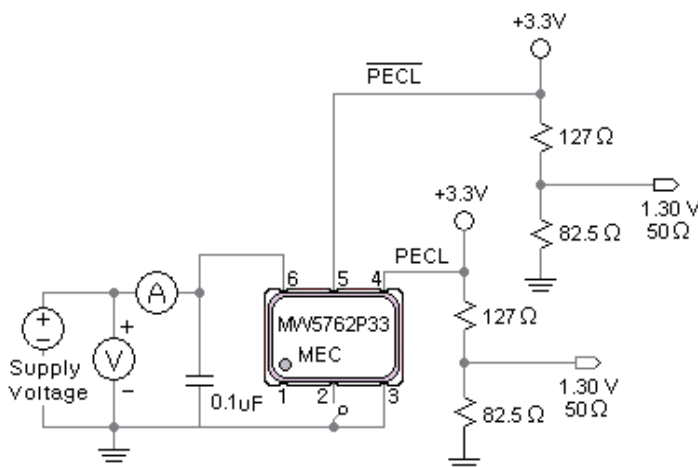
( VC )TCXO with **clipped sine** wave: Ex. VM14S5



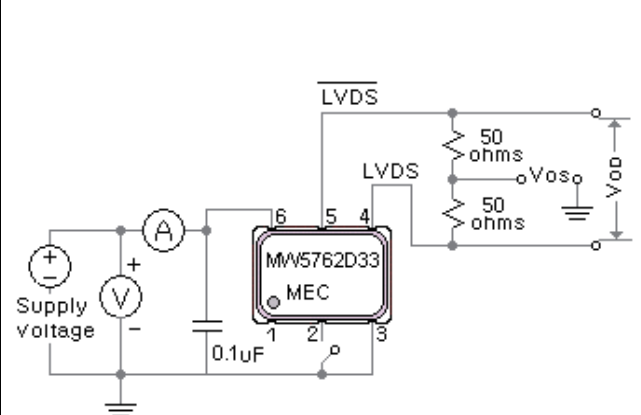
( VC )TCXO with **CMOS** square wave: Ex. VM14T33



( VC )TCXO with **PECL** output: Ex. VMW5762P33



( VC )TCXO with **LVDS** output: Ex. VMW5762D33



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# Temperature Compensated Crystal Oscillators [ TCXO " M " and VCTCXO " VM " ]

Clipped Sine " S "

CMOS " T "

PECL Differential " P "

LVDS Differential " D "

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

[ Please refer to page 6 for product series selections . ]

<p>[ (V) M_42 __ ]</p> <p>Pad Connections :          Pad 1 : Control voltage for VCTCXO. Make no connection if TCXO.          Pad 2 : Ground ; Pin 3 : Output , Pin 4 : Supply Voltage</p>	<p>[ (V) M_62 __ ]</p> <p>Pad Connections :          Pad 1, 2, 4 : Ground , Pad 3 : Output , Pad 6 : Supply Voltage          Pad 5 : Control voltage for VCTCXO. Make no connection if TCXO.</p>
<p>[ (V) M38 __ ]</p> <p>Pin Connections :          Pin 1 : Control voltage for VCTCXO , No physical pin 1 for TCXO ( 3 pins only ) .          Pin 7 : Ground ; Pin 8 : Output , Pin 14 : Supply Voltage</p>	<p>[ (V) M39 __ ]</p> <p>Pin Connections :          Pin 1 : Control voltage for VCTCXO [ No physical pin 1 for TCXO. ( 3 pins only ) ]          Pin 7 : Ground ; Pin 8 : Output , Pin 14 : Supply Voltage</p>
<p>[ (V) M47 __ ]</p> <p>Pin Connections :          Pin 1 : Control voltage for VCTCXO , No connection for TCXO .          Pin 7 : Ground ; Pin 8 : Output , Pin 14 : Supply Voltage</p>	<p>[ (V) M8 __ ] --- Gull - wing SMD is also available .</p> <p>Pin Connections :          Pin 1 : Control voltage for VCTCXO ; No connection for TCXO.          Pin 4 : Ground ; Pin 5 : Output , Pin 8 : Supply Voltage</p>
<p>[ (V) M14 __ ] --- Gull - wing SMD is also available .</p> <p>Pin Connections :          Pin 1 : Control voltage for VCTCXO , No connection for TCXO .          Pin 7 : Ground ; Pin 8 : Output , Pin 14 : Supply Voltage</p>	<p>[ (V) M15 __ ] --- Gull - wing SMD is also available .</p> <p>Pin Connections :          Pin 1 : Control voltage for VCTCXO , No connection for TCXO .          Pin 7 : Ground ; Pin 8 : Output , Pin 14 : Supply Voltage</p>

TCXOs VCTCXOs

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# Temperature Compensated Crystal Oscillators [ TCXO " M " and VCTCXO " VM " ]

Clipped Sine " S "

CMOS " T "

PECL Differential " P "

LVDS Differential " D "

Outline Dimensions ( Unit : mm ) , Suggested pad Layout for SMDs

[ Please refer to page 6 for product series selections . ]

<p>[ (V) M32S ] --- Clipped Sine wave</p> <p>Land Pattern ( reference )</p> <p>Pad Connections : Pad 1 : Control voltage for VCTCXO ; Ground for TCXO . Pad 2 : Ground ; Pin 3 : Output , Pin 4 : Supply Voltage</p>	<p>[ (V) M53S ] --- Clipped Sine wave</p> <p>Land Pattern ( reference )</p> <p>Pad Connections : Pad 1 : Control voltage for VCTCXO ; Ground for TCXO . Pad 2 : Ground ; Pad 3 : Output , Pad 4 : Supply Voltage</p>
<p>[ (V) M57S ] --- Clipped Sine wave</p> <p>Land pattern ( reference )</p> <p>Pad Connections : Pad 1 : Control voltage for VCTCXO ; Ground for TCXO . Pad 2 : Ground ; Pad 3 : Output , Pad 4 : Supply Voltage</p>	<p>[ (V) M_531T_ ] , [ (V) M_53T_ ] --- CMOS output</p> <p>Land Pattern ( reference )</p> <p>Pad Connections : Pad 1 : Control voltage for VCTCXO. Make no connection if TCXO. Pad 2 : Ground ; Pad 3 : Output , Pad 4 : Supply Voltage</p>
<p>[ (V) M_536T_ ] --- CMOS output</p> <p>Land Pattern ( reference )</p> <p>Pad Connections : Pad 1 : Control voltage for VCTCXO. Make no connection if TCXO. Pad 2 : N / C ; Pad 3 : Ground ; Pad 4 : Output Pad 5 : N / C ; Pad 6 : Supply Voltage</p>	<p>[ (V)M_536P for PECL ] ; [ (V)M_536D for LVDS ]</p> <p>Land Pattern ( reference )</p> <p>[ under development ]</p> <p>Pad Connections : Pad 1 : Control voltage for VCTCXO. Make no connection if TCXO. Pad 2 : Tri-state ; Pad 3 : Ground ; Pad 4 : PECL or LVDS output Pad 5 : Complimentary PECL or LVDS Output ; Pad 6 : Supply Voltage</p>
<p>[ (V) M_572T_ ] --- CMOS output</p> <p>Bottom View</p> <p>Land pattern ( reference )</p> <p>Pad Connections : Pad 1 : NC --- TCXO ; Vcon --- VCTCXO Pad 2 : Ground ; Pad 3 : Output , Pad 4 : Supply Voltage</p>	<p>[ (V)M5762P for PECL ] ; [ (V)M5762D for LVDS ]</p> <p>Land pattern ( reference )</p> <p>Pad Connections : Pad 1 : Control voltage for VCTCXO. Do not make any connection for TCXO. Pad 2 : Tri-state ; Pad 3 : Ground ; Pad 4 : PECL or LVDS output Pad 5 : Complimentary PECL or LVDS Output ; Pad 6 : Supply Voltage</p>

TCXOs VCTCXOs

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# " OCXO " [ Oven Controlled Crystal Oscillators ]

" OC\_\_E " series ( True Sine Wave )

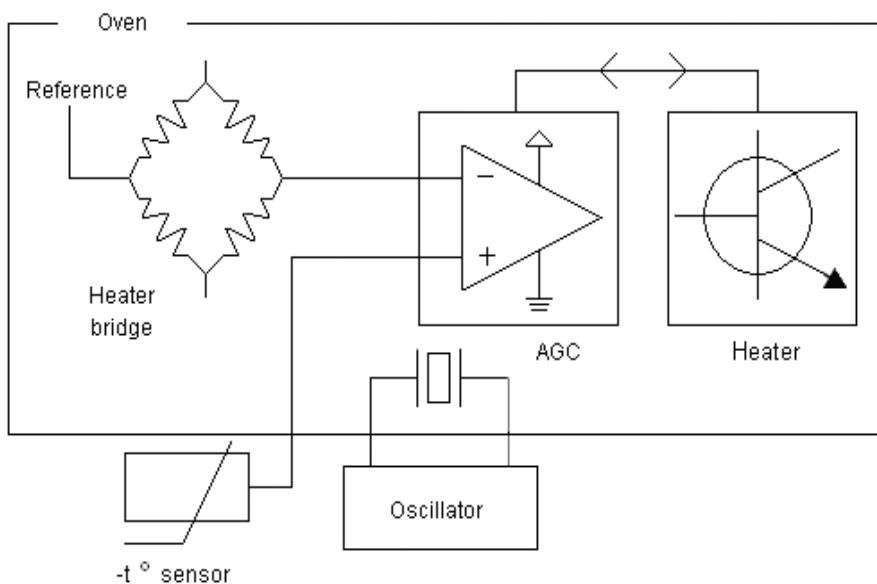
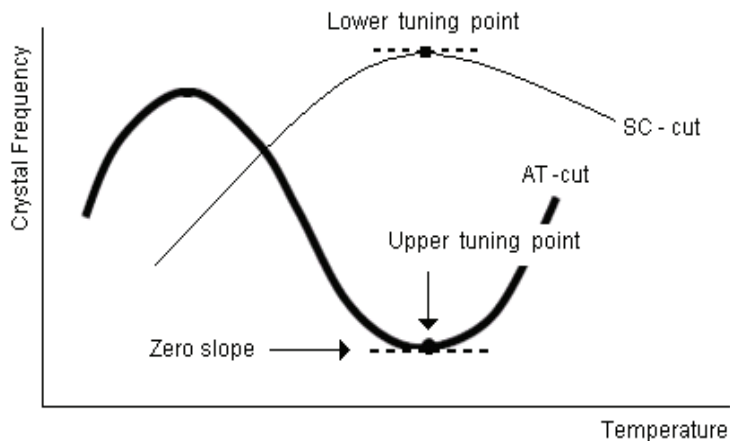
" OC\_\_T " series ( Square Wave )

## What is an OCXO ?

Relatively speaking , an OCXO perform in the  $\pm 0.01 \sim \pm 0.1\text{ppm}$  range , a TCXO performs in the  $\pm 1.0 \sim \pm 5.0\text{ppm}$  range while a non - compensated clock oscillator performs in the  $\pm 25$  with  $\pm 50\text{ppm}$  range .

A TCXO relies upon on a resistor / capacitor compensation network to counter the crystal's temperature - dependent frequency behavior . An OCXO has a crystal that is " ovenized " . This means the crystal " sees " a constant temperature regardless of the ambient temperature condition . The oven consists of a proportional heater ( power transistor ) and an automatic gain control ( AGC ) circuit . Also , a thermister monitors the oven temperature and sends an offset signal to the AGC which then turns the power transistor on and off accordingly . Thermal gradient and heat loss are carefully controlled to minimize the set point fluctuation of the oven . The oven temperature is normally set near the upper tuning point ( UPT ) of the crystal's freq. - temp. curve . At the UPT , the slope is zero and ideally there is no frequency change if the crystal " sees " a constant temperature .

Applications for OCXOs include satellite radio beacons , Stratum 3 systems , PCS / GMS base stations , SONET clocks , frequency synthesizers and instrumentation .



OCXO

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# " OCXO " [ Oven Controlled Crystal Oscillators ]

## " OC\_\_T " series ( Square Wave )

Product Selection Guide [ CMOS output ] , Wave form code " T "

" OC14T __ " series [ Full / Size ( 20.3 * 13.2 * 10.5 mm ) , 4 pins ]										
" OC24T __ " series [ Gull Wing SMD of Full / Size ( 20.3 * 13.2 * 12.0 mm ) , 4 pins ]										
Supply Voltage		+ 12 V			+ 5 V			+ 3.3 V		
Part No.	HCMOS models	( OC14T12A )			( OC14T5A )			( OC14T33A )		
	RoHS Equivalent	( OC14GT12A )			( OC14GT5A )			( OC14GT33A )		
Frequency Range		10.0 ~ 40.0 MHz			1.25 ~ 100.0 MHz			1.25 ~ 100.0 MHz		
( Only AT - cut )					40.0 MHz ( max. ) for RoHS models			40.0 MHz ( max. ) for RoHS models		
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
Freq. Stability ( typ. )		± 0.2 ppm	± 0.3 ppm	± 0.5 ppm	± 0.2 ppm	± 0.3 ppm	± 0.5 ppm	± 0.2 ppm	± 0.3 ppm	± 0.5 ppm
Freq. Stability ( available )		± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.02 ppm	± 0.03 ppm	± 0.05 ppm	± 0.075 ppm	± 0.15 ppm	± 0.25 ppm

" OC18T __ " series [ 5 pins , ( 20.3 * 20.3 * 10.5 mm ) ]										
Supply Voltage		+ 12 V			+ 5 V					
Frequency Range		1.25 ~ 100.0 MHz			1.25 ~ 100.0 MHz					
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC18T12A )			( OC18T5A )					
	Freq. Stability	± 0.02 ppm	± 0.05 ppm	± 0.1 ppm	± 0.02 ppm	± 0.05 ppm	± 0.1 ppm	± 0.02 ppm	± 0.05 ppm	± 0.1 ppm
( SC - cut )	Part No.	( OC18T12S )			( OC18T5S )					
	Freq. Stability	± 0.01 ppm	± 0.01 ppm	± 0.03 ppm	± 0.01 ppm	± 0.01 ppm	± 0.03 ppm	± 0.01 ppm	± 0.01 ppm	± 0.03 ppm

" OC11T __ " series [ 5 pins , ( 25.4 * 25.4 * 16.0 mm ) ]										
Supply Voltage		+ 12 V			+ 5 V					
Frequency Range		1.25 ~ 100.0 MHz			1.25 ~ 100.0 MHz					
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC11T12A )			( OC11T5A )					
	Freq. Stability	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm
( SC - cut )	Part No.	( OC11T12S )			( OC11T12S )					
	Freq. Stability	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm

" OC30T __ " series [ 5 pins , ( 36.2 * 27.2 * 16.0 mm ) ]										
" OC31T __ " series [ same package as OC30T __ except different pin configurations , Euro. Package ]										
Supply Voltage		+ 12 V			+ 5 V					
Frequency Range		1.25 ~ 100.0 MHz			1.25 ~ 100.0 MHz					
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC30T12A )			( OC30T5A )					
	Freq. Stability	± 0.03 ppm	± 0.08 ppm	± 0.2 ppm	± 0.03 ppm	± 0.08 ppm	± 0.2 ppm	± 0.03 ppm	± 0.08 ppm	± 0.2 ppm
( SC - cut )	Part No.	( OC30T12S )			( OC30T5S )					
	Freq. Stability	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm

" OC40T __ " series [ 4 pins , ( 38.8 * 38.8 * 16.0 mm ) ]										
Supply Voltage		+ 12 V			+ 5 V					
Frequency Range		1.25 ~ 100.0 MHz			1.25 ~ 100.0 MHz					
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC40T12A )			( OC40T5A )					
	Freq. Stability	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm
( SC - cut )	Part No.	( OC40T12S )			( OC40T5S )					
	Freq. Stability	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm

" OC22T __ " series [ 7 pins , ( 50.8 * 50.8 * 19.0 mm ) ]										
Supply Voltage		+ 12 V			+ 5 V					
Frequency Range		5.0 ~ 20.0 MHz			5.0 ~ 20.0 MHz					
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC22T12A )			( OC22T5A )					
	Freq. Stability	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm
( SC - cut )	Part No.	( OC22T12S )			( OC22T5S )					
	Freq. Stability	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm

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# " OCXO " [ Oven Controlled Crystal Oscillators ]

## " OC\_\_E " series ( True Sine Wave )

Product Selection Guide [ True Sine output ] , 50 ohm load true sine wave. Wave form code " E "

" OC14E __ " series [ 4 pins , Full / Size ( 20.3 * 13.2 * 10.5 mm ) ]							
" OC24E __ " series [ Gull Wing SMD of Full / Size ( 20.3 * 13.2 * 12.0 mm ) , 4 pins ]							
Supply Voltage		+ 12 V		+ 5.0 V		+ 3.3 V	
Part No.	Sine Wave models	( OC14E12A )		( OC14T5A )		( OC1433A )	
	RoHS Equivalent	( OC14GT12A )		( OC14GT5A )		( OC14GT33A )	
Frequency Range		10.0 ~ 40.0 MHz		1.25 ~ 100.0 MHz		1.25 ~ 40.0 MHz	
( AT - cut only )				40.0 MHz ( max. ) for RoHS models			
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
Freq. Stability ( typ. )		± 0.2 ppm	± 0.3 ppm	± 0.5 ppm	± 0.2 ppm	± 0.3 ppm	± 0.5 ppm
Freq. Stability ( available )		± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.075 ppm	± 0.15 ppm	± 0.25 ppm

" OC18E __ " series [ 5 pins , ( 20.3 * 20.3 * 10.5 mm ) ]							
Supply Voltage		+ 12 V			+ 5 V		
Frequency Range		10.0 ~ 100.0 MHz			10.0 ~ 100.0 MHz		
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC18E12A )			( OC18E5A )		
	Freq. Stability	± 0.02 ppm	± 0.05 ppm	± 0.1 ppm	± 0.02 ppm	± 0.05 ppm	± 0.1 ppm
( ST - cut )	Part No.	( OC18E12S )			( OC18E5S )		
	Freq. Stability	± 0.01 ppm	± 0.01 ppm	± 0.03 ppm	± 0.01 ppm	± 0.01 ppm	± 0.03 ppm

" OC11E __ " series [ 5 pins , ( 25.4 * 25.4 * 16.0 mm ) ]							
Supply Voltage		+ 12 V			+ 5 V		
Frequency Range		10.0 ~ 100.0 MHz			10.0 ~ 100.0 MHz		
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC11E12A )			( OC11E5A )		
	Freq. Stability	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm
( ST - cut )	Part No.	( OC11E12S )			( OC11E5S )		
	Freq. Stability	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm

" OC30E __ " series [ 5 pins , ( 36.2 * 27.2 * 16.0 mm ) ]							
" OC31E __ " series [ same package as OC30T __ except different pin configurations , Euro. Package ]							
Supply Voltage		+ 12 V			+ 5 V		
Frequency Range		10.0 ~ 100.0 MHz			10.0 ~ 100.0 MHz		
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC30E12A )			( OC30E5A )		
	Freq. Stability	± 0.03 ppm	± 0.08 ppm	± 0.2 ppm	± 0.03 ppm	± 0.08 ppm	± 0.2 ppm
( ST - cut )	Part No.	( OC30E12S )			( OC30E5S )		
	Freq. Stability	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm

" OC40E __ " series [ 4 pins , ( 38.8 * 38.8 * 16.0 mm ) ]							
Supply Voltage		+ 12 V			+ 5 V		
Frequency Range		10.0 ~ 100.0 MHz			10.0 ~ 100.0 MHz		
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC40E12A )			( OC40E5A )		
	Freq. Stability	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm
( ST - cut )	Part No.	( OC40E12S )			( OC40E5S )		
	Freq. Stability	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm

" OC22E __ " series [ 7 pins , ( 50.8 * 50.8 * 19.0 mm ) ]							
Supply Voltage		+ 12 V			+ 5 V		
Frequency Range		5.0 ~ 20.0 MHz			5.0 ~ 20.0 MHz		
Operating Temp. Range		0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C	0 ~ +60°C	-20 ~ +70°C	-40 ~ +85°C
( AT - cut )	Part No.	( OC22E12A )			( OC22E5A )		
	Freq. Stability	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm	± 0.05 ppm	± 0.1 ppm	± 0.2 ppm
( ST - cut )	Part No.	( OC22E12S )			( OC22E5S )		
	Freq. Stability	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm	± 0.01 ppm	± 0.02 ppm	± 0.03 ppm

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**OC14T**

Square Wave

**OC14E**

True Sine Wave

Best stability  
±0.025 ppm

Voltage Control

Full Size

12V

5.0V

3.3V

Min.  
1.25 MHzMax.  
100 MHz

## Applications

- For high stability applications including Stratum 3 requiring low jitter and an overall frequency tolerance of <math>\leq \pm 0.6\text{ppm}</math> over 15 years.
- Full Size 4 pin dip full metal package
- +12.0V , +5.0V , +3.3V Supply Voltages
- AT - cut crystal
- Voltage control ( Electronic Frequency Tuning ) is standard .



## General Specifications ( at+25°C and specified input voltage )

Type of Crystal Cut Used		AT - cut , use " A " for crystal code .										
Output Wave Form		Square wave. Wave form code is " T "						True Sine Wave. Wave form code is " E "				
Supply Voltage		+12V		+5.0 V		+3.3 V		+12V		+5 V		
Supply Voltage range , " Voltage code "		+12V ± 0.5V , " 12 "		+5.0V ± 0.2V , " 5 "		+3.3V ± 0.15V , " 33 "		+12V ± 0.5V , " 12 "		+5.0V ± 0.2V , " 5 "		
Frequency Range		1.25 ~ 100.0 MHz		1.25 ~ 100.0 MHz		1.25 ~ 100.0 MHz		10.0 ~ 100.0 MHz		1.25 ~ 100.0 MHz		
Initial Calibration Tolerance		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )		
		Vcon = +2.5 V		Vcon = +2.5 V		Vcon = +1.65 V		Vcon = +2.5 V		Vcon = +2.5 V		
Frequency Stability	vs Temperature ( ppm )	typical	available	typical	available	typical	available	typical	available	typical	available	
	0 ~ 60 °C	± 0.2	± 0.05	± 0.2	± 0.025	± 0.2	± 0.075	± 0.2	± 0.05	± 0.2	± 0.075	
	-20 ~ 70 °C	± 0.3	± 0.1	± 0.3	± 0.05	± 0.3	± 0.15	± 0.3	± 0.1	± 0.3	± 0.15	
	-40 ~ 85 °C	± 0.5	± 0.2	± 0.5	± 0.1	± 0.5	± 0.25	± 0.5	± 0.2	± 0.5	± 0.25	
	vs Voltage Change	( ±0.5V ) <math>\leq \pm 0.1\text{ppm}</math>		( ±0.2V ) <math>\leq \pm 0.1\text{ppm}</math>		( ±0.15V ) <math>\leq \pm 0.1\text{ppm}</math>		( ±0.5V ) <math>\leq \pm 0.1\text{ppm}</math>		( ±0.2V ) <math>\leq \pm 0.1\text{ppm}</math>		
	vs Warm -up time (+25°C)	3 minutes ( max. )		3 minutes ( max. )		5 minutes ( max. )		3 minutes ( max. )		3 minutes ( max. )		
	vs Aging	± 0.7 ppm first year , <math>\leq \pm 4.0\text{ppm}</math> over 10 years .										
vs Load Change	$\leq \pm 0.01\text{ppm}$ , for ±5 % load condition change .											
Voltage Control	Freq. Deviation Range	± 4.0ppm ( min. ) , Reference to fo at +25°C										
On pin 1 ( EFC )	Control Voltage Range	( +12V , +5V ) 0.0 to 5.0V ; ( +3.3V ) 0.0 to 3.3V										
	Transfer Function	Positive : Increasing control voltage increases output frequency .										
( Electronic Freq. Tuning )	Input Impedance	47 K ohms ( min. )										
	EFC Linearity	± 10 % ( max. )										
Power	Power Dissipation ( at +25°C )	steady - state	1 W ( max. )		1.5W ( max. )		1.5W ( max. )		1 W ( max. )		1.5W ( max. )	
		turn-on	2 W ( max. )		2.5 W ( max. )		2.5 W ( max. )		2 W ( max. )		2.5 W ( max. )	
Output	Output Level ( for True Sine )	---		---		---		+3 dBm ( typ. ) into 50Ω load .				
	Harmonic ( for True Sine )	---		---		---		-10 dBc ( min. )				
	Spurious ( for True Sine )	---		---		---		-70 dBc ( min. )				
	Load ( Fan out )	10 LS		10 LS		10 LS or 47 pF		---		---		
	Output Logic High ( V <sub>OH</sub> )	+4.5 V ( min. )		+4.5 V ( min. )		+2.8 V ( min. )		---		---		
	Output Logic Low ( V <sub>OL</sub> )	+ 0.4 V ( max. )		+ 0.4 V ( max. )		+ 0.4 V ( max. )		---		---		
	Duty Cycle ( at 50% V <sub>CC</sub> )	50 % ± 10%										
	Rise and Fall Time ( 20% ~ 80% )	7 nS ( max. ) ( measured at 20% → 80% of waveform )										
	Phase Noise [ 10.0 MHz ] ( typical )	Offset	[ dBc / Hz ]	Offset	[ dBc / Hz ]	Offset	[ dBc / Hz ]	Offset	[ dBc / Hz ]	Offset	[ dBc / Hz ]	
		1 HZ	-60	1 HZ	-70	1 HZ	-80	1 HZ	-70	1 HZ	-80	
10 Hz		-90	10 Hz	-100	10 Hz	-110	10 Hz	-100	10 Hz	-110		
100 Hz		-120	100 Hz	-130	100 Hz	-135	100 Hz	-130	100 Hz	-135		
1 KHz		-130	1 KHz	-140	1 KHz	-145	1 KHz	-140	1 KHz	-145		
10 KHz		-140	10 KHz	-145	10 KHz	-150	10 KHz	-145	10 KHz	-150		
Storage Temperature	- 65°C to + 125°C											
Shock	2000 G's , 0.3 ms 1/2 sine											
Vibration	10 to 2000 Hz / 10 G's											

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<b>OC18T</b> Square Wave	<b>OC18E</b> True Sine Wave	Best stability ±0.01 ppm ( SC-cut ) ±0.02 ppm ( AT-cut )	Voltage Control	5 pins	12V	5.0V	Min. 1.25 MHz	Max. 100 MHz
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**Applications**

- OC18\_ ( 20.3 \* 20.3 \* 10.5 mm ), 5 pin solder sealed metal package
- +12.0V , +5.0V Supply Voltages
- " AT - cut " crystal or " SC - cut " crystal
- Voltage control ( Electronic Frequency Tuning ) is standard .



**General Specifications ( at+25°C and specified input voltage )**

Type of Crystal Cut Used		AT - cut , use " A " for crystal code ; SC - cut , use " S " for crystal code .										
Output Wave Form		Square wave. Wave form code is " T "					True Sine Wave. Wave form code is " E "					
Supply Voltage		+12V		+5 V		+12V		+5 V				
Supply Voltage range , " Voltage code "		+12V ± 0.5V , " 12 "		+5.0V ± 0.2V , " 5 "		+12V ± 0.5V , " 12 "		+5.0V ± 0.2V , " 5 "				
Frequency Range		1.25 ~ 100.0 MHz		1.25 ~ 100.0 MHz		10.0 ~ 100.0 MHz		10.0 ~ 100.0 MHz				
Initial Calibration Tolerance		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )				
		Vcon = +2.5 V		Vcon = +2.5 V		Vcon = +2.5 V		Vcon = +2.5 V				
Frequency Stability	vs Temperature ( ppm )	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut			
	0 ~ 60 °C	± 0.02	± 0.01	± 0.02	± 0.01	± 0.02	± 0.01	± 0.02	± 0.01			
	-20 ~ 70 °C	± 0.05	± 0.01	± 0.05	± 0.01	± 0.05	± 0.01	± 0.05	± 0.01			
	-40 ~ 85 °C	± 0.1	± 0.03	± 0.1	± 0.03	± 0.1	± 0.03	± 0.1	± 0.03			
	vs Warm -up time (+25°C)	AT - cut : 3.0 minutes ( max. ) , within ± 0.5 ppm of its reference frequency . SC - cut : 1.0 minutes ( max. ) , within ± 0.1 ppm of its reference frequency .										
	vs Aging	AT - cut : ± 5.0 ppb ( max. ) / day , ± 0.5 ppm ( max. ) / first year , ± 3.0 ppm ( max. ) over 10 years . SC - cut : ± 2.0 ppb ( max. ) / day , ± 0.1 ppm ( max. ) / first year , ± 0.5 ppm ( max. ) over 10 years .										
	vs Voltage Change	≤ ±20 ppb , supply voltage ±5 % variation										
vs Load Change	≤ ±20 ppb , for a ±5 % load condition change .											
Voltage Control	Freq. Deviation Range	AT - cut : ± 5.0ppm ( min. ) , ± 20ppm ( max. ) Reference to fo at +25°C and over operating temperature range . SC - cut : ± 0.5ppm ( min. ) , ± 2.0ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
On pin 1 (EFC)	Control Voltage Range	2.5 V ± 2.0 V										
	Transfer Function	Positive : Increasing control voltage increases output frequency .										
( Electronic Freq. Tuning )	Input Impedance	100 K ohms ( min. )										
	EFC Linearity	± 10 % ( max. )										
Power	Power Dissipation ( at +25°C )	200 mA ( max. ) at steady-state ; 500 mA ( max. ) at turn-on										
Output	Output Level ( for True Sine )	---		---		+3 dBm ( typ. ) , +8 dBm ( max. ) into 50Ω load .						
	Harmonic ( for True Sine )	---		---		-30 dBc ( min. )						
	Spurious ( for True Sine )	---		---		-75 dBc ( min. )						
	Load ( Fan out )	15 pF HCMOS ( max. )										
	Output Logic High ( VOH )	+4.5 V ( min. )										
	Output Logic Low ( VOL )	+ 0.5 V ( max. )										
	Duty Cycle ( at 50% Vcc )	50 % ± 10%										
	Rise and Fall Time ( 20% ~ 80% )	5 nS ( max. ) ( measured at 20% → 80% of waveform )										
	Reference Voltage	+ 4.0 VD.C. ± 0.3 VD.C. or custom										
	Phase Noise ( 10.0 MHz ) [ Unit : dBc / Hz ] ( typical )	Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type
AT - cut			SC - cut	AT - cut		SC - cut	AT - cut		SC - cut	AT - cut		SC - cut
1 HZ		-75	-85	1 HZ	-75	-85	1 HZ	-75	-85	1 HZ	-75	-85
10 Hz		-100	-120	10 Hz	-100	-120	10 Hz	-100	-120	10 Hz	-100	-120
100 Hz		-130	-140	100 Hz	-130	-140	100 Hz	-130	-140	100 Hz	-130	-140
1 KHz		-140	-145	1 KHz	-140	-145	1 KHz	-140	-145	1 KHz	-140	-145
10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	
Storage Temperature	- 55°C to + 125°C											
Shock	2000 G's , 0.3 ms 1/2 sine											
Vibration	10 to 2000 Hz / 10 G's											

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OCXO

**OC11T**

Square Wave

**OC11E**

True Sine Wave

Best stability

 $\pm 0.01$  ppm ( SC-cut ) $\pm 0.05$  ppm ( AT-cut )

Voltage Control

5 pins

12V

5.0V

Min.

1.25 MHz

Max.

100 MHz

## Applications

- OC11\_ ( 25.4 \* 25.4 \* 16.0 mm ) , 5 pin solder sealed metal package
- +12.0V , +5.0V Supply Voltages
- " AT - cut " crystal and " SC - cut " crystal
- Voltage control ( Electronic Frequency Tuning ) is standard .



## General Specifications ( at+25°C and specified input voltage )

Type of Crystal Cut Used		AT - cut , use " A " for crystal code ; SC - cut , use " S " for crystal code .										
Output Wave Form		Square wave. Wave form code is " T "				True Sine Wave. Wave form code is " E "						
Supply Voltage		+12V		+5 V		+12V		+5 V				
Supply Voltage range , " Voltage code "		+12V $\pm$ 0.5V , " 12 "		+5.0V $\pm$ 0.2V , " 5 "		+12V $\pm$ 0.5V , " 12 "		+5.0V $\pm$ 0.2V , " 5 "				
Frequency Range		1.25 ~ 100.0 MHz		1.25 ~ 100.0 MHz		10.0 ~ 100.0 MHz		10.0 ~ 100.0 MHz				
Initial Calibration Tolerance		$\pm$ 0.5 ppm ( max. )		$\pm$ 0.5 ppm ( max. )		$\pm$ 0.5 ppm ( max. )		$\pm$ 0.5 ppm ( max. )				
		Vcon = +2.5 V		Vcon = +2.5 V		Vcon = +2.5 V		Vcon = +2.5 V				
Frequency Stability	vs Temperature ( ppm )	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut			
	0 ~ 60 °C	$\pm$ 0.05	$\pm$ 0.01	$\pm$ 0.05	$\pm$ 0.01	$\pm$ 0.05	$\pm$ 0.01	$\pm$ 0.05	$\pm$ 0.01			
	-20 ~ 70°C	$\pm$ 0.1	$\pm$ 0.02	$\pm$ 0.1	$\pm$ 0.02	$\pm$ 0.1	$\pm$ 0.02	$\pm$ 0.1	$\pm$ 0.02			
	-40 ~ 85°C	$\pm$ 0.2	$\pm$ 0.03	$\pm$ 0.2	$\pm$ 0.03	$\pm$ 0.2	$\pm$ 0.03	$\pm$ 0.2	$\pm$ 0.03			
	vs Warm -up time (+25°C)	AT - cut : 3.0 minutes ( max. ) , within $\pm$ 0.5 ppm of its reference frequency . SC - cut : 1.0 minutes ( max. ) , within $\pm$ 0.1 ppm of its reference frequency .										
	vs Aging	AT - cut : $\pm$ 5.0 ppb ( max. ) / day , $\pm$ 0.5 ppm ( max. ) / first year , $\pm$ 3.0 ppm ( max. ) over 10 years . SC - cut : $\pm$ 2.0 ppb ( max. ) / day , $\pm$ 0.1 ppm ( max. ) / first year , $\pm$ 0.5 ppm ( max. ) over 10 years .										
	vs Voltage Change	$\leq$ $\pm$ 20 ppb , supply voltage $\pm$ 5 % variation										
vs Load Change	$\leq$ $\pm$ 20 ppb , for a $\pm$ 5 % load condition change .											
Voltage Control	Freq. Deviation Range	AT - cut : $\pm$ 5.0 ppm ( min. ) , $\pm$ 20ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
		SC - cut : $\pm$ 0.5 ppm ( min. ) , $\pm$ 2.0ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
On pin 1 (EFC)	Control Voltage Range	2.5 V $\pm$ 2.0 V										
	Transfer Function	Positive : Increasing control voltage increases output frequency .										
( Electronic Freq. Tuning )	Input Impedance	100 K ohms ( min. )										
	EFC Linearity	$\pm$ 10 % ( max. )										
Power	Power Dissipation ( at +25°C )	1 Watt ( max. ) at steady-state ; 3 Watt ( max. ) at turn-on										
Output	Output Level ( for True Sine )	---		---		+3 dBm ( typ. ) , +8 dBm ( max. ) into 50 $\Omega$ load .						
	Harmonic ( for True Sine )	---		---		-30 dBc ( min. )						
	Spurious ( for True Sine )	---		---		-75 dBc ( min. )						
	Load ( Fan out )	15 pF HCMOS ( max. )										
	Output Logic High ( V <sub>OH</sub> )	+4.5 V ( min. )										
	Output Logic Low ( V <sub>OL</sub> )	+ 0.5 V ( max. )										
	Duty Cycle ( at 50% V <sub>cc</sub> )	50 % $\pm$ 10%										
	Rise and Fall Time ( 20% ~ 80% )	5 nS ( max. ) ( measured at 20% $\rightarrow$ 80% of waveform )										
	Reference Voltage	+ 4.0 VD.C. $\pm$ 0.3 VD.C. or custom										
	Phase Noise ( 10.0 MHz ) [ Unit : dBc / Hz ] ( typical )	Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type
AT - cut			SC - cut	AT - cut		SC - cut	AT - cut		SC - cut	AT - cut		SC - cut
1 HZ		-75	-80	1 HZ	-75	-80	1 HZ	-75	-80	1 HZ	-75	-80
10 Hz		-100	-120	10 Hz	-100	-120	10 Hz	-100	-120	10 Hz	-100	-120
100 Hz		-130	-140	100 Hz	-130	-140	100 Hz	-130	-140	100 Hz	-130	-140
1 KHz		-140	-145	1 KHz	-140	-145	1 KHz	-140	-145	1 KHz	-140	-145
10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	
Storage Temperature	- 55°C to + 125°C											
Shock	2000 G's , 0.3 ms 1/2 sine											
Vibration	10 to 2000 Hz / 10 G's											

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**OC30T**

Square Wave

**OC30E**

True Sine Wave

Best stability

±0.01 ppm ( SC-cut )

±0.02 ppm ( AT-cut )

Voltage Control

5 pins

12V

5.0V

Min.

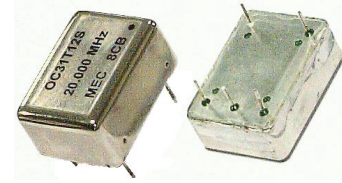
1.25 MHz

Max.

100 MHz

## Applications

- OC30\_ ( 36.2 \* 27.2 \* 16.0 mm ) , 5 pin solder sealed metal package
- +12.0V , +5.0V Supply Voltages
- " AT - cut " crystal or " SC - cut " crystal
- Voltage control ( Electronic Frequency Tuning ) is standard .



## General Specifications ( at+25°C and specified input voltage )

Type of Crystal Cut Used		AT - cut , use " A " for crystal code ; SC - cut , use " S " for crystal code .										
Output Wave Form		Square wave. Wave form code is " T "				True Sine Wave. Wave form code is " E "						
Supply Voltage		+12V		+5 V		+12V		+5 V				
Supply Voltage range , " Voltage code "		+12V ± 0.5V , " 12 "		+5.0V ± 0.2V , " 5 "		+12V ± 0.5V , " 12 "		+5.0V ± 0.2V , " 5 "				
Frequency Range		1.25 ~ 100.0 MHz		1.25 ~ 100.0 MHz		10.0 ~ 100.0 MHz		10.0 ~ 100.0 MHz				
Initial Calibration Tolerance		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )		± 0.5 ppm ( max. )				
		Vcon = +2.5 V		Vcon = +2.5 V		Vcon = +2.5 V		Vcon = +2.5 V				
Frequency Stability	vs Temperature ( ppm )	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut			
	0 ~ 60 °C	± 0.03	± 0.01	± 0.03	± 0.01	± 0.03	± 0.01	± 0.03	± 0.01			
	-20 ~ 70°C	± 0.08	± 0.02	± 0.08	± 0.02	± 0.08	± 0.02	± 0.08	± 0.02			
	-40 ~ 85°C	± 0.2	± 0.03	± 0.2	± 0.03	± 0.2	± 0.03	± 0.2	± 0.03			
	vs Warm -up time (+25°C)	AT - cut : 3.0 minutes ( max. ) , within ± 0.5 ppm of its reference frequency . SC - cut : 1.0 minutes ( max. ) , within ± 0.1 ppm of its reference frequency .										
	vs Aging	AT - cut : ± 5.0 ppb ( max. ) / day , ± 0.5 ppm ( max. ) / first year , ± 3.0 ppm ( max. ) over 10 years . SC - cut : ± 2.0 ppb ( max. ) / day , ± 0.1 ppm ( max. ) / first year , ± 0.5 ppm ( max. ) over 10 years .										
vs Voltage Change		≤ ±20 ppb , supply voltage ±5 % variation										
vs Load Change		≤ ±20 ppb , for a ±5 % load condition change .										
Voltage Control	Freq. Deviation Range	AT - cut : ± 5.0 ppm ( min. ) , ± 20ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
		SC - cut : ± 0.5 ppm ( min. ) , ± 2.0ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
On pin 1 (EFC)	Control Voltage Range	2.5 V ± 2.0 V										
	Transfer Function	Positive : Increasing control voltage increases output frequency .										
( Electronic Freq. Tuning )	Input Impedance	100 K ohms ( min. )										
	EFC Linearity	± 10 % ( max. )										
Power	Power Dissipation ( at+25°C )	1.2 Watt ( max. ) at steady-state ; 3.5 Watt ( max. ) at turn-on										
Output	Output Level ( for True Sine )	---		---		+3 dBm ( typ. ) , +8 dBm ( max. ) into 50Ω load .						
	Harmonic ( for True Sine )	---		---		-30 dBc ( min. )						
	Spurious ( for True Sine )	---		---		-75 dBc ( min. )						
	Load ( Fan out )	15 pF HCMOS ( max. )				---		---				
	Output Logic High ( V <sub>OH</sub> )	+4.5 V ( min. )				---		---				
	Output Logic Low ( V <sub>OL</sub> )	+ 0.5 V ( max. )				---		---				
	Duty Cycle ( at 50% V <sub>cc</sub> )	50 % ± 10%				---		---				
	Rise and Fall Time ( 20% ~ 80% )	5 nS ( max. ) ( measured at 20% → 80% of waveform )				---		---				
	Reference Voltage	+ 4.0 VD.C. ± 0.3 VD.C. or custom										
	Phase Noise ( 10.0 MHz ) [ Unit : dBc / Hz ] ( typical )	Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type
AT - cut			SC - cut	AT - cut		SC - cut	AT - cut		SC - cut	AT - cut		SC - cut
1 HZ		-75	-85	1 HZ	-75	-85	1 HZ	-75	-85	1 HZ	-75	-85
10 Hz		-100	-120	10 Hz	-100	-120	10 Hz	-100	-120	10 Hz	-100	-120
100 Hz		-130	-140	100 Hz	-130	-140	100 Hz	-130	-140	100 Hz	-130	-140
1 KHz		-140	-145	1 KHz	-140	-145	1 KHz	-140	-145	1 KHz	-140	-145
10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	
Storage Temperature		- 55°C to + 125°C										
Shock		2000 G's , 0.3 ms 1/2 sine										
Vibration		10 to 2000 Hz / 10 G's										

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**OC40T**

Square Wave

**OC40E**

True Sine Wave

Best stability

 $\pm 0.01$  ppm ( SC-cut ) $\pm 0.05$  ppm ( AT-cut )

Voltage Control

5 pins

12V

5.0V

Min.

1.25 MHz

Max.

100 MHz

## Applications

- OC40\_ ( 38.8 \* 38.8 \* 16.0 mm ) , 5 pin solder sealed metal package
- +12.0V , +5.0V Supply Voltages
- " AT- cut " crystal and " SC - cut " crystal
- Voltage control ( Electronic Frequency Tuning ) is standard .



## General Specifications ( at+25°C and specified input voltage )

Type of Crystal Cut Used		AT - cut , use " A " for crystal code ; SC - cut , use " S " for crystal code .										
Output Wave Form		Square wave. Wave form code is " T "					True Sine Wave. Wave form code is " E "					
Supply Voltage ( $\pm 5\%$ )		+12V		+5 V			+12V		+5 V			
Supply Voltage range , " Voltage code "		+12V $\pm 0.5V$ , " 12 "		+5.0V $\pm 0.2V$ , " 5 "			+12V $\pm 0.5V$ , " 12 "		+5.0V $\pm 0.2V$ , " 5 "			
Frequency Range		1.25 ~ 100.0 MHz		1.25 ~ 100.0 MHz			10.0 ~ 100.0 MHz		10.0 ~ 100.0 MHz			
Initial Calibration Tolerance		$\pm 0.5$ ppm ( max. )		$\pm 0.5$ ppm ( max. )			$\pm 0.5$ ppm ( max. )		$\pm 0.5$ ppm ( max. )			
		Vcon = +2.5 V		Vcon = +2.5 V			Vcon = +2.5 V		Vcon = +2.5 V			
Frequency Stability	vs Temperature ( ppm )	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	
	0 ~ 60 °C	$\pm 0.05$	$\pm 0.01$	$\pm 0.05$	$\pm 0.01$	$\pm 0.05$	$\pm 0.01$	$\pm 0.05$	$\pm 0.01$	$\pm 0.05$	$\pm 0.01$	
	-20 ~ 70°C	$\pm 0.1$	$\pm 0.02$	$\pm 0.1$	$\pm 0.02$	$\pm 0.1$	$\pm 0.02$	$\pm 0.1$	$\pm 0.02$	$\pm 0.1$	$\pm 0.02$	
	-40 ~ 85°C	$\pm 0.2$	$\pm 0.03$	$\pm 0.2$	$\pm 0.03$	$\pm 0.2$	$\pm 0.03$	$\pm 0.2$	$\pm 0.03$	$\pm 0.2$	$\pm 0.03$	
	vs Warm -up time (+25°C)	AT - cut : 1 minutes ( max. ) , within $\pm 0.2$ ppm of its reference frequency . SC - cut : 1 minutes ( max. ) , within $\pm 0.05$ ppm of its reference frequency .										
	vs Aging	AT - cut : $\pm 3$ ppb ( max. ) / day , $\pm 0.5$ ppm ( max. ) / first year , $\pm 3.0$ ppm ( max. ) over 10 years . SC - cut : $\pm 2$ ppb ( max. ) / day , $\pm 0.1$ ppm ( max. ) / first year , $\pm 0.5$ ppm ( max. ) over 10 years .										
	vs Voltage Change	$\leq \pm 20$ ppb , supply voltage $\pm 5\%$ variation										
vs Load Change	$\leq \pm 20$ ppb , for a $\pm 5\%$ load condition change .											
Voltage Control	Freq. Deviation Range	AT - cut : $\pm 5.0$ ppm ( min. ) , $\pm 20$ ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
		SC - cut : $\pm 0.5$ ppm ( min. ) , $\pm 2.0$ ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
On pin 1 (EFC)	Control Voltage Range	2.5 V $\pm$ 2.0 V										
	Transfer Function	Positive : Increasing control voltage increases output frequency .										
( Electronic Freq. Tuning )	Input Impedance	100 K ohms ( min. )										
	EFC Linearity	$\pm 10\%$ ( max. )										
Power	Power Dissipation ( at +25°C )	1.2 Watt ( max. ) at steady-state ; 3.6 Watt ( max. ) at turn-on										
Output	Output Level ( for True Sine )	---		---			+2 dBm ( typ. ) , +8 dBm ( max. ) into 50 $\Omega$ load .					
	Harmonic ( for True Sine )	---		---			-25 dBc ( min. )					
	Spurious ( for True Sine )	---		---			-75 dBc ( min. )					
	Load ( Fan out )	15 pF HCMOS ( max. )										
	Output Logic High ( V <sub>OH</sub> )	+4.5 V ( min. )										
	Output Logic Low ( V <sub>OL</sub> )	+ 0.5 V ( max. )										
	Duty Cycle ( at 50% V <sub>cc</sub> )	50 % $\pm$ 10%										
	Rise and Fall Time ( 20% ~ 80% )	5 nS ( max. ) ( measured at 20% $\rightarrow$ 80% of waveform )										
	Reference Voltage	+ 4.0 VD.C. $\pm$ 0.3 VD.C. or custom										
	Phase Noise ( 10.0 MHz ) [ Unit : dBc / Hz ] ( typical )	Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type
AT - cut			SC - cut	AT - cut		SC - cut	AT - cut		SC - cut	AT - cut		SC - cut
1 HZ		-75	-85	1 HZ	-75	-85	1 HZ	-75	-85	1 HZ	-75	-85
10 Hz		-100	-120	10 Hz	-100	-120	10 Hz	-100	-120	10 Hz	-100	-120
100 Hz		-130	-140	100 Hz	-130	-140	100 Hz	-130	-140	100 Hz	-130	-140
1 KHz		-140	-145	1 KHz	-140	-145	1 KHz	-140	-145	1 KHz	-140	-145
10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	
Storage Temperature	- 55°C to + 125°C											
Shock	2000 G's , 0.3 ms 1/2 sine											
Vibration	10 to 2000 Hz / 10 G's											

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**OC22T**

Square Wave

**OC22E**

True Sine Wave

Best stability

 $\pm 0.01$  ppm ( SC-cut ) $\pm 0.05$  ppm ( AT-cut )

Voltage Control

7 pins

12V

5.0V

Min.

5.0 MHz

Max.

20.0 MHz

## Applications

- OC22\_ ( 50.8 \* 50.8 \* 16.0 mm ) , 7 pin solder sealed metal package
- +12.0V , +5.0V Supply Voltages
- " AT- cut " crystal and " SC - cut " crystal
- Voltage control ( Electronic Frequency Tuning ) is standard .



## General Specifications ( at+25°C and specified input voltage )

Type of Crystal Cut Used		AT - cut , use " <b>A</b> " for crystal code ; SC - cut , use " <b>S</b> " for crystal code .										
Output Wave Form		Square wave. Wave form code is " T "					True Sine Wave. Wave form code is " E "					
Supply Voltage ( $\pm 5\%$ )		+12V		+5 V			+12V		+5 V			
Supply Voltage range , " Voltage code "		+12V $\pm 0.5V$ , " 12 "		+5.0V $\pm 0.2V$ , " 5 "			+12V $\pm 0.5V$ , " 12 "		+5.0V $\pm 0.2V$ , " 5 "			
Frequency Range		5.0 ~ 20.0 MHz					5.0 ~ 20.0 MHz					
Initial Calibration Tolerance		$\pm 0.1$ ppm ( max. )		$\pm 0.1$ ppm ( max. )			$\pm 0.1$ ppm ( max. )		$\pm 0.1$ ppm ( max. )			
		Vcon = +2.5 V		Vcon = +2.5 V			Vcon = +2.5 V		Vcon = +2.5 V			
Frequency Stability	vs Temperature ( ppm )	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	AT - cut	SC - cut	
	0 ~ 60 °C	$\pm 0.05$	$\pm 0.01$	$\pm 0.05$	$\pm 0.01$	$\pm 0.05$	$\pm 0.01$	$\pm 0.05$	$\pm 0.01$	$\pm 0.05$	$\pm 0.01$	
	-20 ~ 70°C	$\pm 0.1$	$\pm 0.02$	$\pm 0.1$	$\pm 0.02$	$\pm 0.1$	$\pm 0.02$	$\pm 0.1$	$\pm 0.02$	$\pm 0.1$	$\pm 0.02$	
	-40 ~ 85°C	$\pm 0.2$	$\pm 0.03$	$\pm 0.2$	$\pm 0.03$	$\pm 0.2$	$\pm 0.03$	$\pm 0.2$	$\pm 0.03$	$\pm 0.2$	$\pm 0.03$	
	vs Warm -up time (+25°C)	AT - cut : 1 minutes ( max. ) , within $\pm 0.2$ ppm of its reference frequency . SC - cut : 5 minutes ( max. ) , within $\pm 10$ ppb of its reference frequency .										
	vs Aging	AT - cut : $\pm 3$ ppb ( max. ) / day , $\pm 0.5$ ppm ( max. ) / first year , $\pm 3.0$ ppm ( max. ) over 10 years . SC - cut : $\pm 0.5$ ppb ( max. ) / day , $\pm 50$ ppb ( max. ) / first year , $\pm 150$ ppb ( max. ) over 10 years .										
	vs Voltage Change	$\leq \pm 1.0$ ppb , supply voltage $\pm 5\%$ variation										
vs Load Change	$\leq \pm 1.0$ ppb , for a $\pm 5\%$ load condition change .											
Voltage Control	Freq. Deviation Range	AT - cut : $\pm 5.0$ ppm ( min. ) , $\pm 20$ ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
		SC - cut : $\pm 0.5$ ppm ( min. ) , $\pm 1.0$ ppm ( max. ) Reference to fo at +25°C and over operating temperature range .										
On pin 1 ( EFC )	Control Voltage Range	2.5 V $\pm 2.0$ V										
	Transfer Function	Positive : Increasing control voltage increases output frequency .										
( Electronic Freq. Tuning )	Input Impedance	100 K ohms ( min. )										
	EFC Linearity	$\pm 10\%$ ( max. )										
Power	Power Dissipation ( at +25°C )	2.0 Watt ( max. ) at steady-state ; 6.0 Watt ( max. ) at turn-on										
Output	Output Level ( for True Sine )	---		---			+2 dBm ( typ. ) , +8 dBm ( max. ) into 50Ω load .					
	Harmonic ( for True Sine )	---		---			-30 dBc ( min. )					
	Spurious ( for True Sine )	---		---			-75 dBc ( min. )					
	Load ( Fan out )	15 pF HCMOS ( max. )										
	Output Logic High ( V <sub>OH</sub> )	+4.5 V ( min. )										
	Output Logic Low ( V <sub>OL</sub> )	+ 0.5 V ( max. )										
	Duty Cycle ( at 50% V <sub>CC</sub> )	50 % $\pm 5\%$										
	Rise and Fall Time ( 20% ~ 80% )	5 nS ( max. ) ( measured at 20% $\rightarrow$ 80% of waveform )										
	Reference Voltage	+ 4.0 VD.C. $\pm 0.3$ VD.C. or custom										
	Phase Noise ( 10.0 MHz ) [ Unit : dBc / Hz ] ( typical )	Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type		Offset	Crystal Type
AT - cut			SC - cut	AT - cut		SC - cut	AT - cut		SC - cut	AT - cut		SC - cut
1 HZ		-75	-85	1 HZ	-75	-85	1 HZ	-75	-85	1 HZ	-75	-85
10 Hz		-100	-120	10 Hz	-100	-120	10 Hz	-100	-120	10 Hz	-100	-120
100 Hz		-130	-140	100 Hz	-130	-140	100 Hz	-130	-140	100 Hz	-130	-140
1 KHz		-140	-145	1 KHz	-140	-145	1 KHz	-140	-145	1 KHz	-140	-145
10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	10 KHz	-150	-150	
Storage Temperature	- 55°C to + 125°C											
Shock	2000 G's , 0.3 ms 1/2 sine											
Vibration	10 to 2000 Hz / 10 G's											

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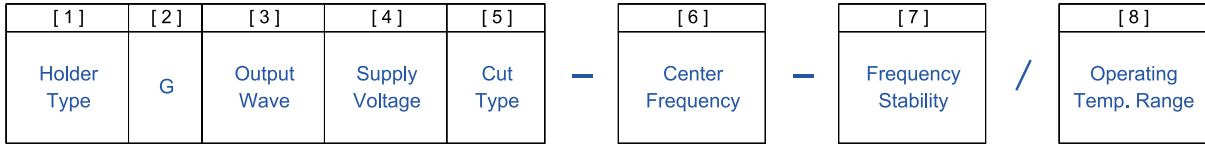
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# " OCXO " [ Oven Controlled Crystal Oscillators ]

True Sine wave " OC \_ E "

Square wave " OC \_ T "

## Part Number Format and Example



Examples	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)
(1)	OC14	G	T	3.3	A	10.000	0.5	-40+85
(2)	OC30	G	E	12	S	100.000	0.02	-20+70
(3)	OC22	G	T	5	A	20.000	0.2	-30+75

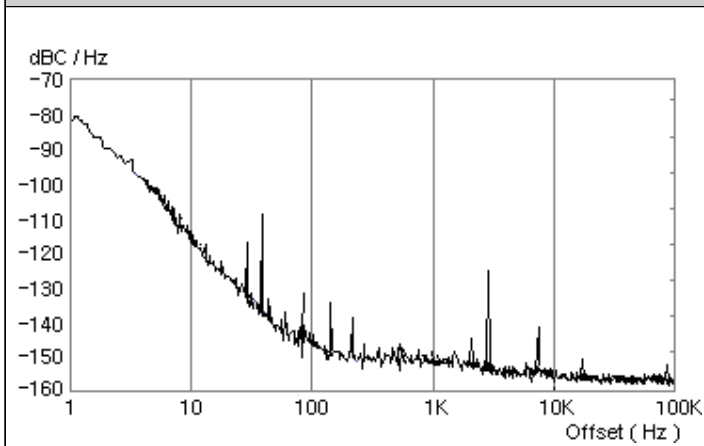
Ex (1): OC14GT33A - 10.000 - 0.5 / -40+85 [ OC14 type, RoHS, CMOS output, 3.3V, 10.000MHz, ± 0.5ppm from -40°C to 85°C ]

Ex (2): OC30GE12S - 100.000 - 0.02 / -20+70 [ OC30 type, RoHS, True Sine wave, 12V, 100.000MHz, ± 0.02ppm from -20°C to 70°C ]

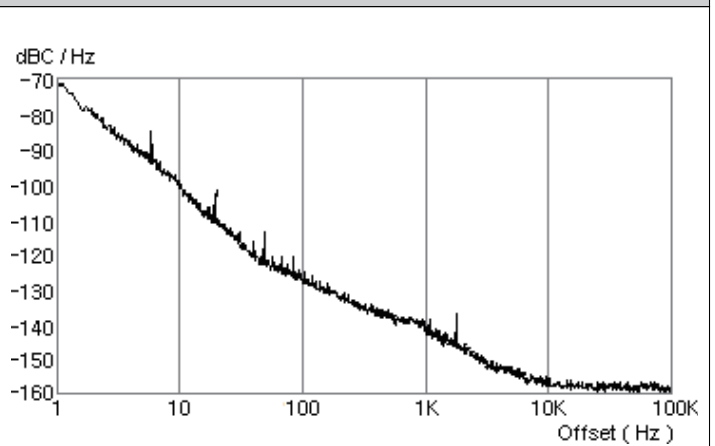
Ex (3): OC22GT5A - 20.000 - 0.2 / -30+75 [ OC22 type, RoHS, CMOS output, 5.0V, 20.000MHz, ± 0.2ppm from -30°C to 75°C ]

[1]	Holder Type "OC__" stands for OCXO,
[2]	Please add "G" after the "type code" for RoHS compliant
[3]	"T" stands for Square Wave, "E" stands for True Sine Wave ex 1: OC14T, OC14 package, CMOS output; ex 2: OC30E, OC30 package, True Sine wave
[4]	Supply voltage, "3" for 3.3V D.C, "5" for 5.0V D.C, "12" for 12V D.C
[5]	Type of crystal used; "A" stands for AT-cut crystal, "S" stands for SC-cut crystal
[6]	Center Frequency in MHz
[7]	Frequency stability in ± ppm; ex 1: ± 0.5ppm — 0.5, ex 2: ± 0.02ppm — 0.02
[8]	Operating temperature range in °C ex 1: -10 °C to 60°C — -10+60; ex 2: -20 °C to 70°C — -20+70; ex 3: -40 °C to 85°C — -40+85

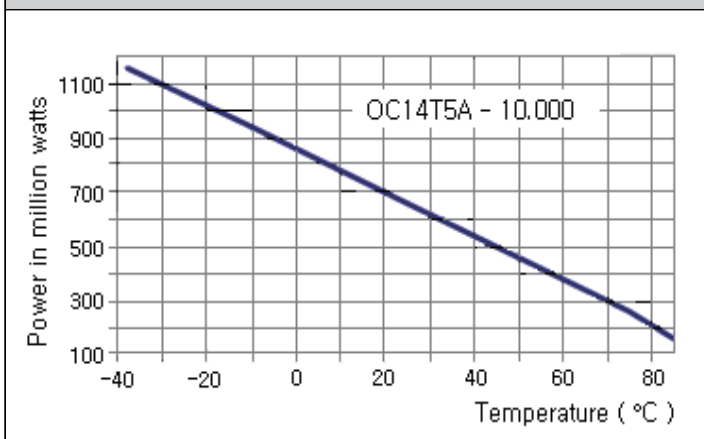
SSB Phase Noise : OC30E12S-10.000 ( SC-cut crystal )



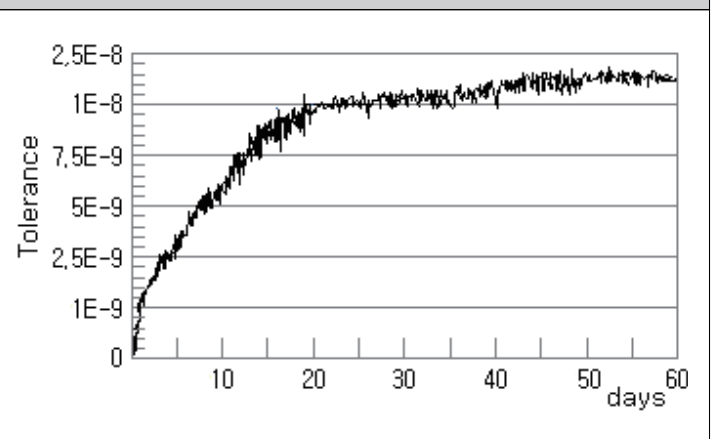
SSB Phase Noise : OC14T5A-10.000 ( AT-cut crystal )



Power Consumption vs Temperature



Aging : OC30E12S-10.000 ( SC - cut crystal )



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# " OCXO " [ Oven Controlled Crystal Oscillators ]

True Sine wave " OC\_E "

Square wave " OC\_T "

## Test Circuits

" OC14T_ " ( square wave )	" OC24T_ " ( square wave )
<p style="text-align: center;">Square Wave Output Load : 15pF True Sine Wave Output Load : 50 Ω (* Include probe . fixture and stray capacitance )</p>	<p style="text-align: center;">Square Wave Output Load : 15pF True Sine Wave Output Load : 50 Ω (* Include probe . fixture and stray capacitance )</p>
" OC11T_ " ( square wave ) and " OC11E " ( true sine wave )	" OC18T_ " ( square wave ) and " OC18E " ( true sine wave )
<p style="text-align: center;">Square Wave Output Load : 15pF True Sine Wave Output Load : 50 Ω (* Include probe . fixture and stray capacitance )</p>	<p style="text-align: center;">Square Wave Output Load : 15pF True Sine Wave Output Load : 50 Ω (* Include probe . fixture and stray capacitance )</p>
" OC30T_ " ( square wave ) and " OC30E " ( true sine wave )	" OC31T_ " ( square wave ) and " OC31E " ( true sine wave )
<p style="text-align: center;">Square Wave Output Load : 15pF True Sine Wave Output Load : 50 Ω (* Include probe . fixture and stray capacitance )</p>	<p style="text-align: center;">Square Wave Output Load : 15pF True Sine Wave Output Load : 50 Ω (* Include probe . fixture and stray capacitance )</p>
" OC40T_ " ( square wave ) and " OC40E " ( true sine wave )	" OC22T_ " ( square wave ) and " OC22E " ( true sine wave )
<p style="text-align: center;">Square Wave Output Load : 15pF True Sine Wave Output Load : 50 Ω (* Include probe . fixture and stray capacitance )</p>	<p style="text-align: center;">Square Wave Output Load : 15pF True Sine Wave Output Load : 50 Ω (* Include probe . fixture and stray capacitance )</p>

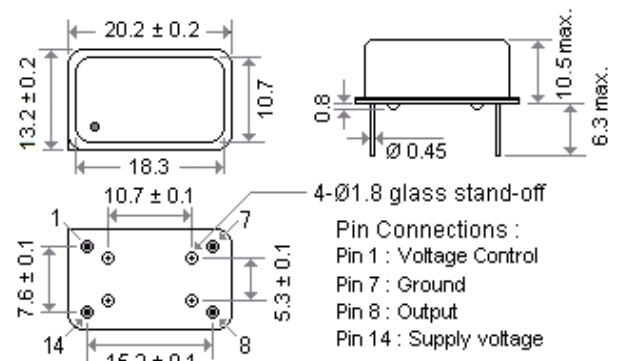
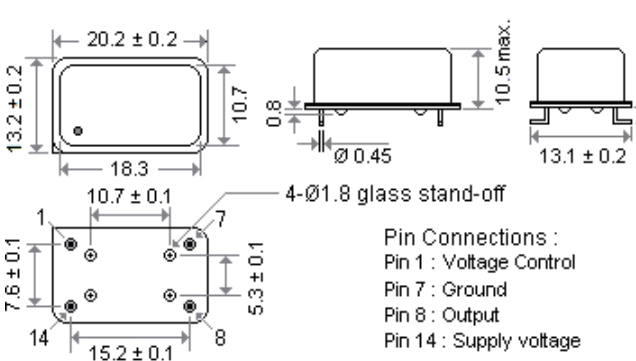
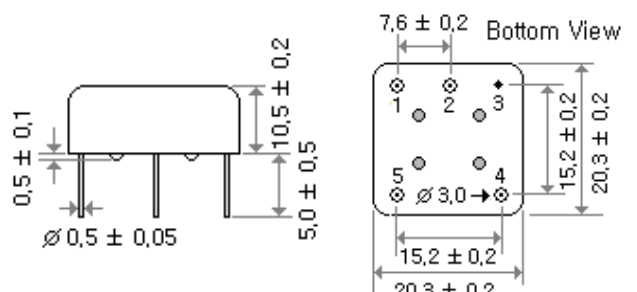
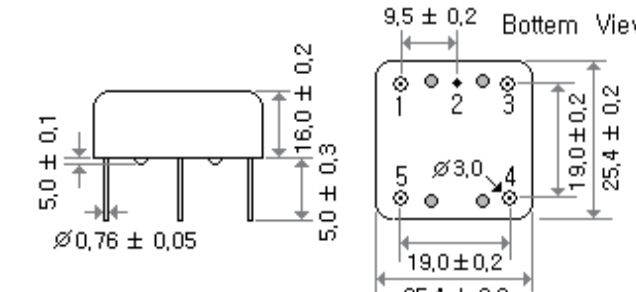
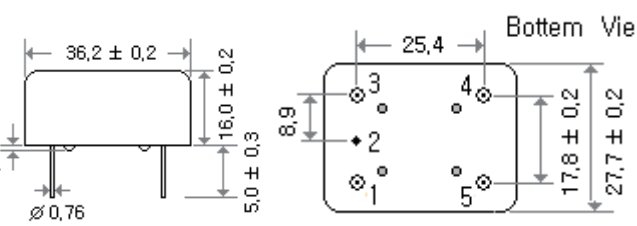
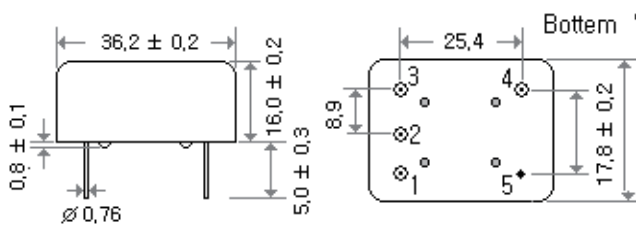
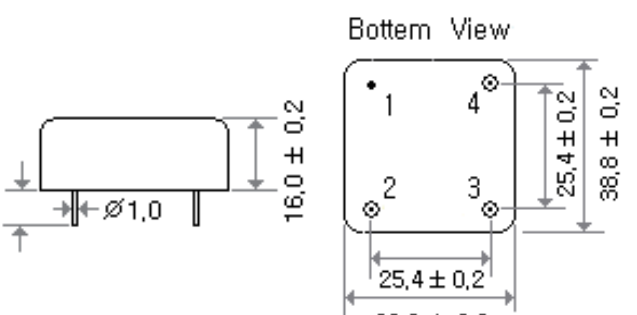
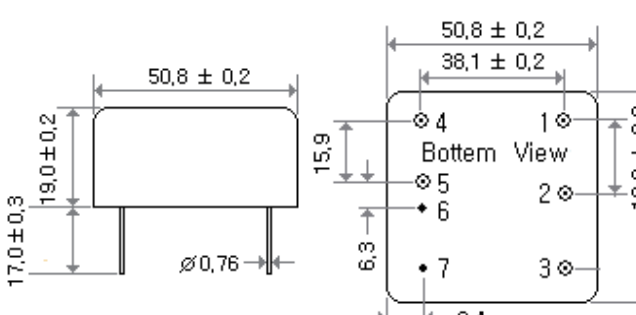
OCXO

# " OCXO " [ Oven Controlled Crystal Oscillators ]

True Sine wave " OC \_ E "

Square wave " OC \_ T "

Outline Dimensions ( Unit : mm )

<p style="text-align: center;">[ OC14__ ]</p>  <p style="text-align: center;">4-<math>\varnothing</math>1.8 glass stand-off</p> <p>Pin Connections :          Pin 1 : Voltage Control          Pin 7 : Ground          Pin 8 : Output          Pin 14 : Supply voltage</p>	<p style="text-align: center;">[ OC24__ ]</p>  <p style="text-align: center;">4-<math>\varnothing</math>1.8 glass stand-off</p> <p>Pin Connections :          Pin 1 : Voltage Control          Pin 7 : Ground          Pin 8 : Output          Pin 14 : Supply voltage</p>
<p style="text-align: center;">[ OC18__ ]</p>  <p style="text-align: center;">Bottom View</p> <p>Pin Connections :          Pin 1 : Supply Voltage , Pin 2 : RF Output , Pin 3 : Ground / Case          Pin 4 : Voltage Control EFC , Pin 5 : Reference Voltage Output</p>	<p style="text-align: center;">[ OC11__ ]</p>  <p style="text-align: center;">Bottom View</p> <p>Pin Connections :          Pin 1 : RF Output , Pin 2 : Ground / Case , Pin 3 : Voltage Control EFC          Pin 4 : Reference Voltage Output , Pin 5 : Supply Voltage</p>
<p style="text-align: center;">[ OC30__ ]</p>  <p style="text-align: center;">Bottom View</p> <p>Pin Connections :          Pin 1 : Voltage Control EFC , Pin 2 : Ground , Case , Pin 3 : RF Output ,          Pin 4 : Reference Voltage Output , Pin 5 : Supply Voltage</p>	<p style="text-align: center;">[ OC31__ ]</p>  <p style="text-align: center;">Bottom View</p> <p>Pin Connections :          Pin 1 : Voltage Control EFC , Pin 2 : Reference Voltage Output ,          Pin 3 : Supply Voltage , Pin 4 : RF Output , Pin 5 : Ground / Case</p>
<p style="text-align: center;">[ OC40__ ]</p>  <p style="text-align: center;">Bottom View</p> <p>Pin Connections :          Pin 1 : Ground / Case , Pin 2 : Supply Voltage          Pin 3 : RF Output , Pin 4 : Control Voltage</p>	<p style="text-align: center;">[ OC22__ ]</p>  <p style="text-align: center;">Bottom View</p> <p>Pin Connections :          Pin 1 : Oven Return , Pin 2 : Osc. Supply Voltage ,          Pin 3 : Electronic Tuning , Pin 4 : Oven Supply Voltage , Pin 5 : Output ,          Pin 6 : RF Ground , Pin 7 : Ground / Case</p>

OCXO

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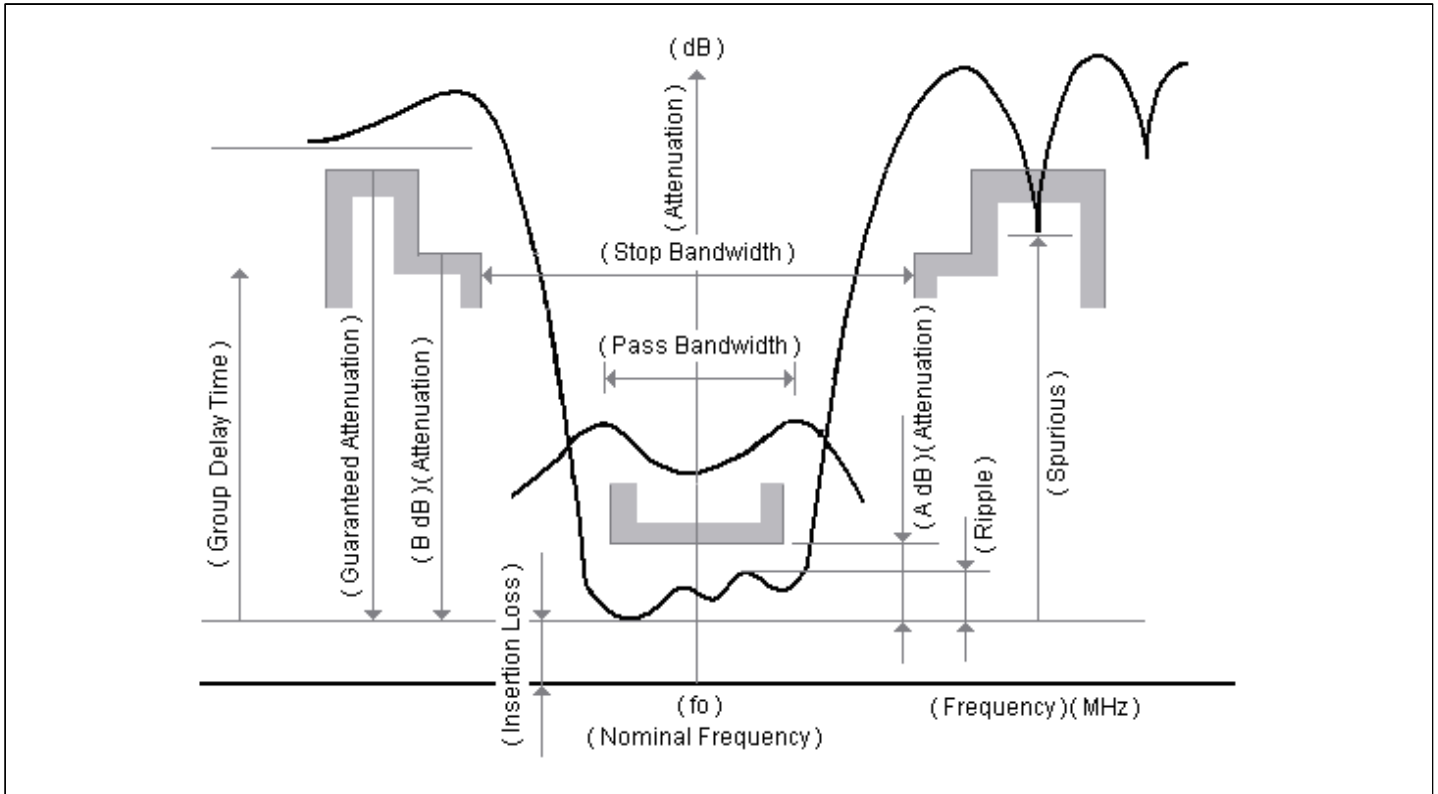
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M.C.F. ( Monolithic Crystal Filters ) features high quality quartz resonators with sharp cutoff characteristics , low loss , good inter-modulation and a high stability over a wide temperature range . Consider in applying band pass filters to communication systems .



RoHS Compliance

Characteristic diagram and terms of crystal filters



● Nominal Frequency :	This is the nominal value of the center frequency ( $f_0$ ) and is used as the reference frequency of related standards.
● Pass Bandwidth :	This is the frequency interval in which the relative attenuation (the attenuation from the minimum insertion loss) is equal to the specified value "A dB" (Usually 3dB).
● Insertion Loss :	This is the difference of attenuation when a filter is and isn't inserted. The minimum insertion loss is the minimum value of insertion loss and becomes as the reference level of attenuation characteristics specification. The constant loss is the insertion loss at the nominal frequency.
● Ripple :	This is the maximum value of the difference between the peak value of attenuation in the pass band and the minimum insertion loss.
● Stop Bandwidth :	This is the frequency interval in which the relative attenuation is equal to the specified value "B dB".
● Guaranteed Attenuation :	This is the relative attenuation guaranteed in the specified range within attenuation band scope.
● Spurious Response :	This is the value of relative attenuation generated by the secondary vibration in the specified range within attenuation band scope.
● Group Delay Time :	This is the difference between the maximum and the minimum value of the group delay in the specified range of the pass band.
● Terminating Impedance :	This is the impedance value terminated to the input and the output side of filter and is indicated by the resistance portion and the parallel capacity portion including the floating capacity.

# MQ

[ 7.0 \* 5.0 \* 1.3 mm ]

SMD ----- Ceramic Type

Fund.

21.4 MHz

21.7 MHz

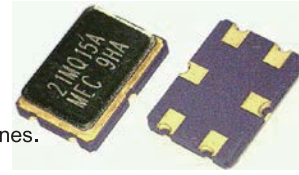
45.0 MHz

2 poles

4 poles

### Features

- Thin and light weight; excellent shock and vibration resistance
- Suitable for automatic pick and place; Solder reflow capable
- Specifically designed for mobile, wireless communications, pagers, cellular and cordless phones.



### Surface Mount Type [ Q series ( 21.400 , 21.700 , 45.000 MHz ) ]

Frequency ( MHz )	Model	No. of poles	Pass Bandwidth		Stop Bandwidth		Ripple	Insertion Loss	Guaranteed Attenuation		Terminating Impedance	
			dB	kHz (min.)	dB	kHz (max.)	dB (max.)	dB (max.)	dB	kHz	ohms // pF	Cc ( pF )
21.400	21MQ7.5A	2	3	± 3.75	20	± 18	1.0	2.0	70	-910	850 // 6.0	
	21MQ15A	2	3	± 7.5	18	± 25	0.5	1.5	70	-910	1500 // 2.5	
	21MQ15B	4	3	± 7.5	40	± 25	1.0	3.0	70	-910	1800 // 0.35	5.0
	21MQ30A	2	3	± 15	15	± 50	1.5	2.0	60	-910	2500 // 0	
21.700	21.7MQ15A	2	3	± 7.5	18	± 28	1.0	2.0	70	-910	1500 // 2.5	
	21.7MQ15B	4	3	± 7.5	40	± 25	1.0	3.0	70	-910	1750 // 0.35	5.0
	21.7MQ30A	2	3	± 15	15	± 50	1.5	2.0	50	-910	2500 // 0	
45.000	45MQ15A	2	3	± 7.5	15	± 25	1.0	2.0	70	-910	650 // 3.5	
	45MQ15B	4	3	± 7.5	30	± 25	1.0	3.0	80	-910	600 // 2.3	7.5
	45MQ30A	2	3	± 15	15	± 60	1.0	2.0	70	-910	1200 // 1.8	
	45MQ30B	4	3	± 15	30	± 40	1.0	3.0	70	-910	1200 // 1.0	2.5

( Operating Temperature Range : -20°C to +70°C ; Storage Temperature Range : -40°C to +85°C )

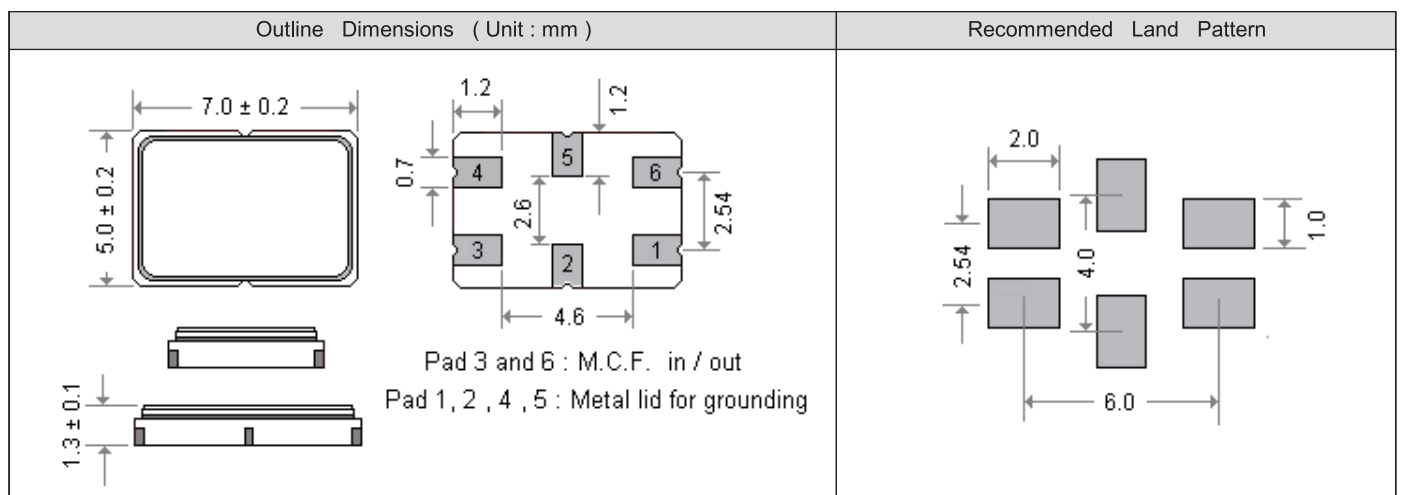
### Environmental and Mechanical Specifications

Green Requirement	RoHS compliant and Pb (lead free)
Gross Leak	60 sec min at +125°C in D.I. water or fluorocarbon fluid
Shock	Half sine wave acceleration of 100G peak amplitude for 11 m. sec. duration, 3 cycles each plane.
Vibration	±5 ppm max. Frequency:10 to 55 Hz, amplitude: 1.5 mm or 10 Gs rms. Duration : 6 hours.
Drop Test	Free drop onto hard wood board at 75 cm, 3 random drops.
Humidity	After 48 hours at 85°C, 85% relative humidity non-condensing
Thermal Shock	Temperature cycling: Exposed at -40°C for 30 minutes then to +85°C for 30 minutes for duration of 5 days.
Drop Test	Free drop onto hard wood board at 75 cm, 3 random drops.

### Part Number Format and Example



[1]	Freq. code : " 21 " for 21.400MHz , " 21.7 " for 21.700MHz , " 45 " for 45.000MHz ,
[2]	" Q " series for ( 7.0 * 5.0 * 1.3mm ) package
[3]	Pass band width ( 3dB ) (min.) * 2 [ " 7.5 " for ± 3.75kHz , " 15 " for ± 7.5kHz , " 30 " for ± 15kHz ]
[4]	No. of poles [ " A " for 2 poles ]



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# 49T

[ 11.5 \* 4.5 \* 11.2 mm ]

Dip Type	Fund.	10.7 MHz	10.8 MHz	2 poles	4 poles	6 poles	8 poles
Jacket Type							

Thru - Hole Type ( standard frequency 10.700 MHz ) ; available frequency range ( 10.695 ~ 10.800 MHz )

Channel Spacing ( kHz )	Model	No. of poles	Pass Bandwidth		Stop Bandwidth		Ripple dB (max.)	Insertion Loss dB (max.)	Guaranteed Attenuation		Terminating Impedance ohms // pF	Package	
			dB	kHz (min.)	dB	kHz (max.)			dB	kHz		Tandem set	One package
12.5	10M7.5A	2	3	± 3.75	20	± 18	0.5	1.5	35	±300 ~ ±1000	1.8K // 5.0	49T	
	10M7.5B	4	3	± 3.75	40	± 14	1.0	2.5	65	±300 ~ ±1000	1.8K // 4.5	49T a pair	
	10M7.5C	6	3	± 3.75	45	± 8.75	2.0	3.5	65	±12.5 ~ ±300	1.8K // 3.5	49T 3 pcs	L1
	10M7.5D	8	3	± 3.75	65	± 8.75	2.0	4.0	90	±12.5 ~ ±300	1.8K // 3.5	49T 4 pcs	L2
20.0	10M12A	2	3	± 6.0	18	± 25	0.5	2.0	35	±300 ~ ±1000	2.5K // 2.5	49T	
	10M12B	4	3	± 6.0	40	± 20	1.0	2.5	65	±300 ~ ±1000	2.5K // 1.5	49T a pair	
	10M12C	6	3	± 6.0	45	± 15	2.0	4.0	65	±20 ~ ±300	2.5K // 1.5	49T 3 pcs	L1
	10M12D	8	3	± 6.0	65	± 15	2.0	2.0	90	±20 ~ ±300	2.5K // 1.5	49T 4 pcs	L2
25.0	10M15A	2	3	± 7.5	18	± 25	0.5	1.5	35	±300 ~ ±1000	3.0K // 2.0	49T	
	10M15B	4	3	± 7.5	40	± 25	1.0	2.5	55	±300 ~ ±1000	3.0K // 1.5	49T a pair	
	10M15C	6	3	± 7.5	45	± 18	2.0	3.0	65	±25 ~ ±300	3.3K // 1.5	49T 3 pcs	L1
	10M15D	8	3	± 7.5	65	± 18	2.0	4.0	90	±25 ~ ±300	3.3K // 1.5	49T 4 pcs	L2
50.0	10M30A	2	3	± 15	15	± 50	0.5	1.5	30	±300 ~ ±1000	5.0K // 0	49T	
	10M30B	4	3	± 15	30	± 40	1.0	2.5	30	±300 ~ ±1000	5.5K // -1.0	49T a pair	
	10M30C	6	3	± 15	60	± 45	2.0	3.0	65	±45 ~ ±300	5.5K // -1.0	49T 3 pcs	L1
	10M30D	8	3	± 15	60	± 30	2.0	3.5	90	±50 ~ ±300	5.5K // -1.0	49T 4 pcs	L2

### Part Number Format and Example

49T	49TMJ									
4 pole M.C.F. ( Paired packages , Tandem set )	( L - 1 ) , ( L - 2 ) , ( L - 3 ) --- One Package Type									
<p>Color dots for pair orientation match</p> <p>User to provide Cc</p>	<table border="1"> <tr> <td></td> <td>L</td> <td>P</td> </tr> <tr> <td>L - 1</td> <td>15.0</td> <td>9.0</td> </tr> <tr> <td>L - 2</td> <td>18.5</td> <td>13.4</td> </tr> </table> <p>Pin 1 : Output Pin 2 : Ground Pin 3 : Ground Pin 4 : Input</p>		L	P	L - 1	15.0	9.0	L - 2	18.5	13.4
	L	P								
L - 1	15.0	9.0								
L - 2	18.5	13.4								

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M. C. F.

**U1**

[ 7.8 \* 3.1 \* 8.0 mm ]

**U5**

[ 7.8 \* 3.1 \* 6.0 mm ]

Dip Type

Jacket Type

Fund.

21.4MHz

45.0MHz

2 poles

4 poles

6 poles

8 poles

Thru - Hole Type ( standard frequency 10.700 MHz ) ; available frequency range ( 10.695 ~ 10.800 MHz )

Channel Spacing ( kHz )	Model	No. of poles	Pass Bandwidth		Stop Bandwidth		Ripple	Insertion Loss	Guaranteed Attenuation		Terminating Impedance	Package	
			dB	kHz (min.)	dB	kHz (max.)	dB (max.)	dB (max.)	dB	kHz	ohms // pF	Tandem set	One package
12.5	<b>21M7.5A</b>	2	3	± 3.75	20	± 18	0.5	1.5	35	±300 ~ ±1000	850 // 6.0	U-1 , U-5	
	<b>21M7.5B</b>	4	3	± 3.75	40	± 15	1.0	2.5	65	±300 ~ ±1000	850 // 5.0	a pair ( U -1,U-5 )	
	<b>21M7.5C</b>	6	3	± 3.75	45	± 8.75	2.0	3.0	65	±12.5 ~ ±300	850 // 5.0	3 pcs	S1
	<b>21M7.5D</b>	8	3	± 3.75	65	± 8.75	2.0	4.0	90	±12.5 ~ ±300	850 // 5.0	4 pcs	S2
20.0	<b>21M12A</b>	2	3	± 6.0	20	± 25	0.5	1.5	35	±300 ~ ±1000	1.2K // 3.0	U-1 , U-5	
	<b>21M12B</b>	4	3	± 6.0	40	± 20	1.0	2.5	65	±300 ~ ±1000	1.2K // 2.5	a pair ( U -1,U-5 )	
	<b>21M12C</b>	6	3	± 6.0	45	± 15	2.0	3.0	65	±20 ~ ±300	1.2K // 2.5	3 pcs	S1
	<b>21M12D</b>	8	3	± 6.0	65	± 15	2.0	4.0	90	±20 ~ ±300	1.2K // 2.5	4 pcs	S2
25.0	<b>21M15A</b>	2	3	± 7.5	18	± 25	0.5	1.5	35	±300 ~ ±1000	1.5K // 2.0	U-1 , U-5	
	<b>21M15B</b>	4	3	± 7.5	40	± 25	1.0	2.5	65	±300 ~ ±1000	1.5K // 2.0	a pair ( U -1,U-5 )	
	<b>21M15C</b>	6	3	± 7.5	45	± 18	2.0	3.0	65	±25 ~ ±300	1.5K // 2.0	3 pcs	S1
	<b>21M15D</b>	8	3	± 7.5	65	± 18	2.0	4.0	90	±25 ~ ±300	1.5K // 2.0	4 pcs	S2
50.0	<b>21M30A</b>	2	3	± 15	15	± 45	0.5	1.5	35	±300 ~ ±1000	1.5K // 1.0	U-1 , U-5	
	<b>21M30B</b>	4	3	± 15	40	± 50	1.0	2.5	65	±300 ~ ±1000	2.2K // 0.5	a pair ( U -1,U-5 )	
	<b>21M30C</b>	6	3	± 15	45	± 35	2.0	3.0	65	±45 ~ ±300	2.2K // 0.5	3 pcs	S1
	<b>21M30D</b>	8	3	± 15	65	± 35	2.0	4.0	90	±50 ~ ±300	2.2K // 0.5	4 pcs	S2

Thru - Hole Type [ standard frequency 45.000 MHz( Fundamental mode ) ] ; available frequency range ( 45.000 ~ 45.100 MHz )

Channel Spacing ( kHz )	Model	No. of poles	Pass Bandwidth		Stop Bandwidth		Ripple	Insertion Loss	Guaranteed Attenuation		Terminating Impedance	Package	
			dB	kHz (min.)	dB	kHz (max.)	dB (max.)	dB (max.)	dB	kHz	ohms // pF	Type	
12.5	<b>45M7.5A</b>	2	3	± 3.75	10	± 12.5	1.0	2.0	65	±300 ~ ±1000	200 // 4.0	U5	U1
	<b>45M7.5B</b>	4	3	± 3.75	30	± 12.5	1.0	4.0	80	±300 ~ ±1000	350 // 6.5	U5 a pair	U1 a pair
25.0	<b>45M15A</b>	2	3	± 7.5	15	± 25	1.0	2.0	35	±300 ~ ±1000	650 // 3.0	U5	U1
	<b>45M15B</b>	4	3	± 7.5	30	± 25	1.0	3.0	80	±300 ~ ±1000	650 // 3.0	U5 a pair	U1 a pair
50.0	<b>45M30A</b>	2	3	± 15	15	± 60	1.5	2.5	35	±300 ~ ±1000	1.2K // 0	U5	U1
	<b>45M30B</b>	4	3	± 15	30	± 50	1.0	3.0	80	±300 ~ ±1000	1.2K // 0.7	U5 a pair	U1 a pair

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## Part Number Format and Example

SMD Type Part Number Format			
[1]	[2]	[3]	[4]
Frequency Code	MQ	Width Code	Poles Code

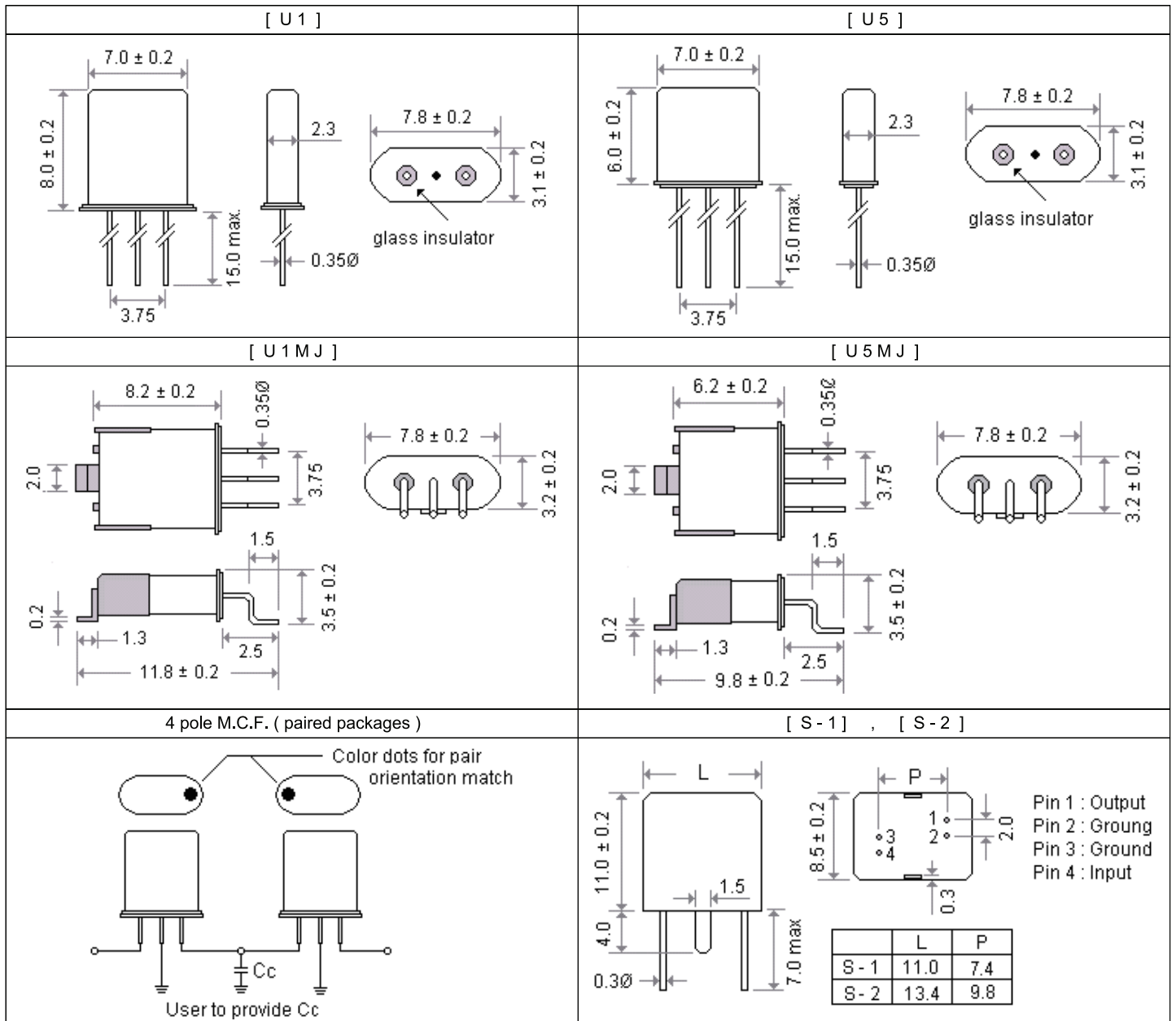
Examples	45	MQ	30	A
----------	----	----	----	---

Dip Type Part Number Format					
[1]	[2]	[3]	[4]	[5]	[6]
Frequency Code	M	Width Code	Poles Code	Holder Type	G

Examples	21.7	M	7.5	D	U5SM	G
----------	------	---	-----	---	------	---

[1]	Freq. code : " 10 " for 10.700MHz , " 21 " for 21.400MHz , " 21.7 " for 21.700MHz , " 45 " for 45.000MHz , Freq. code : If none standard freq. please show frequency with one decimal point .
[2]	" M " Dip Type series , " MQ " SMD Type ( 7.0 * 5.0 * 1.3 mm )
[3]	Pass band width ( 3dB ) ( min. ) " 7.5 " for $\pm 3.75$ kHz , " 15 " for $\pm 7.5$ kHz , " 20 " for $\pm 10$ kHz , " 30 " for $\pm 15$ kHz ,
[4]	No. of poles " A " for 2 poles , " B " for 4 poles , " C " for 6 poles , " D " for 8 poles
[5]	Dip type holder type
[6]	Please add " G " after the " type code " for RoHS compliant ( Does not apply to MQ series ) .
[7]	Standard operating temperature range is -20°C to 70°C , If non-standard please enter the desired temp. range after " / " , for example " /-30+70 " : -30°C to 70°C

## Package Dimensions ( unit : mm )



# Mercury Green Program

## Common points for all crystal products

### Mercury Green Program

Mercury's Green Program is implemented in accordance with the European Union's directive on "Restriction of the use of certain Hazardous Substance(RoHS)". Mercury's Lead-Free and RoHS Compliant products follow the EU directive (2002/95/EC) and include test reports issued by SGS Group on hazardous substances levels for the six substances: lead(pb), cadmium(cd), mercury (Hg), hexavalent chromium(Cr+6), polybrominated biphenyl(PBB), and polybrominated diphenyl ether (PBDE).

- Crystal Green Program-Crystals
- Crystal Oscillator Green Program-XO、VCXO、VCTCXO、TCXO、OCXO
- Crystal Filter Green Program-Filters



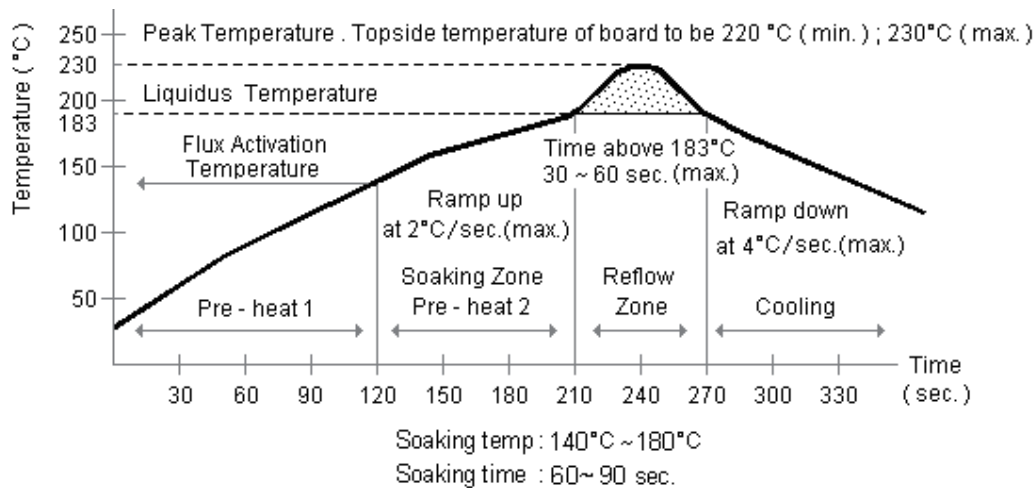
**RoHS Compliant Product  
by Mercury**

### Soldering conditions

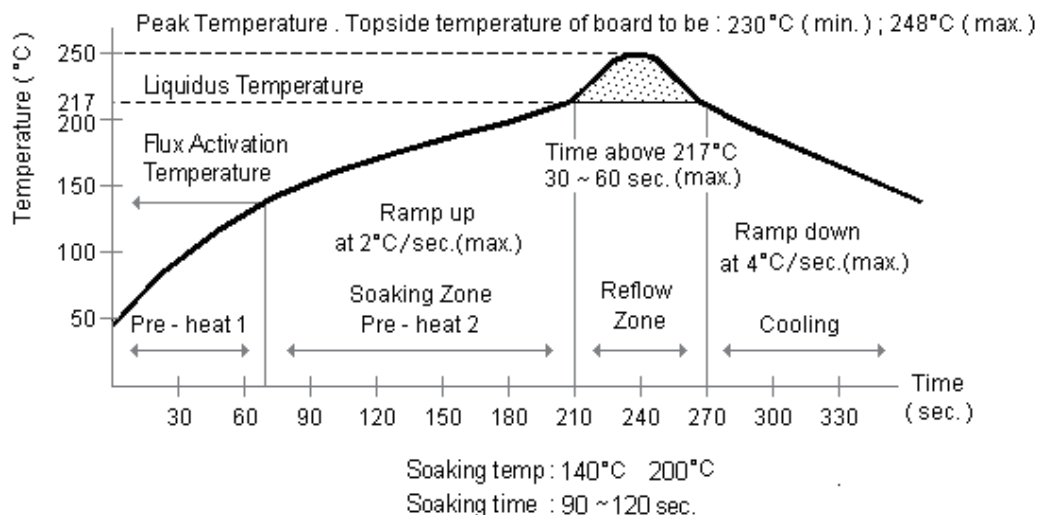
- (1) Lead wires should be soldered within 3 seconds with the iron heated to a temperature of 380°C ( max. ).
- (2) In solder-dip mounting , it should be within 10 seconds with a temperature of 260°C ( max. ).  
Heating the whole crystal unit in the dip mounting process should be avoided .  
Upright mounting is recommended ( to prevent applying heat directly to the body of a crystal unit ).
- (3) Heating the whole body of the crystal unit , for example , in a reflow oven may affect the performance.  
The holder is small and is sealed by solder material by press sealing , so that such a reflow process is not allowed to be applied .

### Suggested Reflow Profile [ SMD type products ]

(1) Low temperature solder reflow : For Sn62 , Pb36 , Ag2 , Sn63 , Pb37 alloy .



(2) High temperature solder reflow : For Sn96.5% , Ag3.5% , Cu0.5% alloy .



**Mercury** [www.mercury-crystal.com](http://www.mercury-crystal.com)

## Mercury Statement of REACH Initiative

Mercury supports the aim of REACH in improving the protection of human health and the environment and is active in limiting our use of chemicals.

**REACH** : Registration, Evaluation, Authorization and Restriction of Chemical Substances in accordance with Regulation (EC) No. 1907/2006.

We have reviewed our products and do not have any parts that we manufacture or purchase with a chemical content that requires REACH registration. Mercury is a producer of articles and its products do not contain any of the 16 Substances of Very High Concern (SVHCs) identified on a REACH regulation, ECHA proposed list (reference list on next page) in their composition or make-up. Additionally, Mercury, as a non-EU based entity, does not exceed 1 metric ton of exports of products to EU.

Mercury will continue to monitor the REACH requirement for any changes in the materials involving registration, and in particular, the SVHC list and the 38 substances currently on the list (updated on June 18, 2010 by ECHA, European Chemicals Agency). Should any changes be made to Mercury products that require REACH registration, or the list of items requiring registration changes, Mercury will notify you accordingly.

Yours truly,



Jason Yen, VP of Quality Assurance

P.S.:

The 38 substances currently on the list (updated on June 18, 2010 by ECHA, European Chemicals Agency) :

[http://echa.europa.eu/chem\\_data/authorisation\\_process/candidate\\_list\\_table\\_en.asp](http://echa.europa.eu/chem_data/authorisation_process/candidate_list_table_en.asp)

Mercury [www.mercury-crystal.com](http://www.mercury-crystal.com)

■ Taiwan : Tel (886)-2-2406-2779 / sales-tw@mercury-crystal.com ■ U.S.A: Tel: (1)-909-466-0427 / sales-us@mercury-crystal.com ■ China: Tel: (86)-512-5763-8100 / sales-cn@mecxtal.com

# Part Number Formats and Product Marking Rules

## Quartz Crystals

### Holder Type

SMD type :	X22	X32	X42	MJ	MF	MQ	M49	ML49	MP5	MP4
Dip type :	H49	49T	H50	H48	HUS	HUSL	U1	U5	T38	T26
Jecket type :	H49MJ	49TMJ	U1MJ	U5MJ	T38MJ	T26MJ				
Gull wing :	H49SM	49TSM	U1SM	U5SM	T38SM	T26SM				

### Part Number Format

[ 1 ]	[ 2 ]	[ 3 ]	[ 4 ]	[ 5 ]	[ 6 ]	[ 7 ]	[ 8 ]
Holder Type	G	Center Freq.	CL	Freq. Tolerance	Freq. Stability	Operating Temp. Range Code	Special Operating Temp. Range

Example	(1)	H49	G	-	40.000A3	-	12					
	(2)	MJ		-	12.000	-	20	-	10	/	10	Y
	(3)	M49	G	-	24.000	-	18	-	20	/	30	/

Ex (1) : H49G - 40.000A3 - 12 [ 49/U type, RoHS, 40.000MHz, AT-cut 3rd overtone, 12pF, ±30ppm ( 25°C ), ±30ppm ( -10°C to 60°C ) ]

Ex (2) : MJ - 12.000 - 20 - 10 / 10 Y [ MJ type, 12.000MHz, 20pF, ±10ppm ( 25°C ), ±10ppm ( -20°C to 70°C ) ]

Ex (3) : M49G - 24.000 - 18 - 20 / 30 / -30+75 [ M49 type, RoHS, 24.000MHz, 18pF, ±20ppm ( 25°C ), ±30ppm ( -30°C to 75°C ) ]

[ 1 ]	Holder Type									
[ 2 ]	Please add " G " after the " type code " for RoHS compliant ( Does not apply to X22 , X32 , X42 , MJ , MF , MQ series )									
[ 3 ]	Center frequency . Please add " A3 , A5 or B " after the " Freq. in MHz " for the quartz cut other options . Blank : AT-cut fund. mode ; A3 : AT-cut 3rd overtone ; A5 : AT-cut 5th overtone ; B : BT-cut fund. mode									
[ 4 ]	Load Capacitance ( CL ) : series ( spec. code is " S " ) or Parallel ( If parallel , please specify CL value , typical CL ranges from 8 to 32 pF ) Available Options " V " = Vinyl sleeve around holder , " K " = 3rd lead at bottom center , " R " = On reel " G " = 3rd lead at top center , " I " = Teflon insulator at bottom									
[ 5 ]	Calibration tolerance value : freq. tolerance value ( at 25°C ) , industrial temp. range									
[ 6 ]	Frequency Stability , industrial temp. range									
[ 7 ]	Temp. Range	W	0°C ~ +50°C	X	-10°C ~ +60°C	Y	-20°C ~ +70°C	Y1	-30°C ~ +70°C	
	Options	Y2	-10°C ~ +80°C	Y3	-30°C ~ +80°C	I	-40°C ~ +85°C	M	-55°C ~ +105°C	
[ 8 ]	If non-standard please enter the desired temp. range after " / " , for example " / -30+70 " : -30°C to 70°C									

### Production Marking Rules

General X'tal package type marking rules	MQ , MF , MJ , X42 marking rules	X22 , X32 marking rules
<p>( X22 , X32 , X42 , MJ , MF , MQ series are not included.) Suffix " G " for RoHS compliant .</p> <p>Frequency ← XX,XXX G MEC XXXXXX Lot code ( Cutting method ) : A : AT-cut ( fundamental ) B : BT-cut ( fundamental ) 3 : AT-cut ( 3rd overtone ) 5 : AT-cut ( 5th overtone )</p> <p>ex: 2010 --- 0 2011 --- 1</p>	<p>MQ , MF , MJ , X42 marking rules</p> <p>XX,XXX → Freq. MEC XXXXXX → lot code ; Mercury Logo ( Month ) --- Table 2 ( Year ) --- 2010 --- 0 Load capacitance ( CL ) : Table 1 ( Cutting method ) : A : AT-cut , fundamental B : BT-cut , fundamental 3 : AT-cut , 3rd overtone 5 : AT-cut , 5rd overtone</p>	<p>X22 , X32 marking rules</p> <p>XX.XX → Freq. M XXX → Load capacitance ( CL ) : Table 1 Mercury Logo lot code : ( Month ) --- Table 2 ( Year ) 2010 --- 0 2011 --- 1</p>

Table 1	CL	< 10	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	>34	Series
	Code	A	B	C	D	E	F	G	H	I	J	K	L	M	N	O	P	Q	R	S	T	U	V	W	X	Y	Z	a	b

Table 2	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
	Code	A	B	C	D	E	F	G	H	I	J	K	L

# Part Number Formats and Product Marking Rules

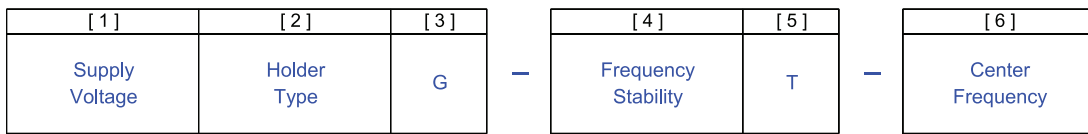
## Crystal Oscillators

### Holder Type

Product and Series	Output Wave Output Logic	Supply Voltage	SMD types	Thru-Hole types	Gull Wing types
SWO	Square Wave CMOS	1.8 / 2.5 / 3.3 / 5.0			
Hxx		1.8 / 2.5 / 3.3 / 5.0	xx = 42, 44, 53, 32		
HFxx		2.5 / 3.3	xx = 42, 44, 62, 64, 5761, 5762	xx = 8, 14	xx = 18, 24
HWxxx		3.3			
HVxxx		3.3			
HBxx		1.8 / 2.5 / 3.3 / 5.0	xx = 42, 44, 57, 53, 32		
HKxx		1.8 / 2.5 / 3.3 / 5.0	xx = 42, 44, 57, 53		
LPOxxx	3.3 / 5.0		xx = 8, 14	xx = 18, 24	
HPFxxx	Square Wave LVPECL	2.5 / 3.3	xx = 621, 622, 5761, 5762	xx = 8, 14	xx = 8, 14
HPWxxx		3.3			
HDFxxx	Square Wave LVDS	2.5 / 3.3	xx = 621, 622, 5761, 5762	xx = 8, 14	xx = 8, 14
HDWxxx		3.3			
HSRxxx	True Sine Wave	2.8 / 3.0 / 5.0	xx = 42, 44, 62, 64, 53, 57		
HSxxx		3.3 / 5.0			

xx = package code

### Part Number Format



Examples	(1)	(2)	(3)	-	(4)	(5)	-	(6)
	18	SWO		-	B	T	-	25.000
	5	H14	G	-	C30		-	10.000
	3	HDW5761		-	E		-	156.250

Ex (1): 18SWO - BT - 25.000 [ 1.8V, SWO type, ±50ppm from -10°C to 70°C, Tri-state, 25.000MHz, ]

Ex (2): 5H14G - C30 - 10.000 [ 5.0V, H14 type, RoHS, ±30ppm from -10°C to 70°C, 10.000MHz ]

Ex (3): 3HDW621G - E - 156.250 [ 3.3V, ( HDW62 type, Tri-state on pin 1 ), RoHS, ±50ppm from -40°C to 85°C, 156.250MHz ]

[1]	Supply voltage, "1" for +1.0V ; "12" for +1.2V ; "18" for +1.8V ; "25" for +2.5V ; "28" for +2.8V ; "3" for +3.3V ; "5" for +5.0V
[2]	Holder Type
[3]	Please add "G" after the "type code" for RoHS compliant ( Does not apply to SWO, H_53, H_32, H22, H_576_, H_534 series )
[4]	-10°C ~ 70°C "A" ± 25ppm ; "B" ± 50ppm ; "C" ± 100ppm ; If non-standard please enter the desired stability after "C", for example "C15": represents ±15ppm over -10 to +70°C
	-40°C ~ 85°C "D" ± 25ppm ; "E" ± 50ppm ; "F" ± 100ppm ; If non-standard please enter the desired stability after "I", for example "I20": represents ±20ppm over -40 to +85°C
[5]	"T" for Tri-state, Leave this space blank if no connection on pin 1 or pad 1.
[6]	Frequency in MHz
	Assigned by Mercury if customer spec, Ex (1): S — duty cycle ± 5%, Ex: "-S" (2): 50p — output load 50pF, Ex: "- 50p"

### Production Marking Rules

General Osc. Package types marking rules	SWO, H53 marking rules	H32 marking rules	H22 marking rules
<p>Suffix "G" for RoHS compliant</p> <p>Hole type ←</p> <p>Stability: Table 1</p> <p>Input Voltage ← XH14G-XX</p> <p>"18" for +1.8V</p> <p>"25" for +2.5V</p> <p>"28" for +2.8V</p> <p>"33" for +3.3V</p> <p>"5" for +5.0V</p> <p>Pin # 1 indicator</p> <p>(Year): 2010 --- 0</p> <p>2011 --- 1</p> <p>"T": Tri-state</p> <p>Frequency</p> <p>lot code</p>	<p>XX,XXX → Freq.</p> <p>MECXXXXX → lot code</p> <p>Pin # 1 indicator</p> <p>Stability -- Table 1</p> <p>(Month) --- Table 2</p> <p>(Year) --- 2010 --- 0</p> <p>Input Voltage</p>	<p>Freq. →</p> <p>Stability -- Table 1</p> <p>Pin # 1</p> <p>(Month) --- Table 2</p> <p>(Year) --- 2010 --- 0</p> <p>Input Voltage --- Table 3</p>	<p>Stability</p> <p>Freq. ← - Table 1</p> <p>Pin # 1</p> <p>Input Voltage (Year) - Table 3 - 2010 - 0</p>

Table 1	-10°C ~ 70°C	"A" ± 25ppm ; "B" ± 50ppm ; "C" ± 100ppm ; If non-standard please enter the desired stability after "C", for example "C10": ± 10ppm
	-40°C ~ 85°C	"D" ± 25ppm ; "E" ± 50ppm ; "F" ± 100ppm ; If non-standard please enter the desired stability after "I", for example "I10": ± 10ppm

Table 2	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
	Code	A	B	C	D	E	F	G	H	I	J	K	L

Table 3	Input Voltage	( Enable / Disable )	5.0 V	3.3 V	2.8 V	2.5 V	1.8 V	1.2 V	1.0 V
			B	D	F	H	J	L	N

# Part Number Formats and Product Marking Rules

## Spread Spectrum Low EMI Clock Oscillators

### Holder Type

Type	Thru-Hole types	Gull Wing types	SMD types	Wave form
HM x x	x x = 8 , 14	x x = 18 , 24	x x = 44 , 53 , 57	square wave

x x = package code

### Part Number Format

[ 1 ]	[ 2 ]	[ 3 ]	[ 4 ]	[ 5 ]	[ 6 ]	[ 7 ]	[ 8 ]
Supply Voltage	Holder Type	G	Frequency Stability	T	Center Frequency	Group Type	Spread type Percentage

Examples	(1)	(2)	(3)	(4)	(5)	(6)	(7)	(8)
(1)	3	HM 576	-	B	T	10.000	R	C1.5
(2)	3	HM 53	-	F	T	75.000	Y	D1.0
(3)	3	HM 14	G	C30	-	100.000	P	D3.0

Ex (1) : 3HM57 - BT - 10.000R - C1.5 [ 3.3V , HM57 type , ±50ppm from -10°C to 70°C , Tri-state , 10.000MHz , R group , Center Spread 1.5% ]

Ex (2) : 3HM53 - FT - 75.000Y - D1.0 [ 3.3V , HM57 type , ±100ppm from -40°C to 85°C , Tri-state , 75.000MHz , Y group , Down Spread 1.0% ]

Ex (3) : 3HM14G - C30 - 100.000P - D3.0 [ 3.3V , HM14 type , RoHS , ±30ppm from -10°C to 70°C , 100.000MHz , P group , Down Spread 3.0% ]

[ 1 ]	Supply voltage , " 3 " for +3.3V
[ 2 ]	Holder Type
[ 3 ]	Please add " G " after the " type code " for RoHS compliant ( Does not apply to HM53 , HM57 series ).
[ 4 ]	-10°C ~ 70 °C " A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C " , for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85 °C " D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I " , for example " I20 " : represents ±20ppm over -40 to +85°C
[ 5 ]	" T " for Tri-state , Leave this space blank if no connection on pin 1 or pad 1 .
[ 6 ]	Frequency in MHz
[ 7 ]	Group " R " , " P " or " Y "
[ 8 ]	Spread type & percentage ; " C " for center spread , " D " for down spread

### Production Marking Rules

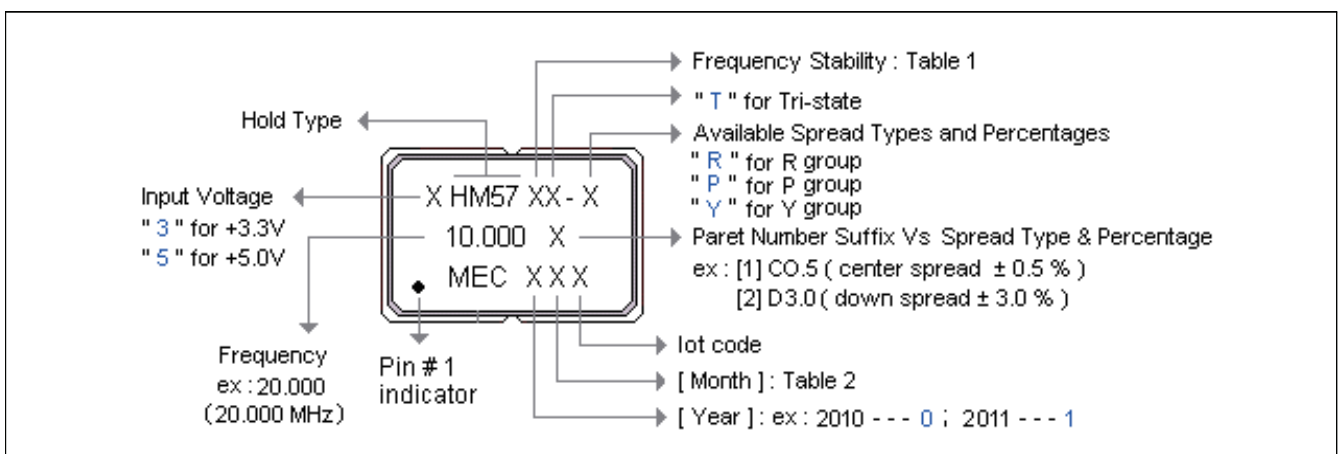


Table 1	-10°C ~ 70 °C	" A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C " , Ex : " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85 °C	" D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I " , Ex : " I20 " : represents ±20ppm over -40 to +85°C

Table 2	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
	Code	A	B	C	D	E	F	G	H	I	J	K	L

# Part Number Formats and Product Marking Rules

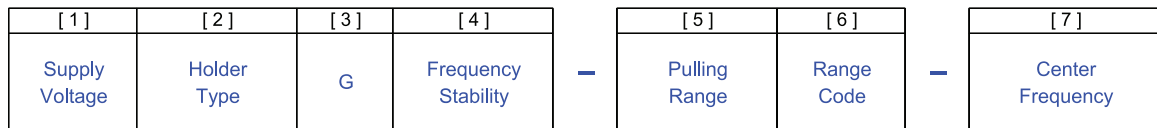
## [ VCXO ] Voltage Controlled Crystal Oscillators

### Holder Type

Type	Output Wave	Supply Voltage	Note	SMD types	Thru-Hole	Gull Wing		
G xx	CMOS	1.8 / 2.5 / 3.3 / 5.0		xx = 42, 62, 534, 536, 576	xx = 8, 14	xx = 18, 24		
GF xx			Phase Jitter : 0.4 ( typ.)	xx = 42, 62, 536, 576				
GW xx			Phase Jitter : 2.6 ( typ.)	xx = 42, 62, 536, 576				
GV xx			Phase Jitter : 2.3 ( typ.)	xx = 42, 62, 536, 576				
GPF xx	LVPECL	3.3	Phase Jitter : 0.4 ( typ.)	xx = 42, 62, 576	xx = 8, 14	xx = 18, 24		
GPW xx			Phase Jitter : 2.6 ( typ.)					
GDF xx	LVDS	3.3 / 5.0	Phase Jitter : 0.4 ( typ.)	xx = 42, 62, 576			xx = 8, 14	xx = 18, 24
GDW xx			Phase Jitter : 2.6 ( typ.)					
GS xx	True Sine	3.3 / 5.0	Load : 50Ω Load	xx = 44	xx = 8, 14	xx = 18, 24		
GSR xx		3.3 / 5.0	Load : 10KΩ // 10pF	xx = 42, 44, 62, 53, 57				

xx = package code

### Part Number Format



Examples	(1)	(2)	(3)	(4)	(5)	(6)	(7)		
(1)	18	G14	G	B	-	80	N	-	35.328
(2)	3	GPF576		C20	-	150	M	-	200.000
(3)	5	GDW62	G	I30	-	100	T	-	800.000

EX (1): 18G14GB - 80N - 35.328 [ 1.8V, G14 type, RoHS, ±50ppm from -10°C to 70°C, Pulling : ±80ppm ( min. ), 35.328MHz, ]

Ex (2): 3GPF576C20 - 150M - 200.000 [ 3.3V, GPF576 type, ±10ppm from -10°C to 70°C, , Pulling : ±150ppm ( max. ), 200.000MHz ]

Ex (3): 3GDW62G130 - 100T - 800.000 [ 3.3V, GDW62 type, RoHS, ±30ppm from -40°C to 85°C, Pulling : ±150ppm ( Typical ), 800.000MHz ]

[ 1 ]	Supply voltage , " 18 " for +1.8V ; " 25 " for +2.5V ; " 3 " for +3.3V ; " 5 " for +5.0V	
[ 2 ]	Holder Type	
[ 3 ]	Please add " G " after the " type code " for RoHS compliant equivalent ( Does not apply to G_534 , G_536 , G_576 ).	
[ 4 ]	-10°C ~ 70°C	" A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C ", for example " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85°C	" D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I ", for example " I20 " : represents ±20ppm over -40 to +85°C
[ 5 ]	Frequency Pulling Range	3.3V From ±30ppm ~ ±150ppm , control Voltage range : 0.3V ~ 3.0 ; control voltage center : ± 1.65 V 5.0V From ±70ppm ~ ±200ppm , control Voltage range : 0.5V ~ 4.5V ; control voltage center : ± 2.5 V
	[ 6 ]	Pulling Range Code " M " stands for maximum ; " N " stands for minimum ; " T " stands for typical ( tolerance is ± 20% )
[ 7 ]	Center Frequency in MHz	

### Production Marking Rules

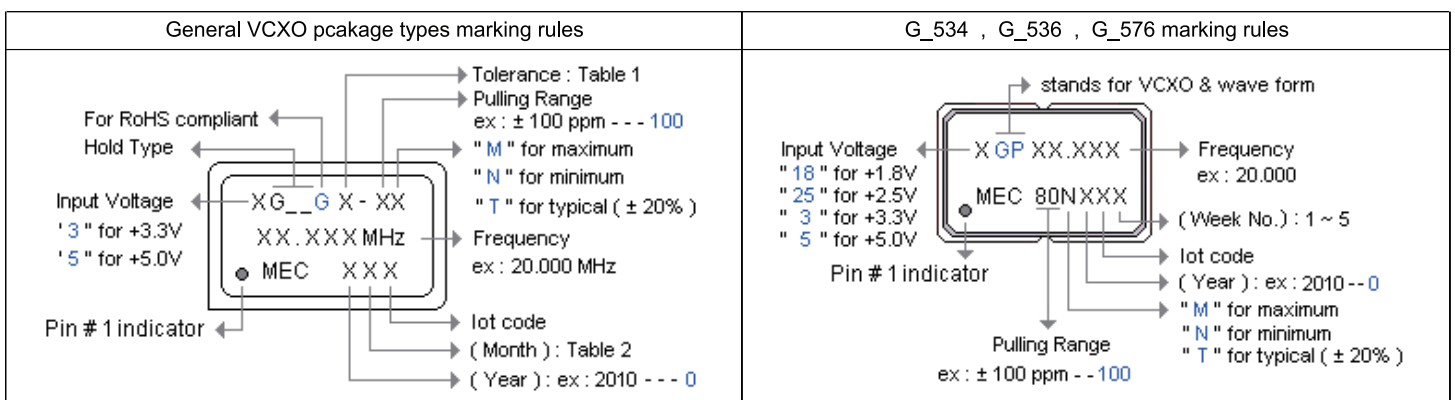


Table 1	-10°C ~ 70°C [ Commercial ]	" A " ± 25ppm ; " B " ± 50ppm ; " C " ± 100ppm ; If non-standard please enter the desired stability after " C ", Ex : " C15 " : represents ±15ppm over -10 to +70°C
	-40°C ~ 85°C [ Industrial ]	" D " ± 25ppm ; " E " ± 50ppm ; " F " ± 100ppm ; If non-standard please enter the desired stability after " I ", Ex : " I20 " : represents ±20ppm over -40 to +85°C

Table 2	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
	Code	A	B	C	D	E	F	G	H	I	J	K	L

# Part Number Formats and Product Marking Rules

## [ TCXO vs VCTCXO ] Temperature Compensated Crystal Oscillators

### Holder Type

TCXO	VCTCXO	Wave Form	SMD types	Thru-Hole types	Gull Wing types
Mxx(G)S	VMxx(G)S	Clipped Sine Wave	XX = 42, 32, 53, 57, 62	XX = 8, 9, 14, 15, 38, 39	XX = 47
Mxx(G)T	VMxx(G)T	CMOS output	XX = 42, 53, 536, 57, 62		
MVxx(G)T	VMVxx(G)T			LVPECL output	XX = 536, 576
MWxx(G)T	VMWxx(G)T				
MWxx(G)P	VMWxx(G)P	LVDS output	XX = 536, 576	---	---
MFxx(G)P	VMFxx(G)P				
MWxx(G)D	VMWxx(G)D	LVDS output	XX = 536, 576	---	---
MFxx(G)D	VMFxx(G)D				

x x = package code

### Part Number Format

	[1]	[2]	[3]	[4]		[5]		[6]		[7]
	Holder Type	G	Output Wave	Supply Voltage	—	Center Frequency	—	Frequency Stability	/	Operating Temp. Range
Examples	(1)	VM38	G	T	5	10.000	—	1.5	/	-20+70
	(2)	M57		S	3	20.000	—	2.5	/	-30+75
	(2)	VMF576		P	33	432.000	—	1.0	/	0+50

Ex (1): VM38GT5 - 10.000 - 1.5 / -20+70 [ VCTCXO, VM38 type, RoHS, CMOS output, 5.0V, 10.000MHz, ±1.5ppm from -20°C to 70°C ]

Ex (2): M57S3 - 20.000 - 2.5 / -30+75 [ TCXO, M57 type, Clipped Sine Wave, 3.0V, 20.000MHz, ±2.5ppm from -30°C to 75°C ]

Ex (3): VMF576P33 - 432.000 - 1.0 / 0+50 [ VCTCXO, VMF576 type, PECL output, 3.3V, 432.000MHz, ±1.0ppm from 0°C to 50°C ]

[1]	Holder Type "M" stands for TCXO, "VM" stands for VCTCXO
[2]	Please add "G" after the "type code" for RoHS compliant (Does not apply to (V)M32, (V)M53, (V)M536_, (V)M57_, (V)M576_)
[3]	"S" stands for Clipped Sine Wave; "T" stands for CMOS output; "D" stands for LVDS differential; "P" stands for PECL differential ex 1: M44T — TCXO, M44 package, CMOS output; ex 2: VM38P — VCTCXO, VM38 package, PECL differential
[4]	Supply voltage, "28" stands for +2.8V; "3" stands for +3.0V; "33" stands for +3.3V; "5" stands for +5.0V
[5]	Center Frequency in MHz
[6]	Frequency stability in ± ppm; ex 1: ± 2.5ppm — 2.5, ex 2: ± 1.0ppm — 1.0
[7]	Operating temperature range in °C ex 1: -10 °C to 60 °C — -10+60; ex 2: -20 °C to 70 °C — -20+70; ex 3: -40 °C to 85 °C — -40+85

### Production Marking Rules

General (VC)TCXO package types marking rules	(V)M53_, (V)M57_, (V)M576_	(V)M32S

Table 2	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
	Code	A	B	C	D	E	F	G	H	I	J	K	L

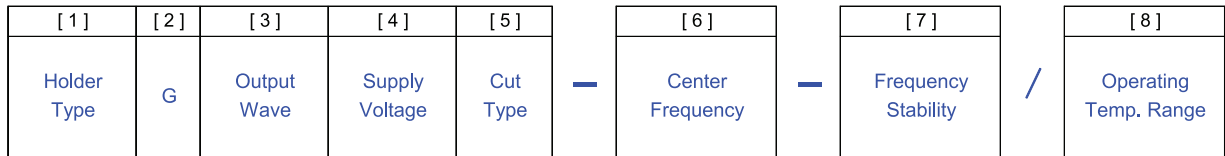
# Part Number Formats and Product Marking Rules

## [OCXO] Oven Controlled Crystal Oscillators

### Holder Type

Type	Thru-Hole types	Gull Wing types	Wave Form
OC x x	x x = 14 , 22 , 30 , 78	x x = 24	Square Wave ; True Sine Wave

### Part Number Format



Examples	(1)	(2)	(2)
	OC14	OC30	OC22
	G	G	G
	T	E	T
	3.3	12	5
	A	S	A
	-	-	-
	10.000	100.000	20.000
	-	-	-
	0.5	0.02	0.2
	/	/	/
	-40+85	-20+70	-30+75

Ex (1): OC14GT33A - 10.000 - 0.5 / -40+85 [ OC14 type , RoHS , CMOS output , 3.3V , 10.000MHz , ± 0.5ppm from -40°C to 85°C ]

Ex (2): OC30GE12S - 100.000 - 0.02 / -20+70 [ OC30 type , RoHS , True Sine wave , 12V , 100.000MHz , ± 0.02ppm from -20°C to 70°C ]

Ex (3): OC22GT5A - 20.000 - 0.2 / -30+75 [ OC22 type , RoHS , CMOS output , 5.0V , 20.000MHz , ± 0.2ppm from -30°C to 75°C ]

[1]	Holder Type "OC__" stands for OCXO ,
[2]	Please add "G" after the " type code " for RoHS compliant
[3]	"T" stands for Square Wave , "E" stands for True Sine Wave ex 1 : OC14T , OC14 package , CMOS output ; ex 2 : OC30E , OC30 package , True Sine wave
[4]	Supply voltage , "3" for 3.3V D.C , "5" for 5.0V D.C , "12" for 12V D.C
[5]	Type of crystal used ; "A" stands for AT-cut crystal , "S" stands for SC-cut crystal
[6]	Center Frequency in MHz
[7]	Frequency stability in ± ppm ; ex 1 : ± 0.5ppm — 0.5 , ex 2 : ± 0.02ppm -- 0.02
[8]	Operating temperature range in °C ex 1 : -10 °C to 60°C — -10+60 ; ex 2 : -20 °C to 70°C -- -20+70 ; ex 3 : -40 °C to 85°C --- -40+85

### Production Marking Rules

#### General OCXO package types marking rules

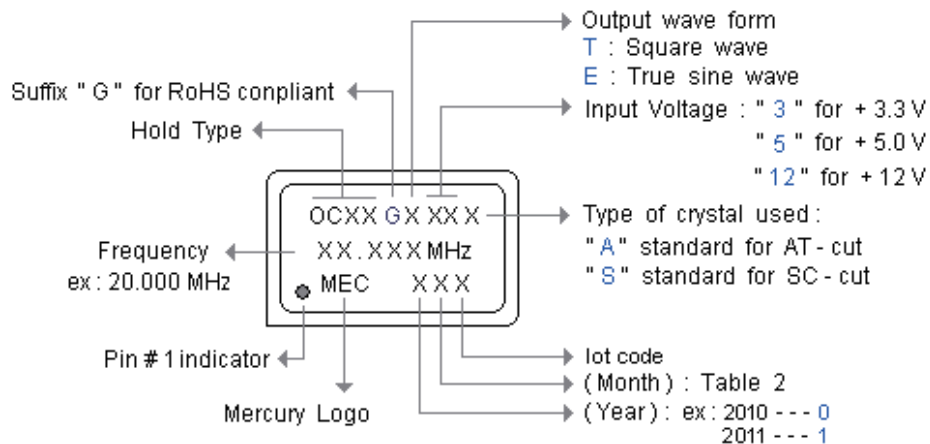


Table 2	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
Code		A	B	C	D	E	F	G	H	I	J	K	L

# Part Number Formats and Product Marking Rules

## [ M. C. F. ] Monolithic Crystal Filters

### Holder Type

SMD Types :	<b>MQ</b>						
Dip Types :	49T	U1	U5	S1	S2	L1	L2
Jacket Types :	49TMJ	U1MJ	U5MJ				
Gull Wing Types :	49TSM	U1SM	U5SM				

### SMD Type ( 7.0 \* 5.0 \* 1.3 mm ) Part Number Format

[ 1 ]	[ 2 ]	[ 3 ]	[ 4 ]
Frequency Code	<b>MQ</b>	Width Code	Poles Code

Examples	45	MQ	30	A
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Ex : 45MQ30A [ 45.000MHz , SMD type MQ series ( 7.0 \* 5.0 \* 1.3 mm ) , Passband : ±15KHz , 2poles ]

### Dip Type Part Number Format

[ 1 ]	[ 2 ]	[ 3 ]	[ 4 ]	[ 5 ]	[ 6 ]
Frequency Code	M	Width Code	Poles Code	Holder Type	G

Examples	21.7	M	7.5	D	U5SM	G
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Ex : 21.7M7.5DU5SMG [ 21.700MHz , Passband : ±3.75KHz , 8poles , RoHS compliant , Dip type ( UM - 5 type , Gull Wing ) , RoHS ]

[ 1 ]	Freq. code : " 10 " for 10.700MHz , " 21 " for 21.400MHz , " 21.7 " for 21.700MHz , " 45 " for 45.000MHz , Freq. code : If none standard freq. please show frequency with one decimal point .
[ 2 ]	" M " Dip Type series , " MQ " SMD Type ( 7.0 * 5.0 * 1.3 mm )
[ 3 ]	Pass band width ( 3dB ) ( min. ) " 7.5 " for ± 3.75kHz , " 15 " for ± 7.5kHz , " 20 " for ± 10kHz , " 30 " for ± 15kHz ,
[ 4 ]	No. of poles " A " for 2 poles , " B " for 4 poles , " C " for 6 poles , " D " for 8 poles
[ 5 ]	Dip type holder type
[ 6 ]	Please add " G " after the " type code " for RoHS compliant ( Does not apply to MQ series ) .
[ 7 ]	Standard operating temperature range is -20°C to 70°C , If non-standard please enter the desired temp. range after " / " , for example " / -30+70 " : -30°C to 70°C

### Production Marking Rules

#### General MCF package types marking rules

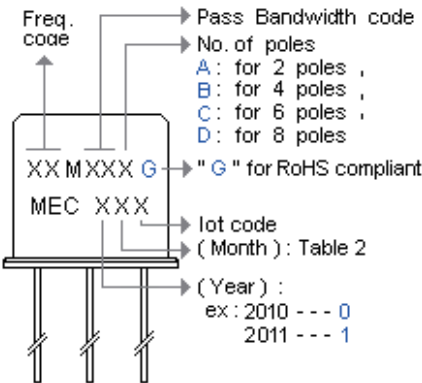
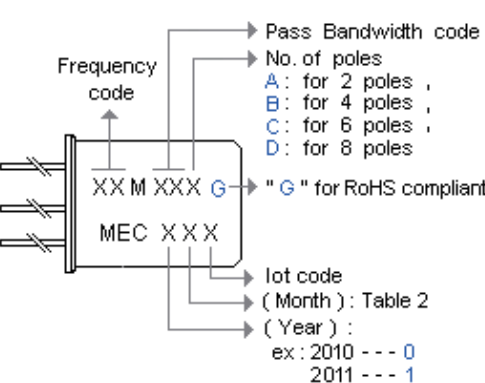
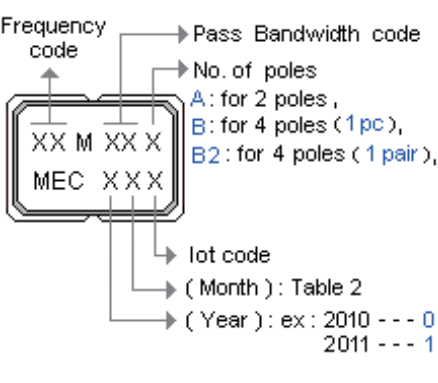
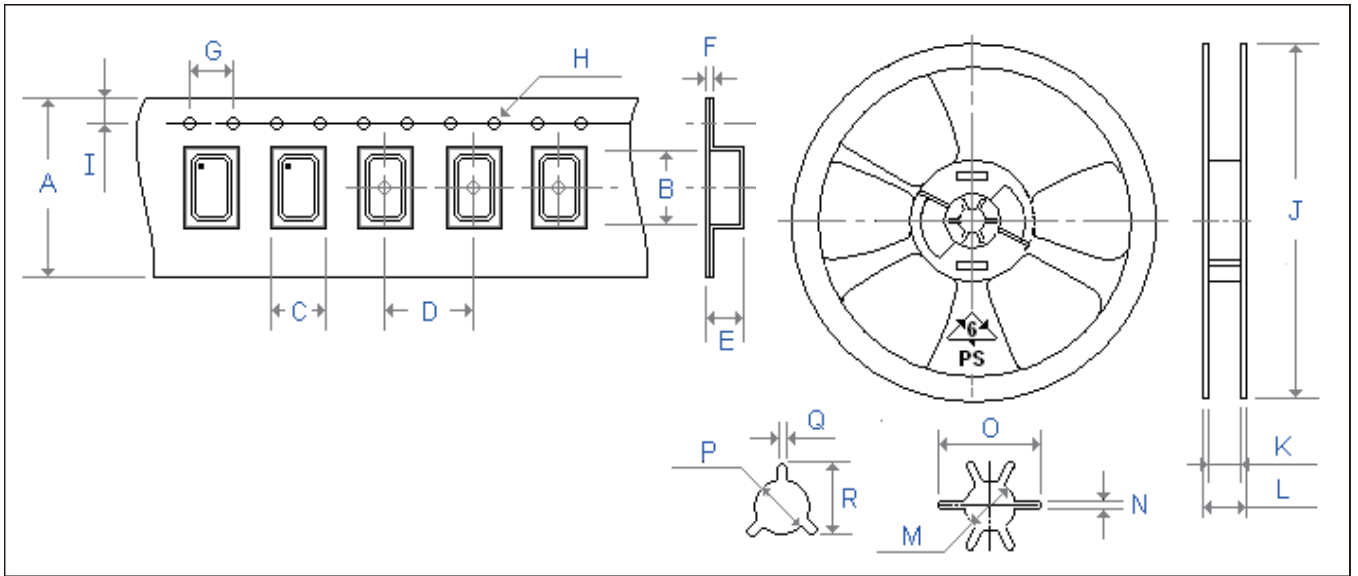
U5 series	U1 , 49T series	MQ series
 <p>                     Freq. code                      Pass Bandwidth code                      No. of poles                      A: for 2 poles ,                      B: for 4 poles ,                      C: for 6 poles ,                      D: for 8 poles                      " G " for RoHS compliant                      lot code                      ( Month ) : Table 2                      ( Year ) :                      ex : 2010 --- 0                      2011 --- 1                 </p>	 <p>                     Frequency code                      Pass Bandwidth code                      No. of poles                      A: for 2 poles ,                      B: for 4 poles ,                      C: for 6 poles ,                      D: for 8 poles                      " G " for RoHS compliant                      lot code                      ( Month ) : Table 2                      ( Year ) :                      ex : 2010 --- 0                      2011 --- 1                 </p>	 <p>                     Frequency code                      Pass Bandwidth code                      No. of poles                      A: for 2 poles ,                      B: for 4 poles ( 1pc ) ,                      B2: for 4 poles ( 1 pair ) ,                      lot code                      ( Month ) : Table 2                      ( Year ) : ex : 2010 --- 0                      2011 --- 1                 </p>

Table 2 :	Month	Jan.	Feb.	Mar.	Apr.	May	Jun.	Jul.	Aug.	Sep.	Oct.	Nov.	Dec.
	Code	A	B	C	D	E	F	G	H	I	J	K	L

# Emboss Taping and Reel Specifications

[ Crystal Units ]

[ M . C . F . Units ]



Carrier Type Dimensions ( unit : mm )

	A	B	C	D	E	F	G	H	I	pcs / reel
X21	8.0	2.3	1.9	4.0	1.2	0.3	4.0	∅1.0	1.7	3000
X22	8.0	2.7	2.3	4.0	1.2	0.3	4.0	∅1.5	1.7	3000
X32	12.0	3.3	2.7	3.7	1.4	0.3	4.0	∅1.5	1.7	3000
X42	12.0	4.3	2.7	7.8	1.3	0.3	4.0	∅1.5	1.7	1000
MJ	16.0	5.4	3.6	7.8	1.6	0.3	4.0	∅1.5	1.8	1000
MF	16.0	6.3	3.8	7.9	2.0	0.4	4.1	∅1.5	1.7	1000
MQ	16.0	8.0	5.5	8.1	2.0	0.3	4.1	∅1.4	1.8	1000
M49	24.0	13.7	5.6	8.0	4.5	0.4	4.1	∅1.4	1.8	1000
ML49	24.0	13.7	5.6	7.8	3.7	0.4	4.1	∅1.4	1.7	1000
MP4	24.0	13.1	5.6	7.9	5.5	0.5	4.0	∅1.45	1.7	1000
MP5	24.0	13.1	5.6	7.9	5.5	0.5	4.0	∅1.45	1.7	1000

Reel Dimensions ( unit : mm )

	J	K	L	M	N	O	P	Q	R	pcs / reel
X21	180.0	11.4	8.4	13.2	2.2	22.0	-	-	-	3000
X22	180.0	11.4	8.4	13.2	2.2	22.0	-	-	-	3000
X32	180.0	18.4	12.4	13.2	2.2	22.0	-	-	-	3000
X42	180.0	18.4	12.4	13.2	2.2	22.0	-	-	-	1000
MJ	180.0	19.6	16.5	-	-	-	13.4	2.5	19.5	1000
MF	180.0	19.6	16.5	-	-	-	13.4	2.5	19.5	1000
MQ	180.0	19.6	16.5	-	-	-	13.4	2.5	19.5	1000
M49	330.0	30.0	25.0	-	-	-	13.4	2.5	19.5	1000
ML49	330.0	30.0	25.0	-	-	-	13.4	2.5	19.5	1000
MP4	330.0	30.0	25.0	-	-	-	13.4	2.5	19.5	1000
MP5	330.0	30.0	25.0	-	-	-	13.4	2.5	19.5	1000

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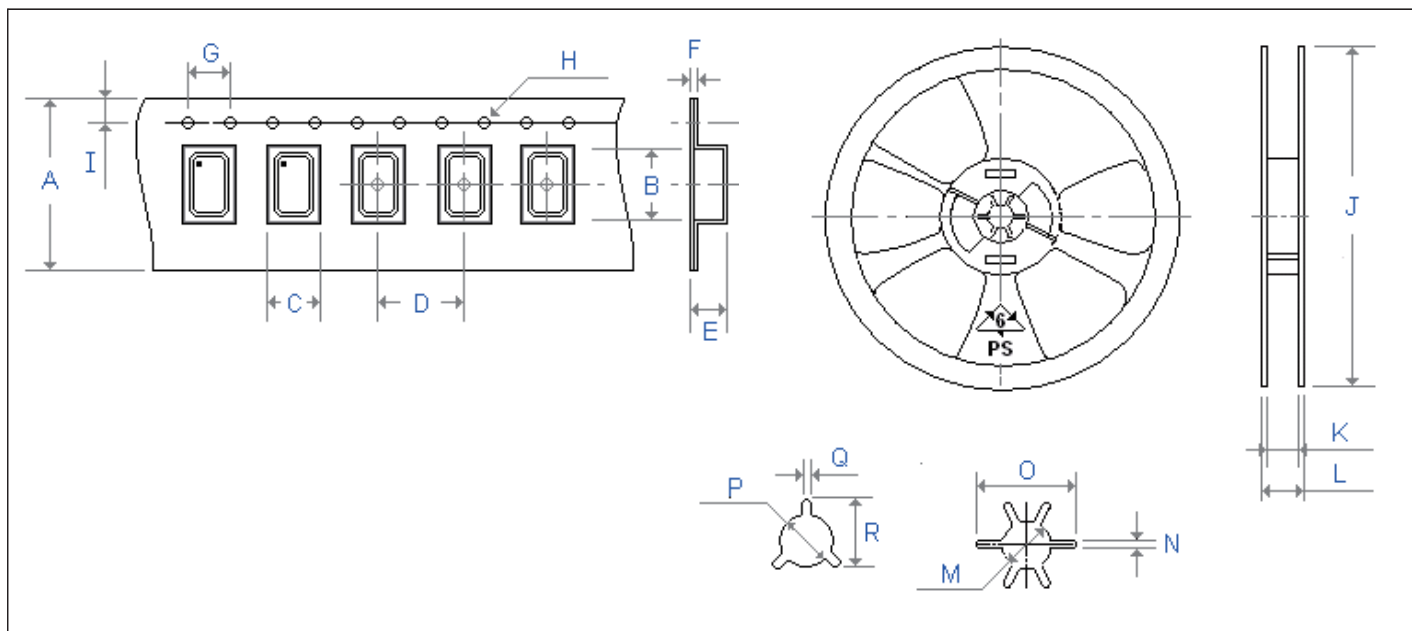
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■U.S.A: Tel: (1)-909-466-0427 / sales-us@mercury-crystal.com

■China: Tel: (86)-512-5763-8100 / sales-cn@mecxtal.com

# Emboss Taping and Reel Specifications

## [ Crystal Oscillator Units ]



Carrier Type Dimensions ( unit : mm )

	A	B	C	D	E	F	G	H	I	pcs / reel
H_22	8.0	2.7	2.3	4.0	1.2	0.3	4.0	Ø1.0	1.8	3000
H_32	12.0	3.8	2.8	4.0	1.4	0.3	4.0	Ø1.5	1.8	3000
H_53	16.0	5.4	3.6	7.8	1.6	0.3	4.0	Ø1.5	1.8	1000
H_57	16.0	8.0	5.5	8.1	2.0	0.3	4.1	Ø1.4	1.8	1000
SWO	16.0	8.0	5.5	8.1	2.0	0.3	4.1	Ø1.4	1.8	1000
H_576	16.0	8.0	5.5	8.1	2.0	0.3	4.1	Ø1.4	1.8	1000
HP_576	16.0	8.0	5.5	8.1	2.0	0.3	4.1	Ø1.4	1.8	1000
HD_576	16.0	8.0	5.5	8.1	2.0	0.3	4.1	Ø1.4	1.8	1000
H_42	24.1	12.4	10.3	15.6	5.1	0.3	3.9	Ø1.45	1.8	500
H_44	24.1	12.4	10.3	15.6	5.1	0.3	3.9	Ø1.45	1.8	500

Reel Dimensions ( unit : mm )

	J	K	L	M	N	O	P	Q	R	pcs / reel
H_32	180.0	9.0	12.0	13.2	2.2	22.0	-	-	-	3000
H_32	180.0	12.8	17.0	13.2	2.2	22.0	-	-	-	3000
H_53	180.0	16.5	19.6	-	-	-	13.4	2.5	19.5	1000
H_57	180.0	16.5	19.6	-	-	-	13.4	2.5	19.5	1000
SWO	180.0	16.5	19.6	-	-	-	13.4	2.5	19.5	1000
H_576	180.0	16.5	19.6	-	-	-	13.4	2.5	19.5	1000
HP_576	180.0	16.5	19.6	-	-	-	13.4	2.5	19.5	1000
HD_576	180.0	16.5	19.6	-	-	-	13.4	2.5	19.5	1000
H_42	330.0	30.0	25.0	-	-	-	13.4	2.5	19.5	500
H_44	330.0	30.0	25.0	-	-	-	13.4	2.5	19.5	500

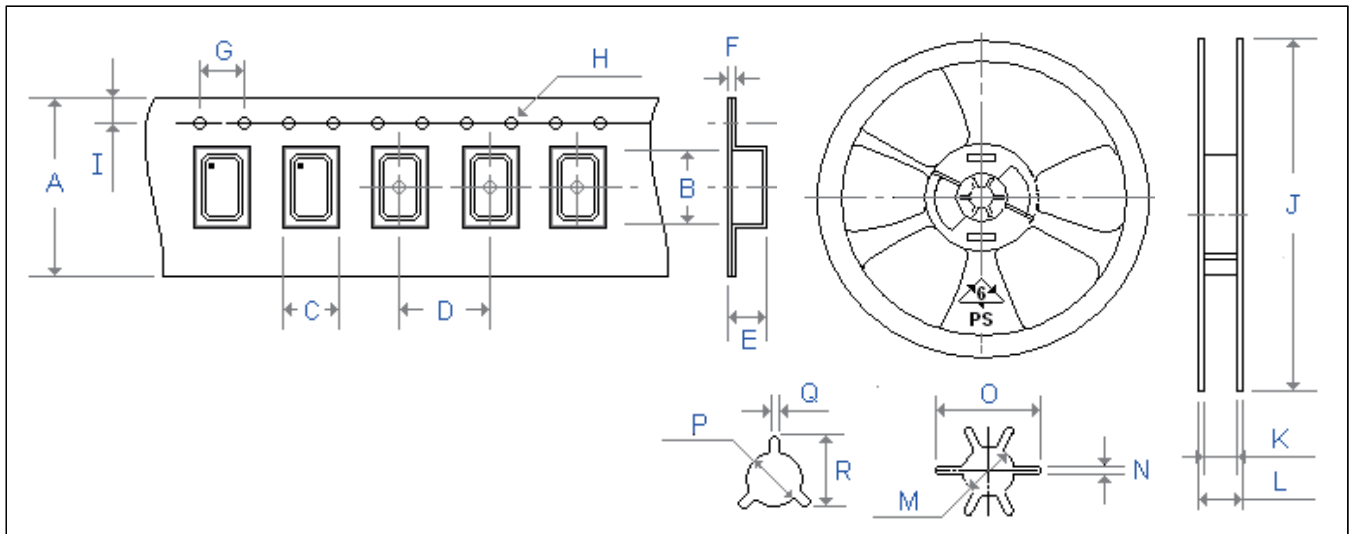
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# Emboss Taping and Reel Specifications

[ VCXO ]

[ ( VC )TCXO ]



Carrier Type Dimensions ( unit : mm )

	A	B	C	D	E	F	G	H	I	pcs / reel
G_534	16.0	5.4	3.6	7.8	1.6	0.3	4.0	1.5	1.8	1000
G_576	16.0	7.9	5.5	8.1	2.0	0.3	4.1	1.4	1.8	1000
G_42	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500
G_44	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500
G_62	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500
G_64	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500
(V)M_53	16.0	5.4	3.6	7.8	1.6	0.3	4.0	1.4	1.8	1000
(V)M_57	16.0	8.0	5.5	8.1	2.0	0.3	4.1	1.4	1.8	1000
(V)M_42	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500
(V)M_44	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500
(V)M_62	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500
(V)M_64	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500
(V)M_64	24.0	12.4	10.3	15.5	5.0	0.3	3.9	1.4	1.8	500

Reel Dimensions ( unit : mm )

	J	K	L	P	Q	R	pcs / reel
G_534	179.0	16.5	19.6	13.4	2.5	19.5	1000
G_576	179.0	16.5	19.6	13.4	2.5	19.5	1000
G_42	330.0	30.0	25.0	13.4	2.5	19.5	500
G_44	330.0	30.0	25.0	13.4	2.5	19.5	500
G_62	330.0	30.0	25.0	13.4	2.5	19.5	500
G_64	330.0	30.0	25.0	13.4	2.5	19.5	500
(V)M_53	179.0	16.5	19.6	13.4	2.5	19.5	1000
(V)M_57	179.0	16.5	19.6	13.4	2.5	19.5	1000
(V)M_42	330.0	30.0	25.0	13.4	2.5	19.5	500
(V)M_44	330.0	30.0	25.0	13.4	2.5	19.5	500
(V)M_62	330.0	30.0	25.0	13.4	2.5	19.5	500
(V)M_64	330.0	30.0	25.0	13.4	2.5	19.5	500
(V)M_64	330.0	30.0	25.0	13.4	2.5	19.5	500

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